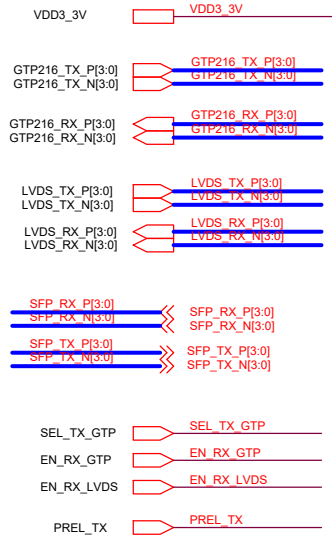




FPGA LVDS_25
tx single-end Swing
247~600mV
type 350mV

rx single-end Swing
100~600mV
type 350mV

FPGA GTP CML
x single-end Swing
253~1074mV
vt = 1.2V

rx single-end Swing
75~1000mV
vt = 0~1.2V

