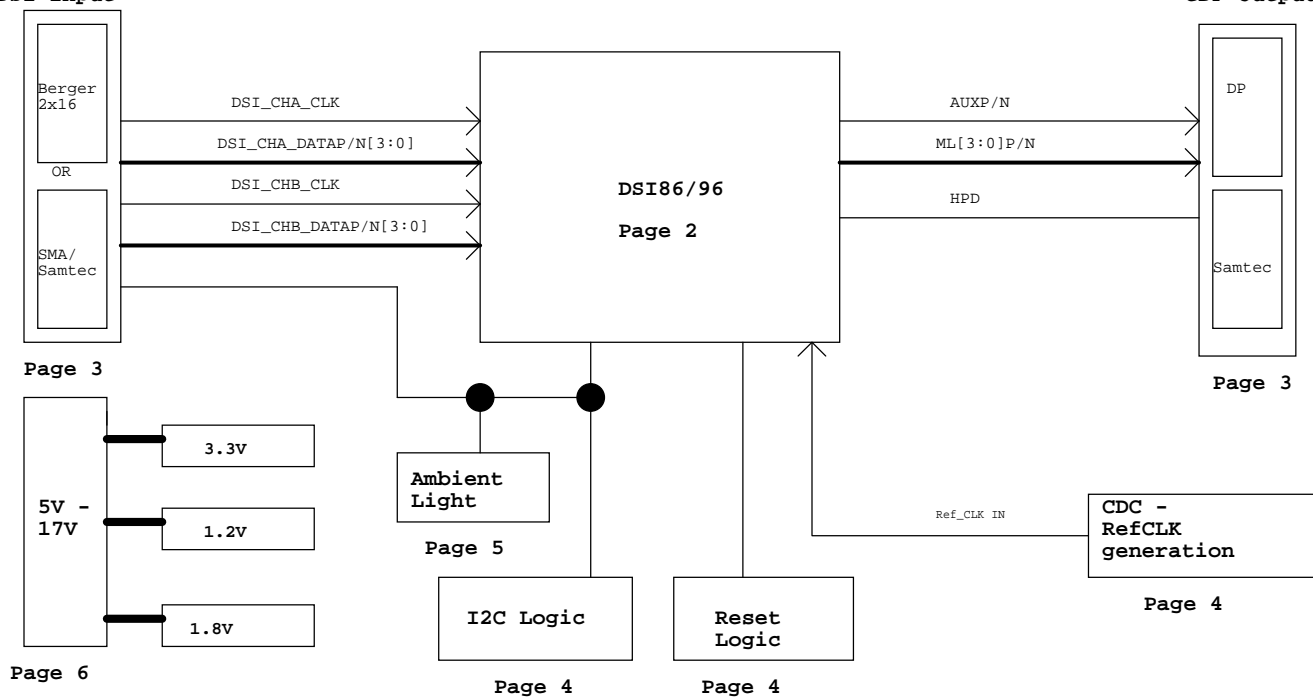


# SN65DSI86/96 EVM

## DSI Input

## eDP Output



### \*\*NOTE LEGEND\*\*

**SilkScreen:** SILK SCREEN

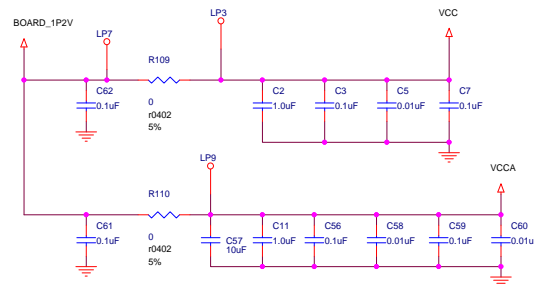
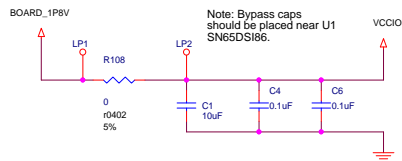
DESIGN NOTES  
NOTE:

### FUNCTIONAL BLOCKS

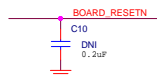
### POWER BLOCKS

DNI: Do Not Install

<b>SN65DSI86/96 EVM BLOCK DIAGRAM</b>	
<b>SIZE</b> B	<b>DWG NO:</b>
<b>SCALE:</b> NONE	Tuesday, March 26, 2013
Sheet 1 of 6	



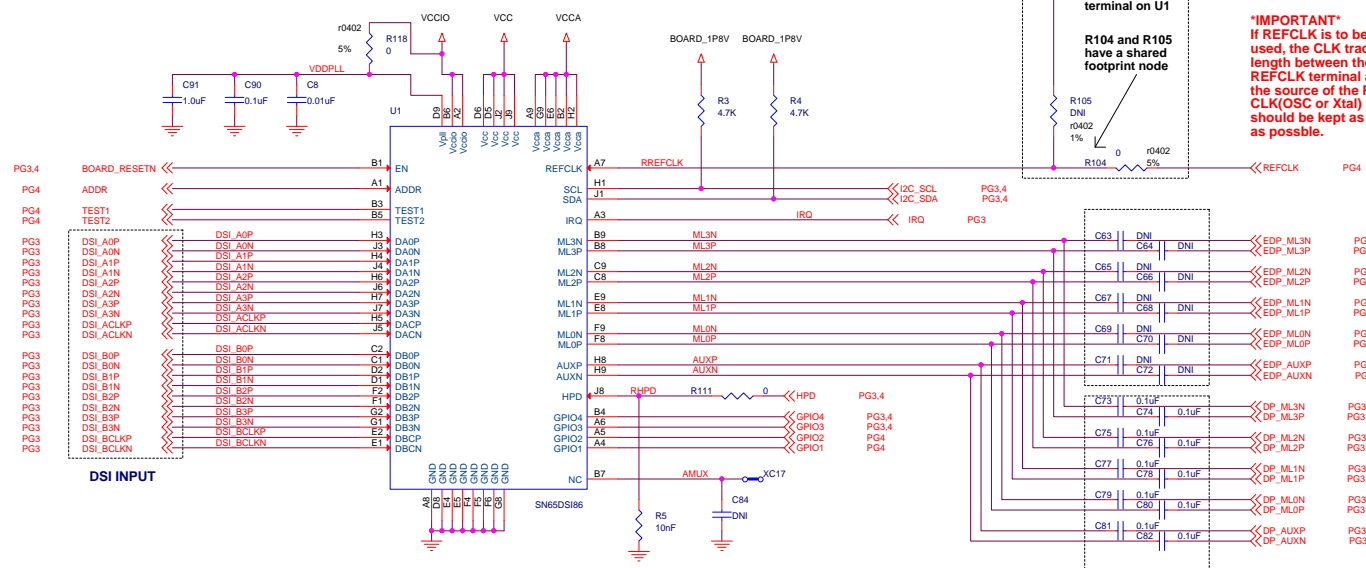
## Reset Implementation



Reset(EN) can be implemented with passive components as shown or active circuitry. In case of using passive components, the values of the RC circuitry need to be adjusted to make sure the low to high transition occurs after the Vcc supply has reached the minimum recommended operating voltage. For this reason, it is recommended to USE ACTIVE CIRCUITRY for better control of the RESET/EN timing.

ADDR = 1, Slave Addr = 0x2D (0101101)  
ADDR = 0, Slave Addr = 0x2C (0101100)

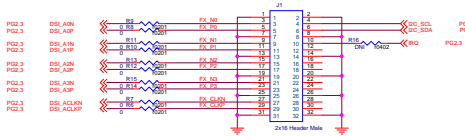
## SN65DSI86/96



ES1 Device:  
- R111 populate with 0-ohm resistor  
- R5 populate with 10nF capacitor.

Production Device:  
- R111 populate with 51K 1% resistor  
- R5 should be a DNI

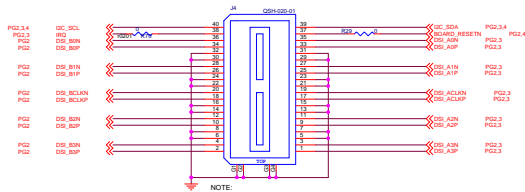
# DSI ChA Display Expansion Connector



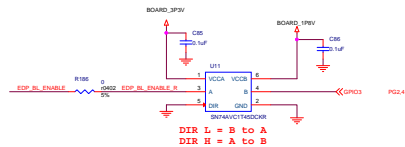
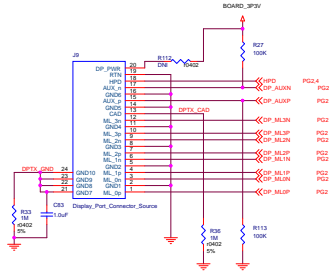
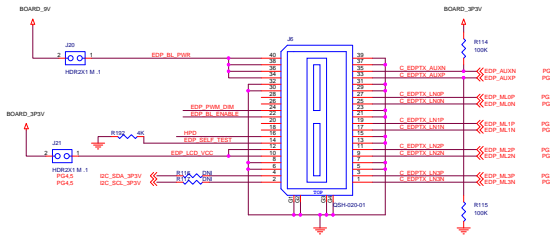
NOTE:  
 - Remove R6-R15 for DSI source connected to J4  
 - Populate R6-R15 when a source connected through J1  
 - R6 - R15 to be placed as near as J4 to avoid drop when J1 is not in use"

## Samtec to SMA Connector for DSIA and DSIB

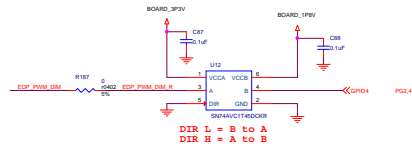
NOTE:  
 Add mounting holes as instructed in the layout notes.



NOTE:  
 The connector is to interface DSI interface with any source with SMA connectors via Samtec to SMA cable

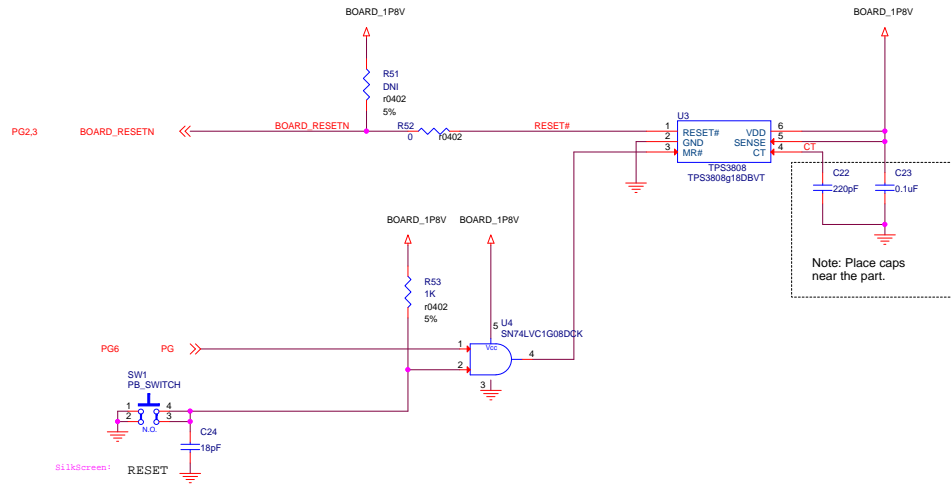


DIR L = B to A  
 DIR H = A to B

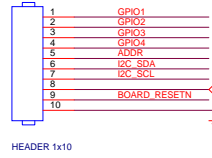


DIR L = B to A  
 DIR H = A to B

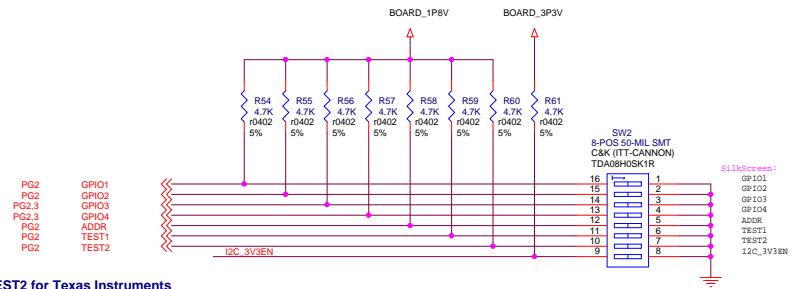
## RESET



J16

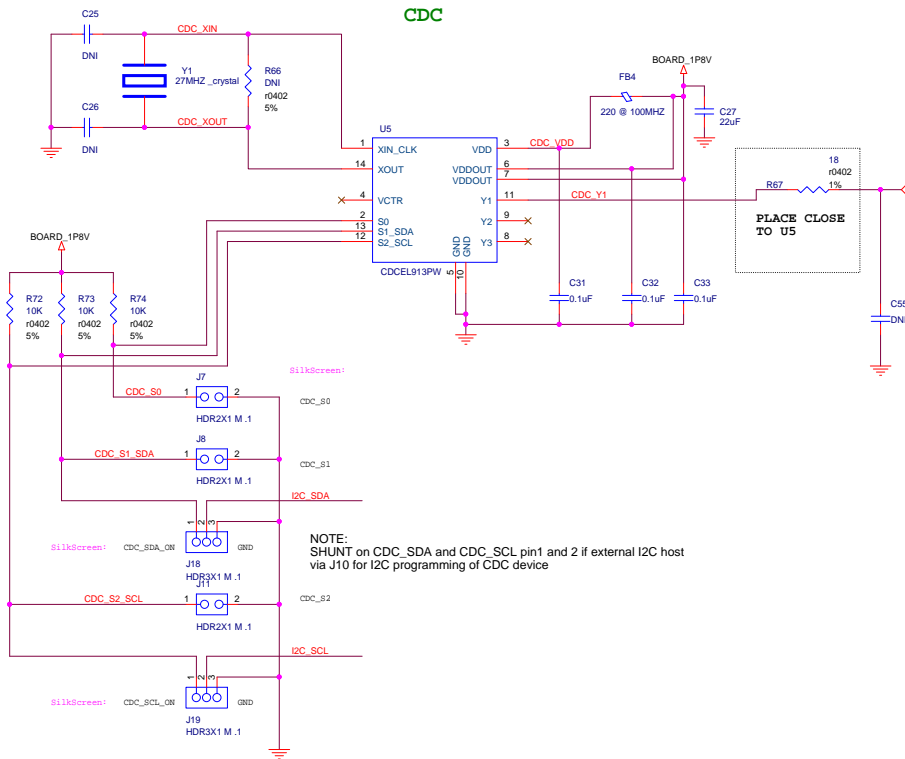


## DIPSW

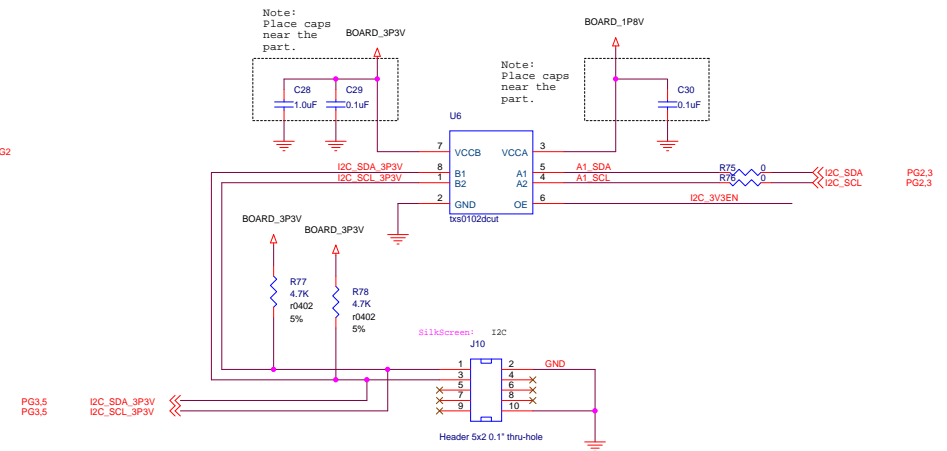


TEST1 and TEST2 for Texas Instruments internal use only. TEST1 shall be left unconnected or tied to GND. TEST2 shall be left unconnected or tied to GND.

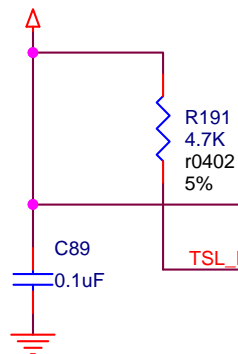
## CDC



## I2C



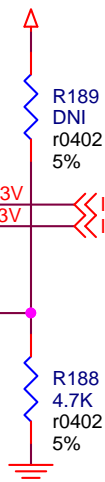
BOARD\_3P3V



U13

VDD SDA  
INT SCL  
GND ASEL  
TSL2561T

BOARD\_3P3V



R189  
DNI  
r0402  
5%

R188  
4.7K  
r0402  
5%

I2C\_SDA\_3P3V  
I2C\_SCL\_3P3V

PG3,4  
PG3,4

**I2C SLAVE ADDRESS:**

0x29 or 0101001 (ASEL = 0)

0x39 or 0111001 (ASEL = float)

0x49 or 1000101 (ASEL = 1)



TEXAS INSTRUMENTS

AMBIENT LIGHT SENSOR

SIZE

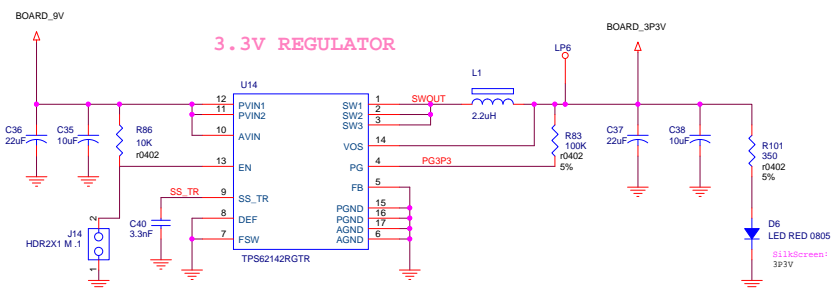
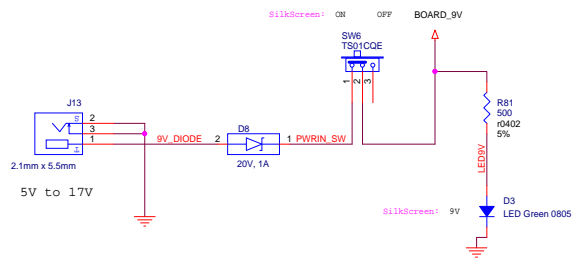
A

DWG NO:

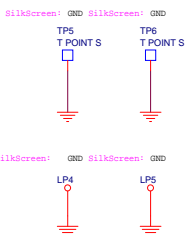
SCALE: NONE

Thursday, November 01, 2012

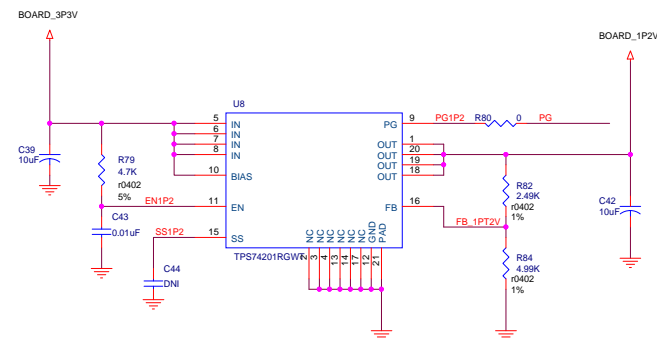
Sheet 5 of 6



TO BE PLACED ACROSS PCB AS CONVENIENT  
FOR OSCILLOSCOPE PROBE GROUNDS



## 1.2V REGULATOR



## 1.8V REGULATOR

