

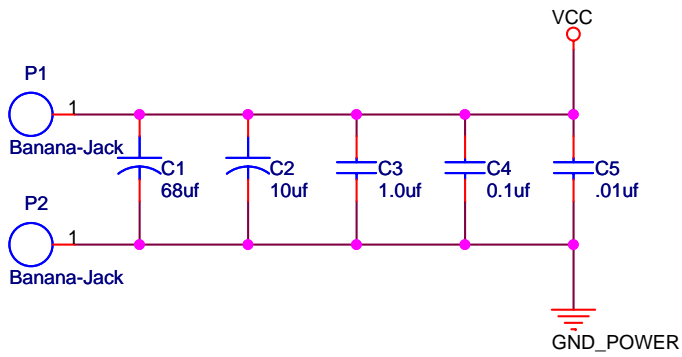
Notes:

1. Place net names on all jumpers and headers
2. All parts other than SMA connectors on a 0 or 90 degree orientation.
3. High speed serial data should be routed as single-ended 50 ohm transmission lines.
Routing distance should be 3 inches or less.
4. Use FR4-370 Material.
5. Serial TD, RD, and CLK nets must match within +/- 0.5 mils.
6. Match differential trace width of serial TD, RD, and CLK lines with SMA pads.
7. Parallel TD and RD, D0-D27 and CLK nets must match within +/- 0.5 mils
8. Place TI logo in top side metal.

Schematic Sheet Index:

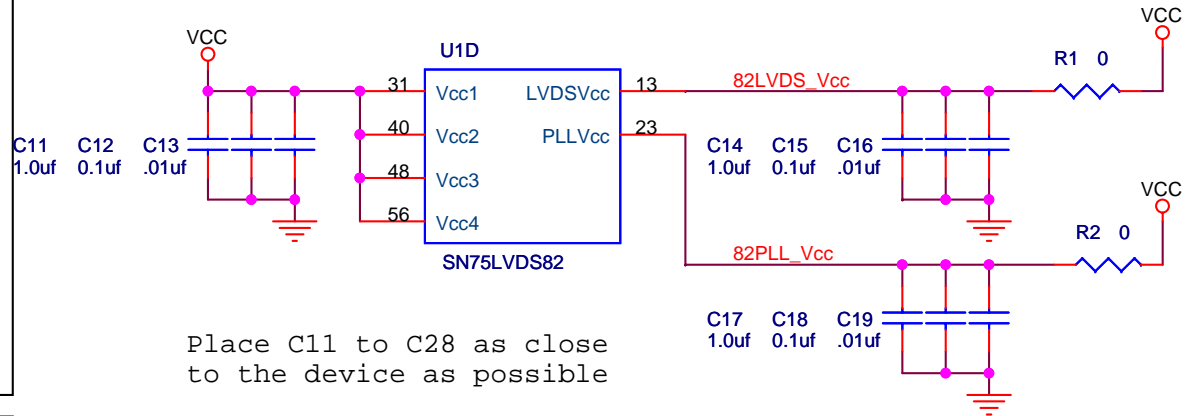
Sheet 01: Cover Sheet and Notes
Sheet 02: Device Power Ground
Sheet 03: Serial Inputs and Outputs
Sheet 04: Parallel Inputs and Outputs
Sheet 05: Control Logic and N.C.

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Schematic Sheet Index		
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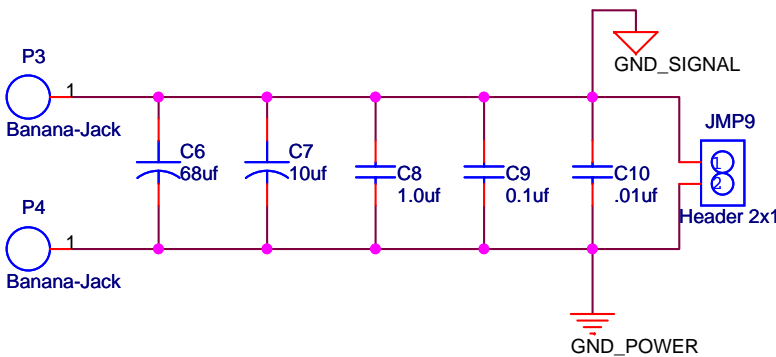


Vcc and Bulk By-Pass Capacitors

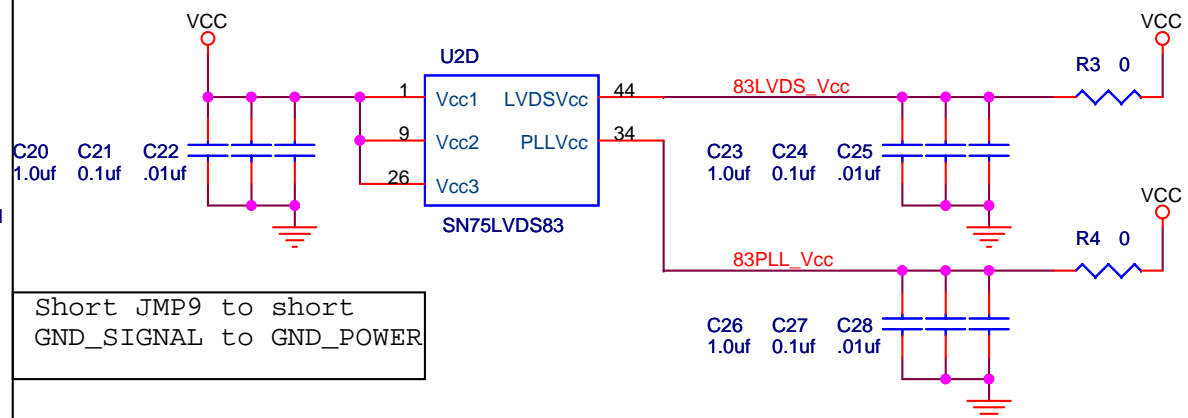
Device Power



Place C11 to C28 as close to the device as possible



Signal Ground and Bulk By-Pass Capacitor

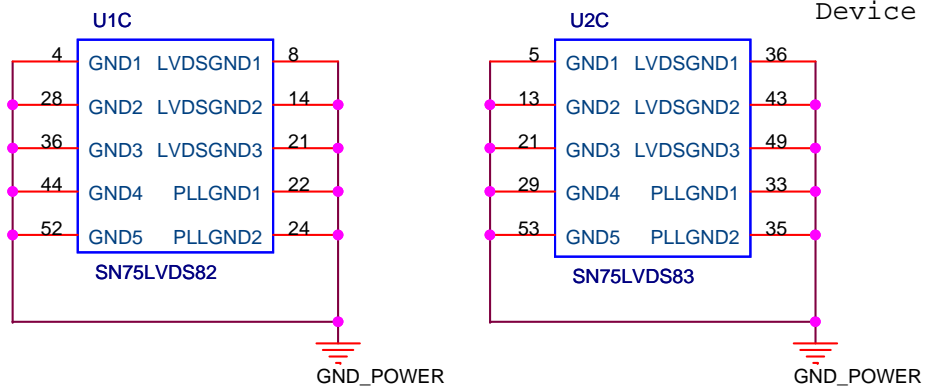


Short JMP9 to short GND_SIGNAL to GND_POWER

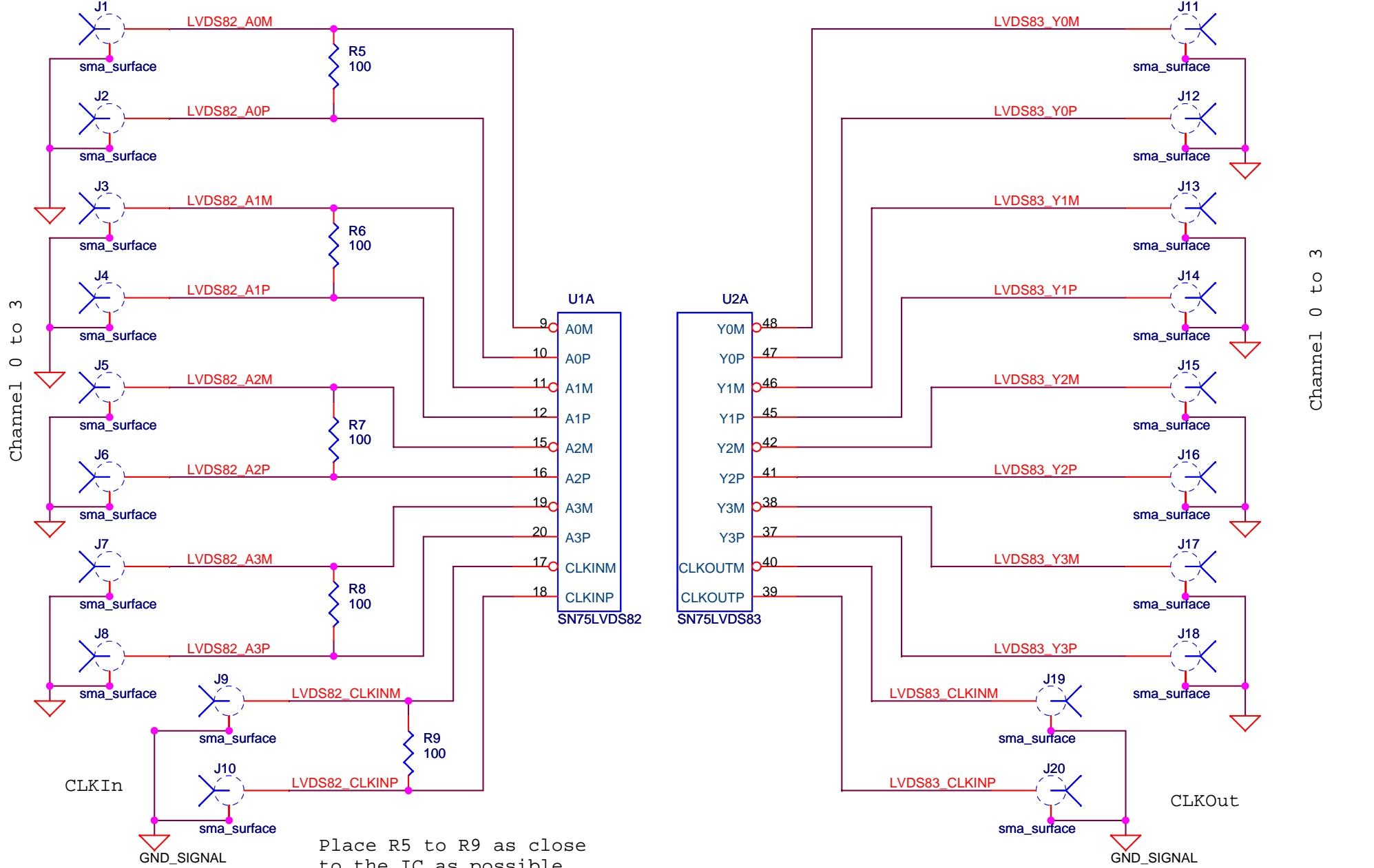
Place R1, R2, R3, R4 as close to bulk Vcc as possible.

R1, R2, R3, and R4 are 0 ohm 1206 resistors. Replace with ferrite beads if needed.

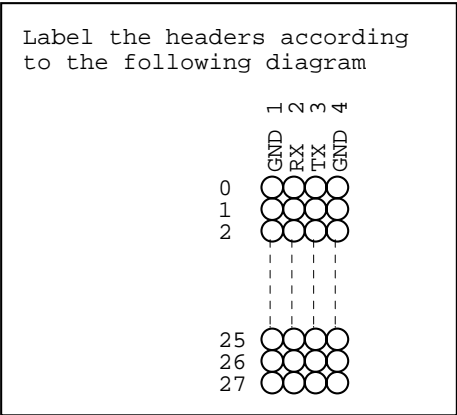
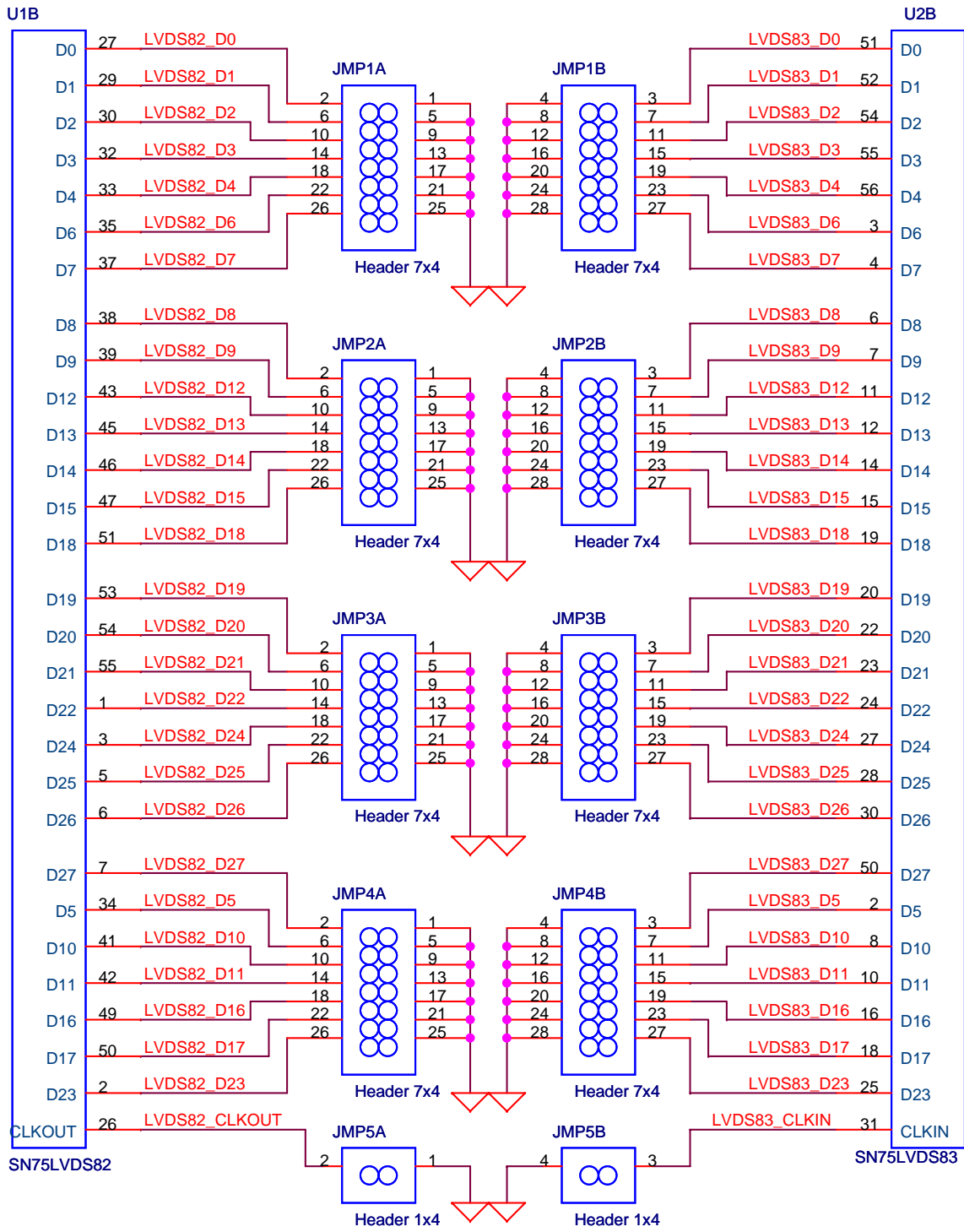
Device Ground



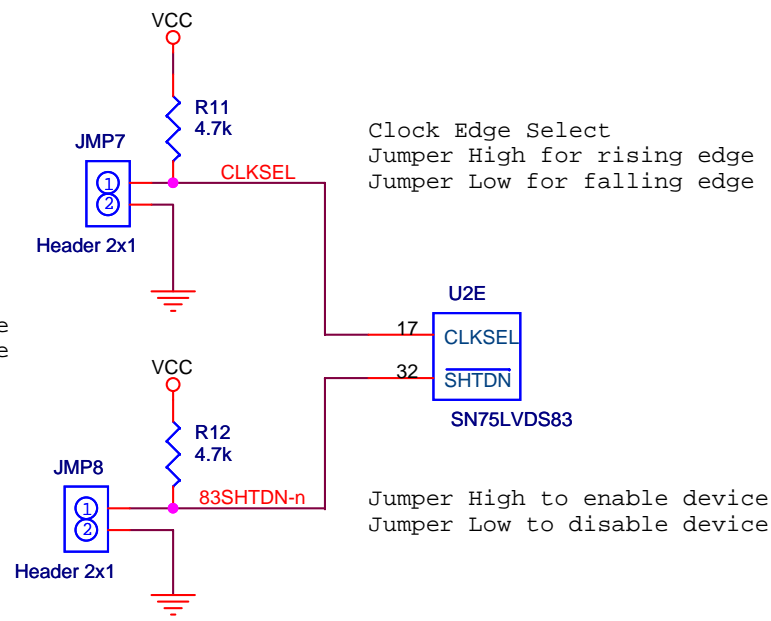
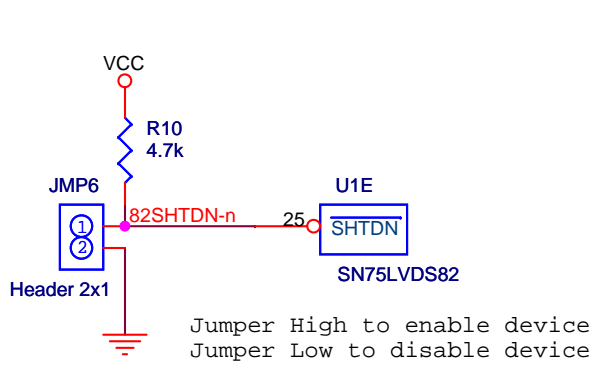
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Device Power Ground		
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Serial Inputs and Outputs		
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Parallel Inputs and Outputs		
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Control Logic and N.C.		
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