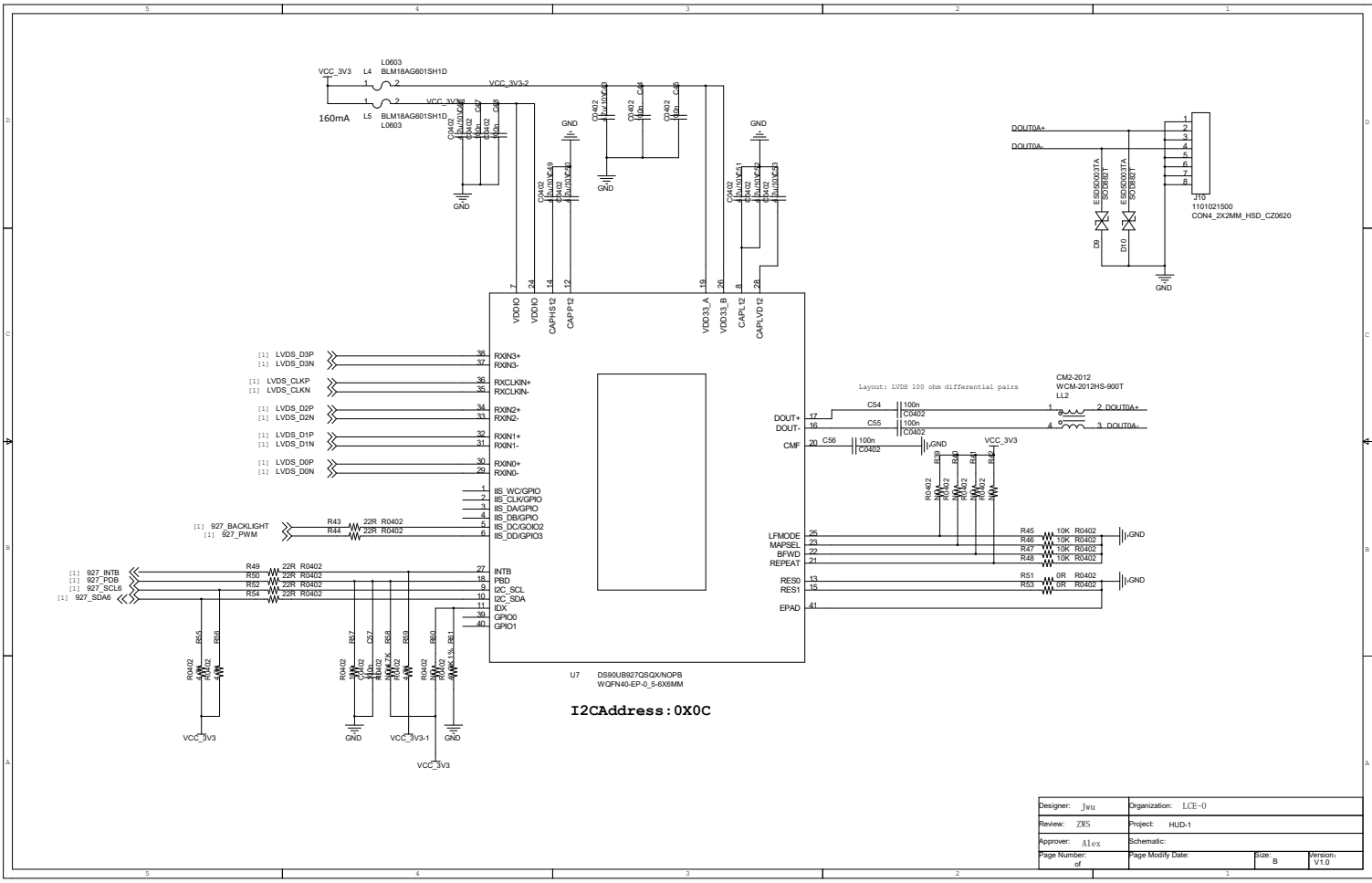
VCC_3V3
L4 L0603 BLM18AG601SH1D
L5 BLM18AG601SH1D L0603
160mA
VCC_3V3-2
VCC_3V3-1
GND
DOUT0A+
DOUT0A-
ESD5D03TA SOD882T
D9
D10
J10
1101021500
CON4_2X2MM_HSD_CZ0620
VDDIO
CAPHS12
CAPP12
VDD33_A
VDD33_B
CAPL12
CAPLVD12
RXIN3+
RXIN3-
RXCLKIN+
RXCLKIN-
RXIN2+
RXIN2-
RXIN1+
RXIN1-
RXIN0+
RXIN0-
IIS_WC/GPIO
IIS_CLK/GPIO
IIS_DA/GPIO
IIS_DB/GPIO
IIS_DC/GOIO2
IIS_DD/GPIO3
INTB
PBD
I2C_SCL
I2C_SDA
IDX
GPIO0
GPIO1
LVDS_D3P
LVDS_D3N
LVDS_CLKP
LVDS_CLKN
LVDS_D2P
LVDS_D2N
LVDS_D1P
LVDS_D1N
LVDS_D0P
LVDS_D0N
927_BACKLIGHT
927_PWM
927_INTB
927_PDB
927_SCL6
927_SDA6
R43 22R R0402
R44 22R R0402
R49 22R R0402
R50 22R R0402
R52 22R R0402
R54 22R R0402
U7 DS90UB927QSQX/NOPB
WQFN40-EP-0_5-6X6MM
I2CAddress:0X0C
Layout: LVDS 100 ohm differential pairs
CM2-2012
WCM-2012HS-900T
L12
DOUT+
DOUT-
CMF
C54 100n C0402
C55 100n C0402
C56 100n C0402
2 DOUT0A+
3 DOUT0A-
LFMODE
MAPSEL
BFWD
REPEAT
RES0
RES1
EPAD
R45 10K R0402
R46 10K R0402
R47 10K R0402
R48 10K R0402
R51 0R R0402
R53 0R R0402
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Project: HUD-1
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Schematic:
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