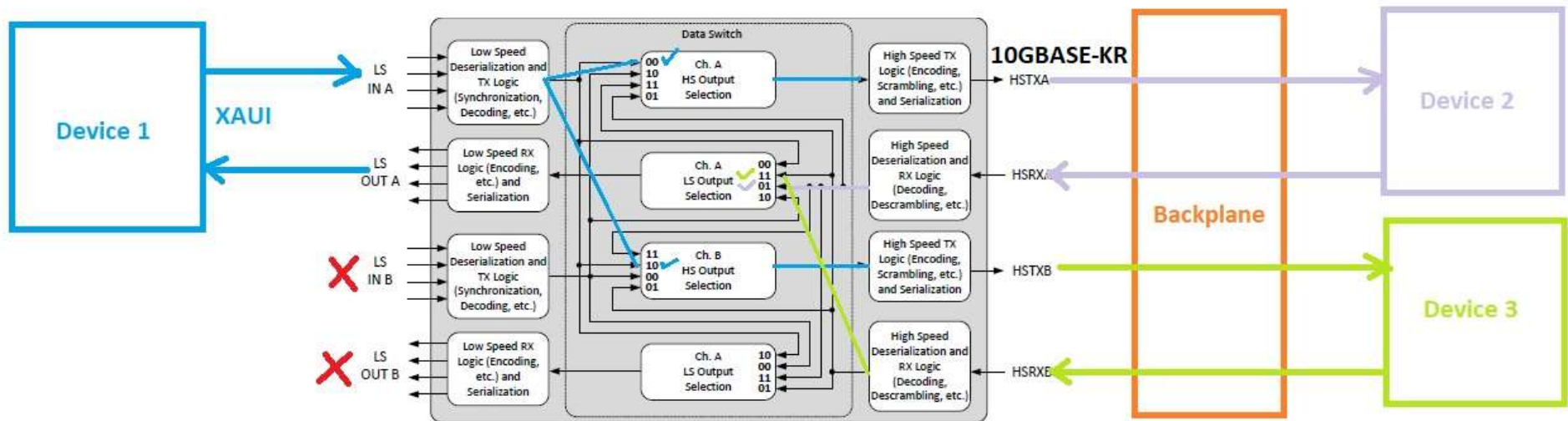




Channel A LS input is to be duplicated on Channel A & B HS TX.

Channel B LS is not connected anywhere.

Channel A LS O/P is to be HSRXA or HSRXB based on pin selection or MDIO reg config.