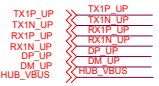
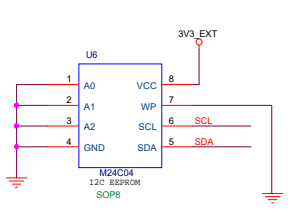
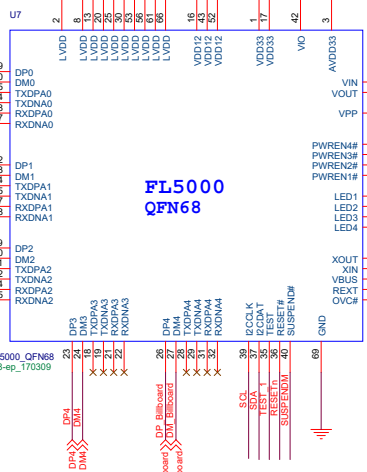
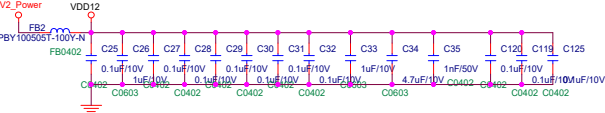
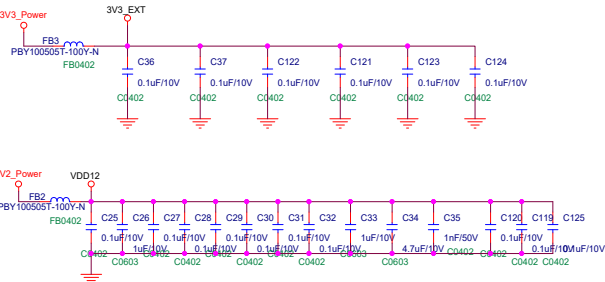


# EEPROM



**UP\_PORT**  
TX/RX P/N SWAP  
for Layout



**TYPE A Port1**

**TYPE A Port2**

