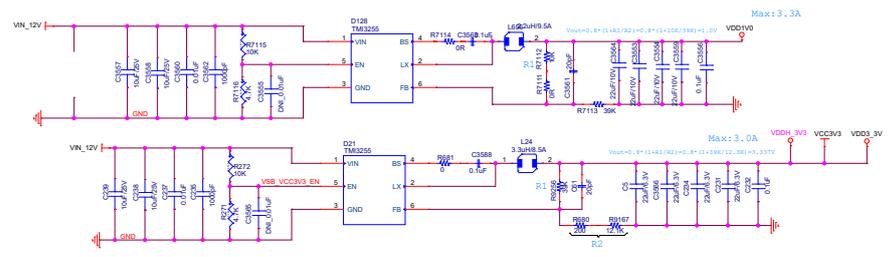
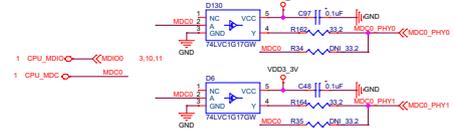
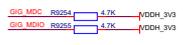
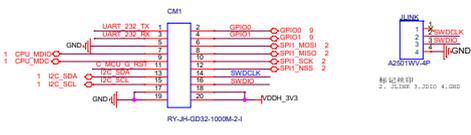
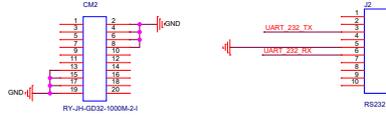
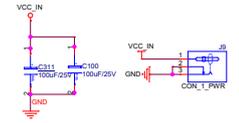
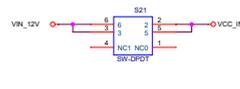
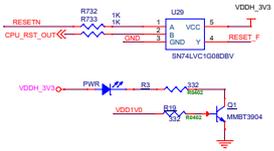
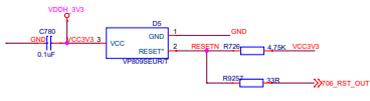


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 1-50285-->2-50285
 1-TX0->2-RX0, 1-TX1->2-RX1

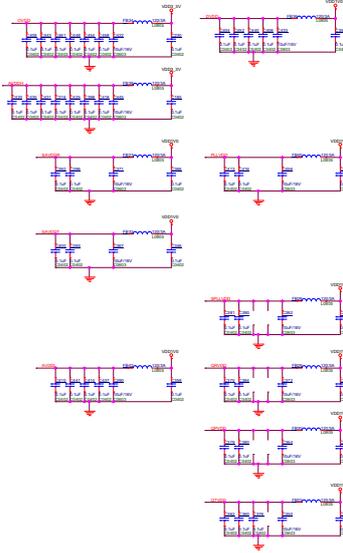
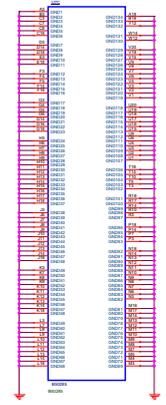
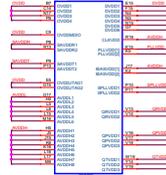
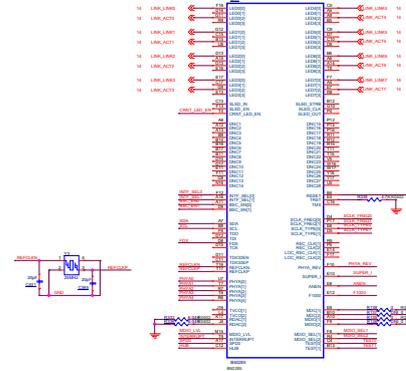
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Date	Monday, May 06, 2024	Sheet 1 of 1

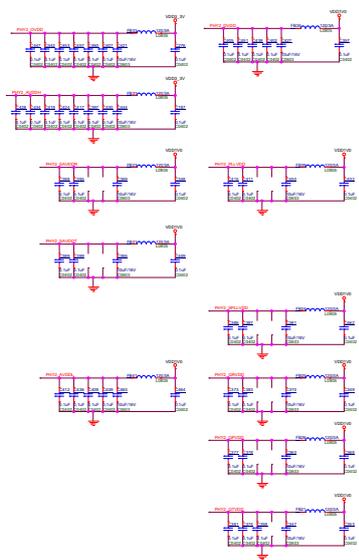
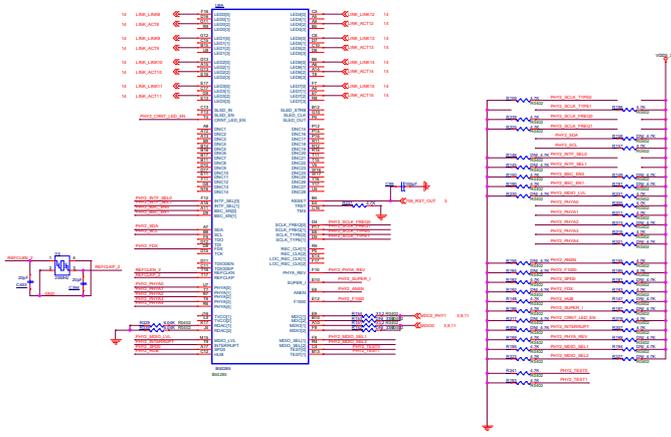
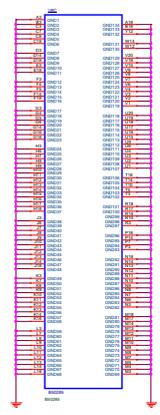


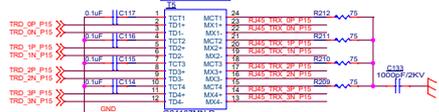
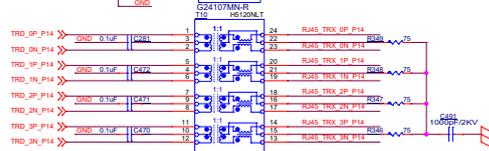
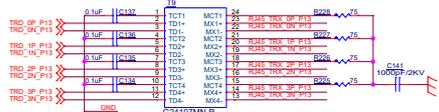
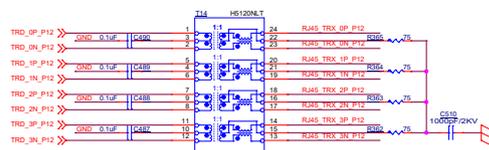
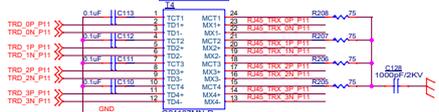
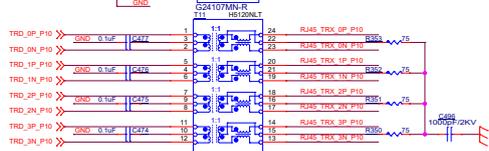
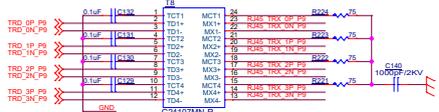
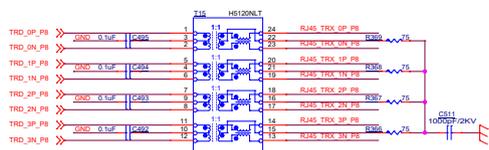
File	<Title>	Rev
Rev	Document Number	<Rev>
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Doc	TH969V_M001P_004	Sheet 2 of 14

INTF_SEL[0]	I/O	Interface Mode Select. Active high. Select the PHY configuration shown below. INTF_SEL[0] MODE 00 QSGMII to Copper 01 QSGMII to Fiber 10 Reserved 11 Reserved Note * Copper = 10BASE-T, 100BASE-TX, 1000BASE-T. * Fiber = 100BASE-FX, 1000BASE-X, SGMII-Slave.
INTF_SEL[1]	SOR, D	
BSC_EN[0]	I/O	BSC Enable. Active high. Broadcom Serial Control. The BSC interface acts as an FC Master. These inputs enable and select the type of EEPROM. See "Broadcom Serial Control Interface" on page 15 for details. BSC_EN[0] 0 * 00 = Disable BSC interface. * 11 = Auto detect between Small and Large PROM. Note: The Broadcom serial control (BSC) bus is NOT FC compatible.
BSC_EN[1]	SOR, D	
TEST[0]	I/O	Test Mode Enables. Active high. These balls must always be pulled low during normal operation. Pulling both balls high tristates all outputs. These balls are used by Broadcom for test purposes only. These balls should be tied to ground.
TEST[1]	SOR, D	

ANEN	I/O	Auto-negotiation Select. Active high. When high, auto-negotiation is enabled, when low, auto-negotiation is disabled. After reset, the auto-negotiation function is under software control. This ball works in conjunction with the F1000, FDX, and SPD0 to set up the default duplex and speed of the twisted-pair interface.			
SPD0	SOR, D				
F1000	I/O	Speed Select. Active high. Sets the default advertisement of the B50285, according to the following table.			
	SOR, D				
	I/O	ANEN	F1000	SPD0	Description
	SOR, D	0	0	0	Force 100BASE-T
		0	0	1	Force 100BASE-TX
		0	1	X	Force 1000BASE-T (See Note below)
		1	0	0	Auto-negotiate advertise: 10BASE-T
		1	0	1	Auto-negotiate advertise: 10/100BASE-TX
		1	1	0	Auto-negotiate advertise: 10/100/1000BASE-T
		1	1	1	Auto-negotiate advertise: 1000BASE-T







R445 TRX OP P8 >> R445 TRX OP P8 (9)
 R445 TRX DN P8 >> R445 TRX DN P8 (9)
 R445 TRX IP P8 >> R445 TRX IP P8 (9)
 R445 TRX IN P8 >> R445 TRX IN P8 (9)
 R445 TRX 2P P8 >> R445 TRX 2P P8 (9)
 R445 TRX 2N P8 >> R445 TRX 2N P8 (9)
 R445 TRX 3P P8 >> R445 TRX 3P P8 (9)
 R445 TRX 3N P8 >> R445 TRX 3N P8 (9)

R445 TRX OP P9 >> R445 TRX OP P9 (9)
 R445 TRX DN P9 >> R445 TRX DN P9 (9)
 R445 TRX IP P9 >> R445 TRX IP P9 (9)
 R445 TRX IN P9 >> R445 TRX IN P9 (9)
 R445 TRX 2P P9 >> R445 TRX 2P P9 (9)
 R445 TRX 2N P9 >> R445 TRX 2N P9 (9)
 R445 TRX 3P P9 >> R445 TRX 3P P9 (9)
 R445 TRX 3N P9 >> R445 TRX 3N P9 (9)

R445 TRX OP P10 >> R445 TRX OP P10 (9)
 R445 TRX DN P10 >> R445 TRX DN P10 (9)
 R445 TRX IP P10 >> R445 TRX IP P10 (9)
 R445 TRX IN P10 >> R445 TRX IN P10 (9)
 R445 TRX 2P P10 >> R445 TRX 2P P10 (9)
 R445 TRX 2N P10 >> R445 TRX 2N P10 (9)
 R445 TRX 3P P10 >> R445 TRX 3P P10 (9)
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 R445 TRX DN P11 >> R445 TRX DN P11 (9)
 R445 TRX IP P11 >> R445 TRX IP P11 (9)
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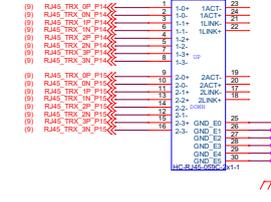
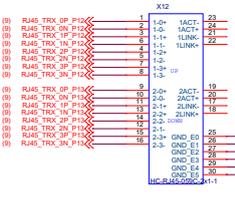
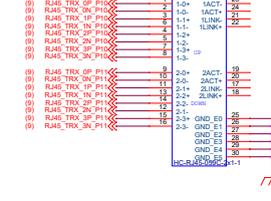
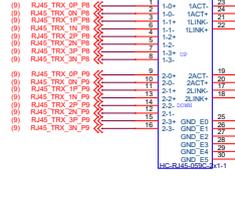
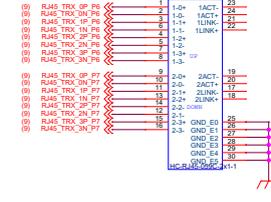
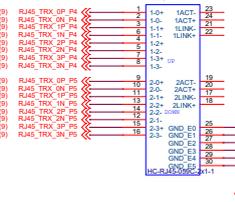
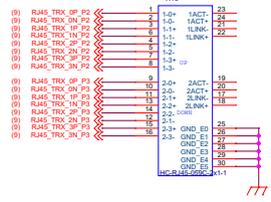
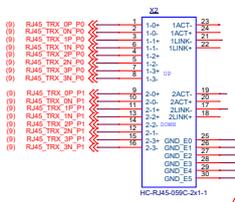
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 R445 TRX 2N P13 >> R445 TRX 2N P13 (9)
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 R445 TRX IP P14 >> R445 TRX IP P14 (9)
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 R445 TRX 2N P14 >> R445 TRX 2N P14 (9)
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 R445 TRX 3N P15 >> R445 TRX 3N P15 (9)

File	<Title>	Rev
Doc	Document Number	
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Rev	Thursday, May 19, 2011	Sheet 12 of 14



File	<Title>	Rev
Doc	Document Number	1.0
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Date:	Thursday, May 02, 2024	Sheet 13 of 14