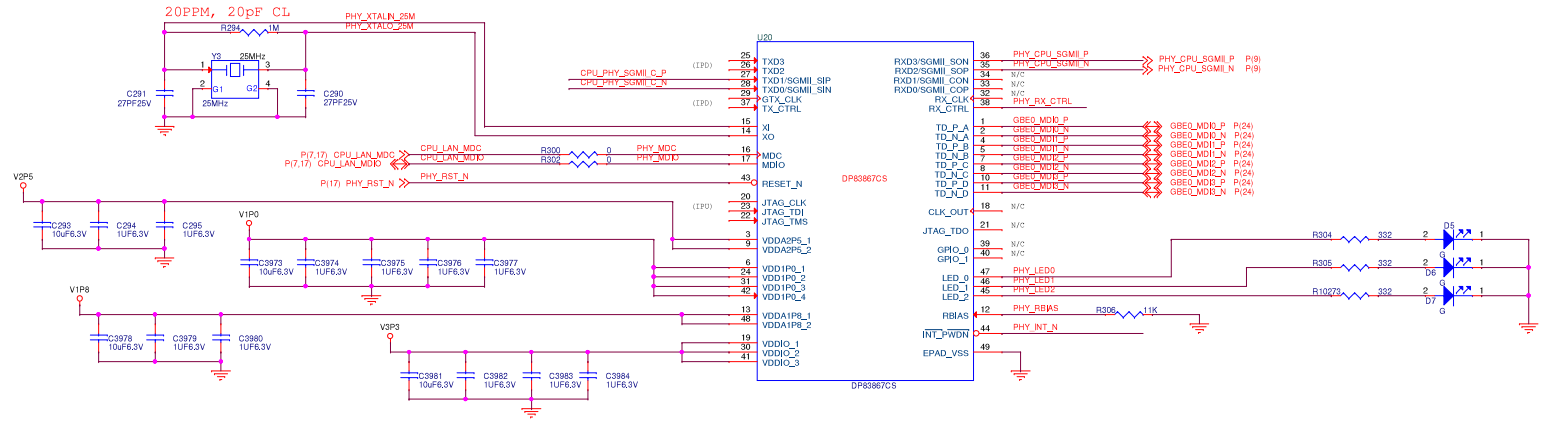
SGMII-TO-COPPER TRI-SPEED PHY



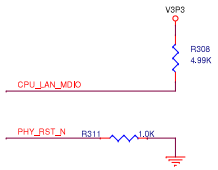
PHY STRAPS

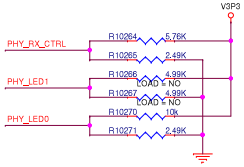
RXD2: PHY address: Deault PHY_AD = 00000b

RX_CTRL: ANEG control: Default = ANEG ON

LED1: ANEG_SEL: Default = 10/100/1000 all advertised

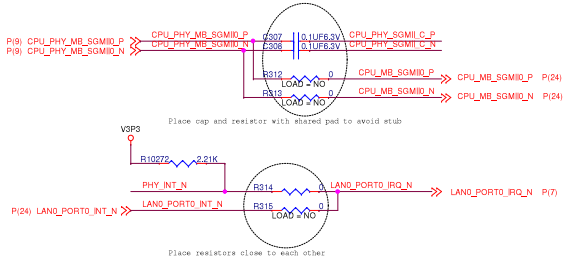
LED0: Port mirror/SGMII Enable: Default = mirror disabled, SGMII enabled





PHY BYPASS OPTION IN CASE CARRIER BOARD NEEDS SGMII RATHER THAN MDI

(Also see similar bypass resistors on CPU page)



<variant_name>

Gigamon

Title: 10/100/1000BASE-T PHY

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