

Requirement :

- 1. Normal mode, $I_{DD} \gg 1\text{mA}$ to 150mA
- 2. Sleep mode $I_{DD} \gg 1\mu\text{A}$ to $100\mu\text{A}$

Optional Components for Proto Board >>
1. Schottky Diodes D8, D9, D10 (drop 0.3V) can be assembled and remove R116, R149 to eliminate the DS1314 quiescent current error in SRAM current reading. Drawback will be the additional 0.3V drop in VDD to SRAM in normal mode and battery mode.
2. Make R53 = 0.68 Ohm . Compensate for FET ON resistance (30 mOhm approx. Will have device to device variation). If this error is acceptable for application, no need to use 2nd Instrumentation Amp U20. Also, we can use one channel pin compatible ADC - ADS1014

