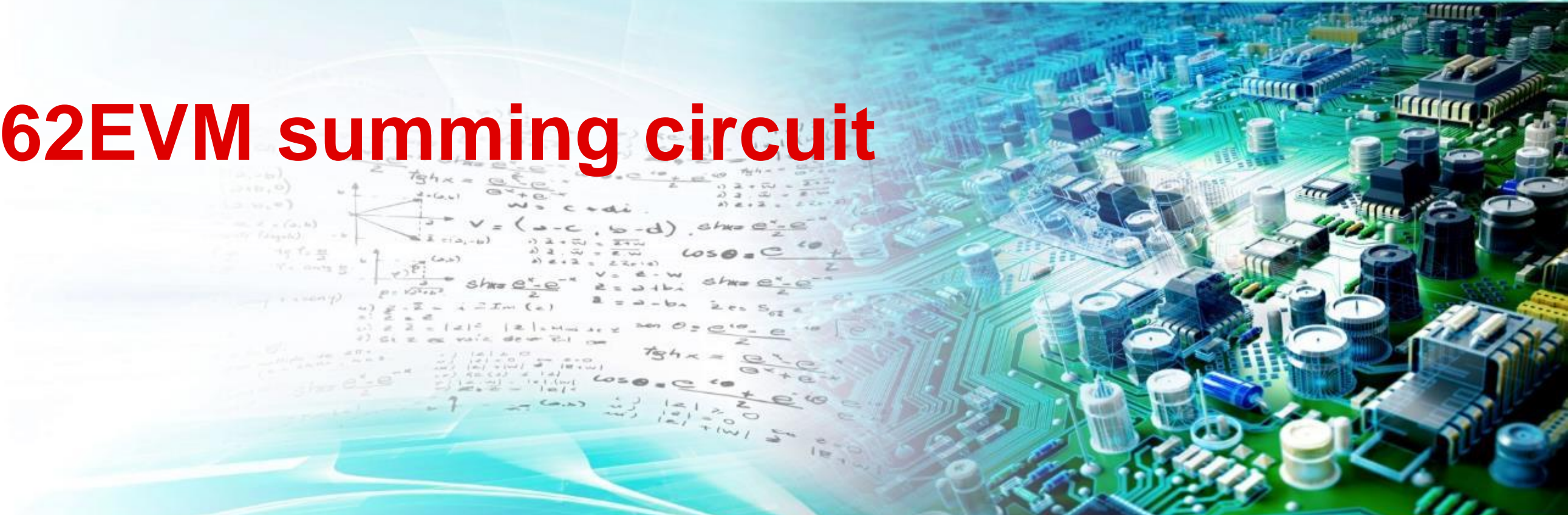


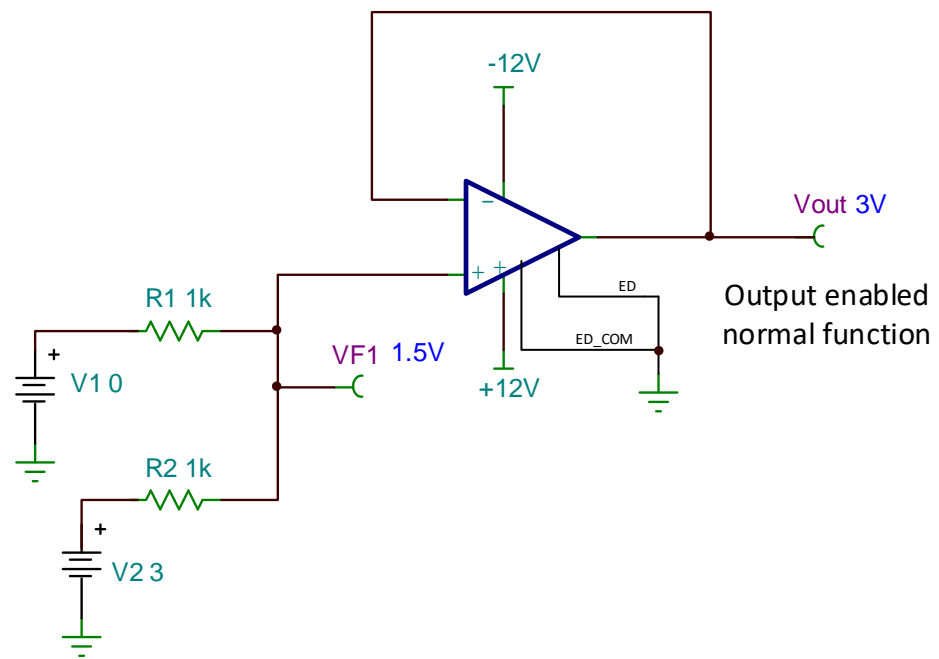
OPA462EVM summing circuit

Art Kay

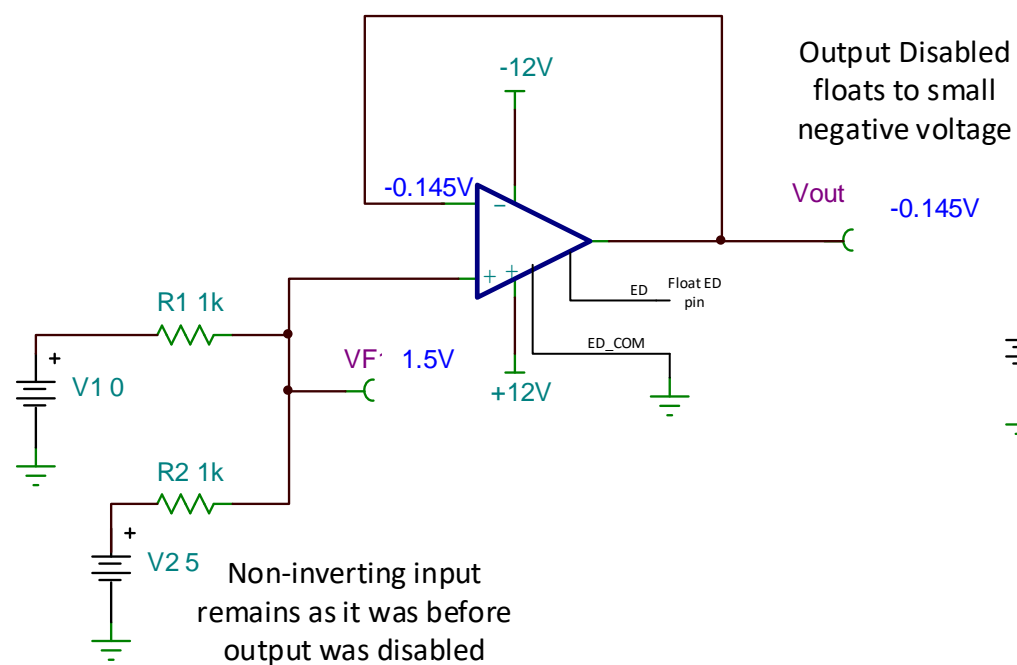
4-4-2023



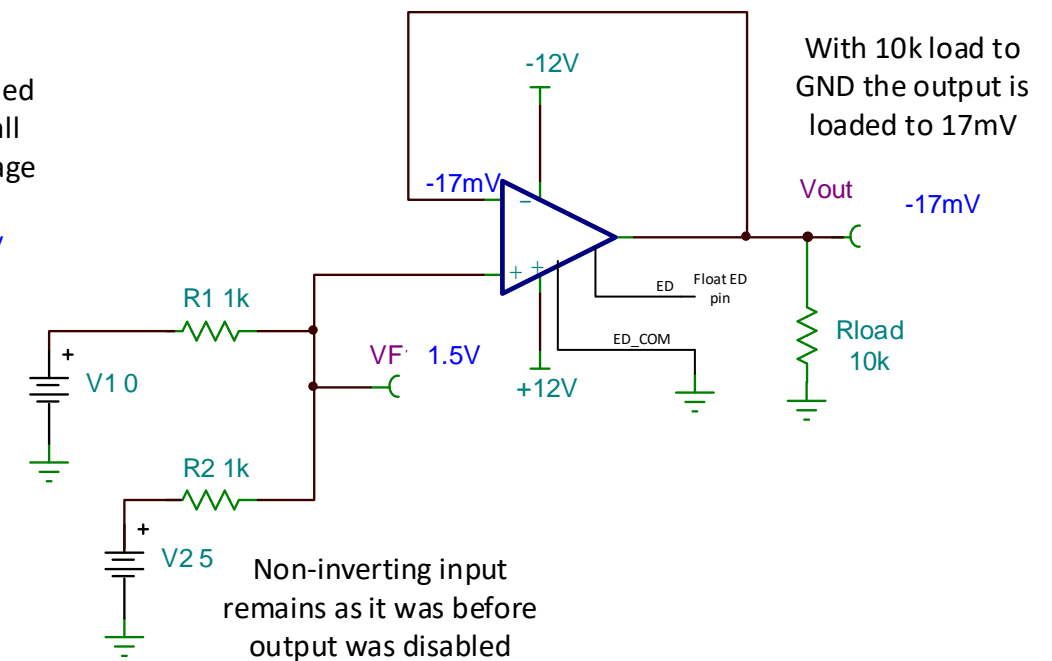
Summing circuit



Normal operation



Disabled no load

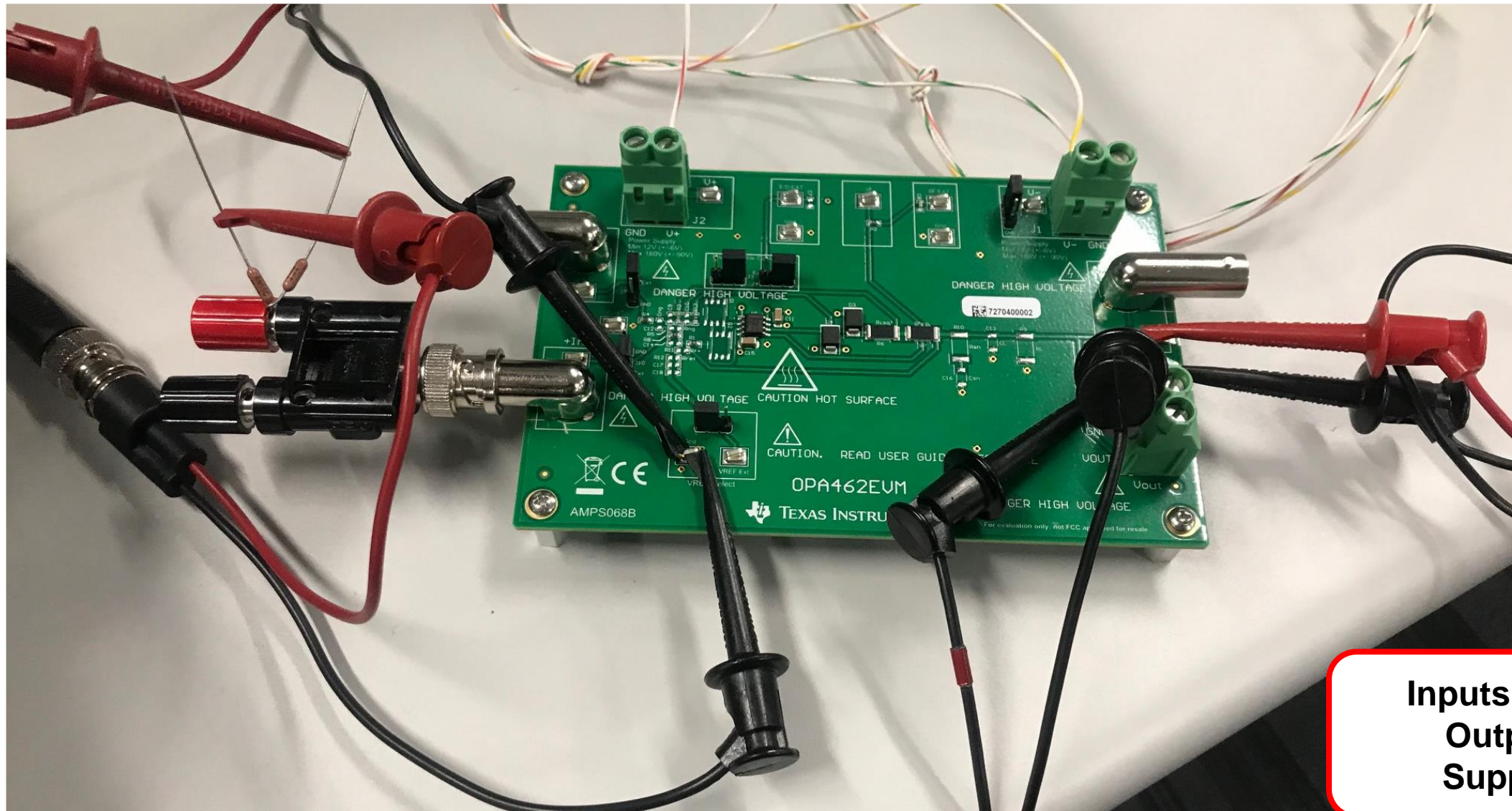


Disabled with load

Disabled output

	OPA462 Disabled Output				
Input	No load	1M	100k	10k	1k
3V, 0V	-0.145V	-0.135V	-0.084V	-0.017V	-0.002V
3V, 3V	0.812V	0.760V	0.480V	0.100V	0.011V
6V, 6V	2.720V	2.560V	1.640V	0.350V	0.040V
-1V, -1V	-0.606V	-0.570V	-0.210V	-0.030V	-0.003V
-6V, -6V	-3.090V	-2.900V	-1.890V	-0.410V	-0.046V

Hardware setup



**Inputs: 2 x DP8200
Output: 34401
Supply: E6331**