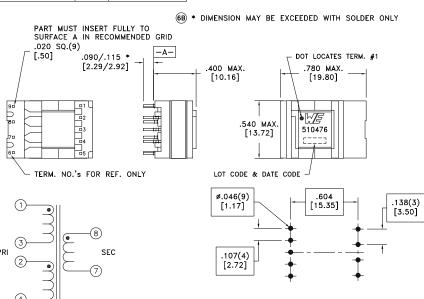


Customer to tie terminals

2+3 on PCB.





## ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-3	@20°C	0.045 ohms max.
D.C. RESISTANCE	2-4	@20°C	0.035 ohms max.
D.C. RESISTANCE	7-8	@20°C	0.085 ohms max.
INDUCTANCE	1-4	tie(2+3), 100kHz, 100mVAC, Ls	870uH ±30%
LEAKAGE INDUCTANCE	1-4	tie(2+3, 7+8), 100kHz, 10mVAC, Ls	400nH typ., 800nH max.
DIELECTRIC	1-8	tie(2+3), 5000VAC, 1 second	4000VAC, 1 minute
TURNS RATIO		(1-4):(8-7), tie(2+3)	1.307:1, ±1%
TURNS RATIO		(2-4):(1-3)	1.125:1, ±1%

## GENERAL SPECIFICATIONS:

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Reinforced insulation for a primary circuit at a working voltage of 400VDC.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Specifications
		Method: Tray
		PKG-0730
6B	2/17	www.we-online.com/midcom convention placement
6A	4/12	SEE REVISION SHEET FOR REVISION LEVEL

Tolerances unless otherwise specified: Angles:  $\pm 1^{\circ}$  Decimals:  $\pm .005$  [.13] Fractions:  $\pm 1/64$  Footprint:  $\pm .001$  [.03]

RECOMMENDED P.C. PATTERN, COMPONENT SIDE

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

**TRANSFORMER** 

PART NO.

750510476

eiSos p/n: 750510476 SPECIFICATION SHEET 1 OF 1

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