

Comparator Applications

TIPL Presentation

TI Precision Labs – Op Amps

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The Comparator Function – Non-inverting

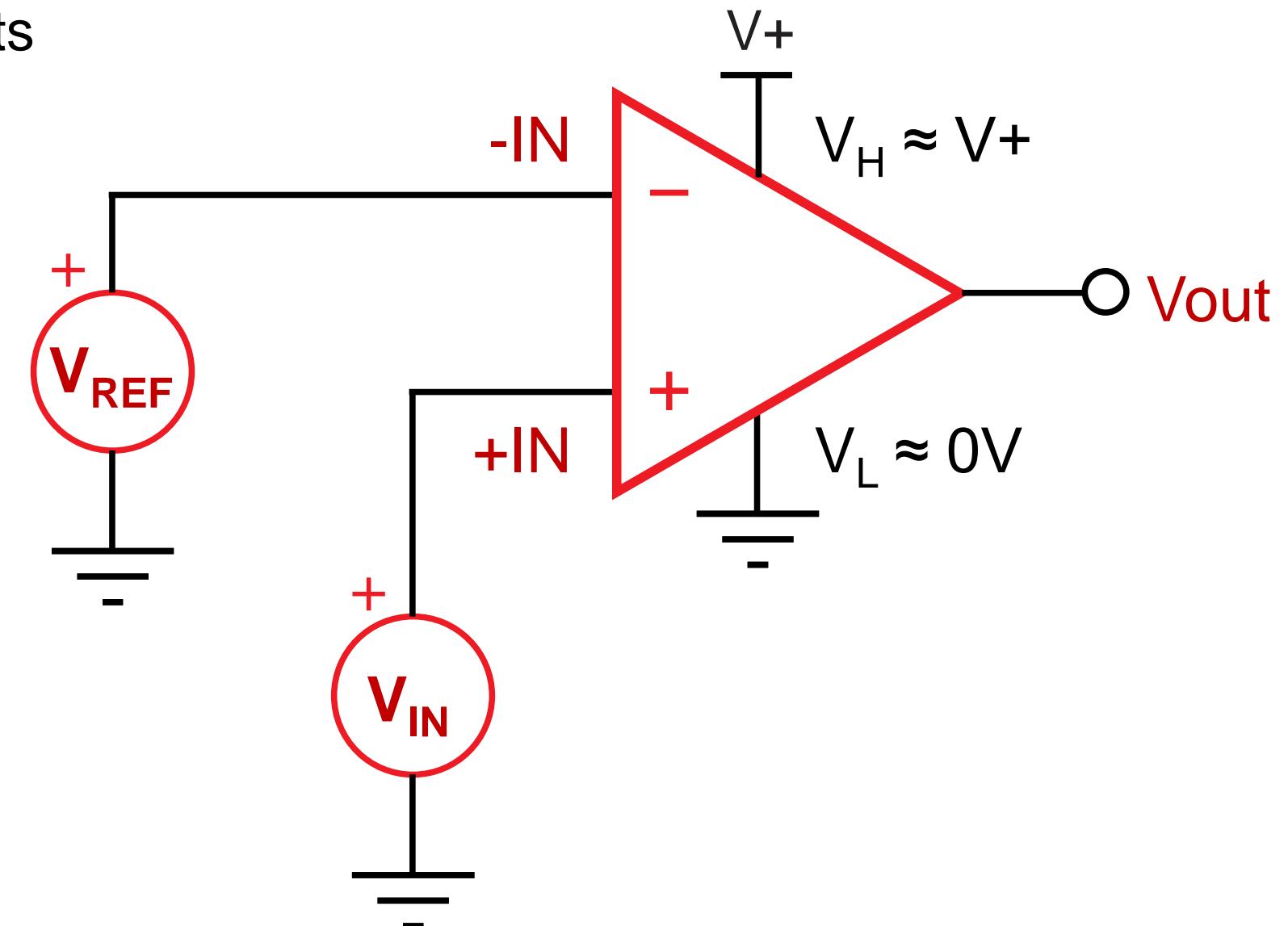
V_{IN} and V_{REF} applied to **+IN** and **-IN** inputs

- V_{IN} = input signal
- V_{REF} = reference signal
- may have both dc and ac components

Vout can be one of two levels

- V_H = HIGH (1)
- V_L = LOW (0)

Input	Output
$V_{IN} \Rightarrow V_{REF}$	HIGH (1)
$V_{IN} < V_{REF}$	LOW (0)

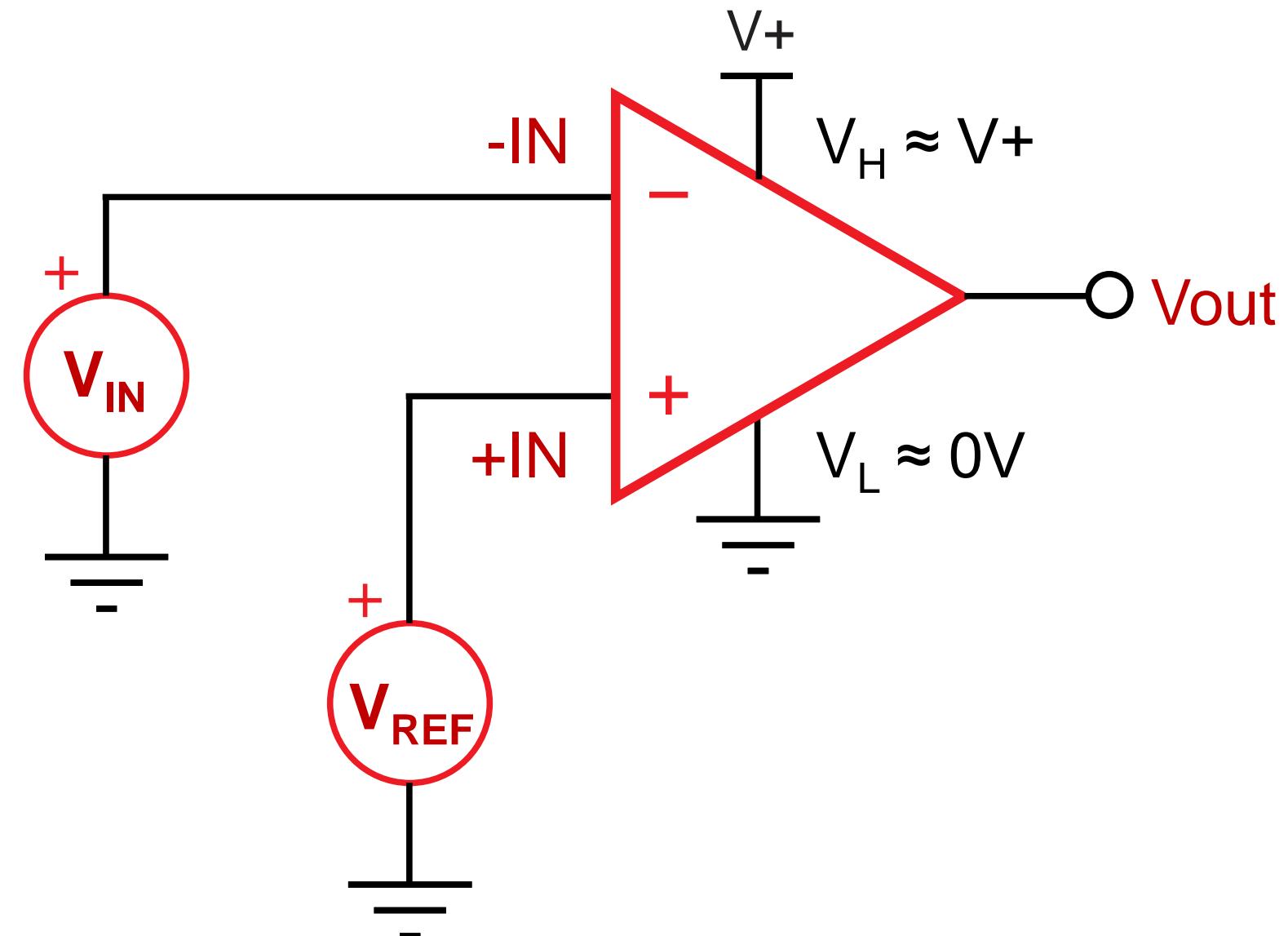


The Comparator Function – Inverting

Switch the input and reference signals to use the comparator in an inverting configuration

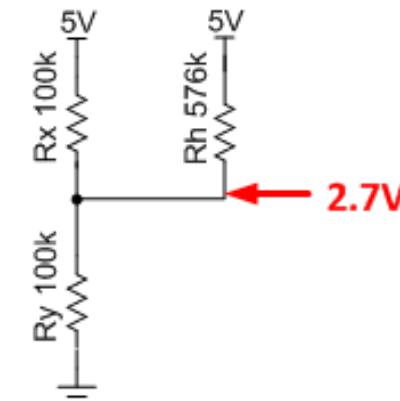
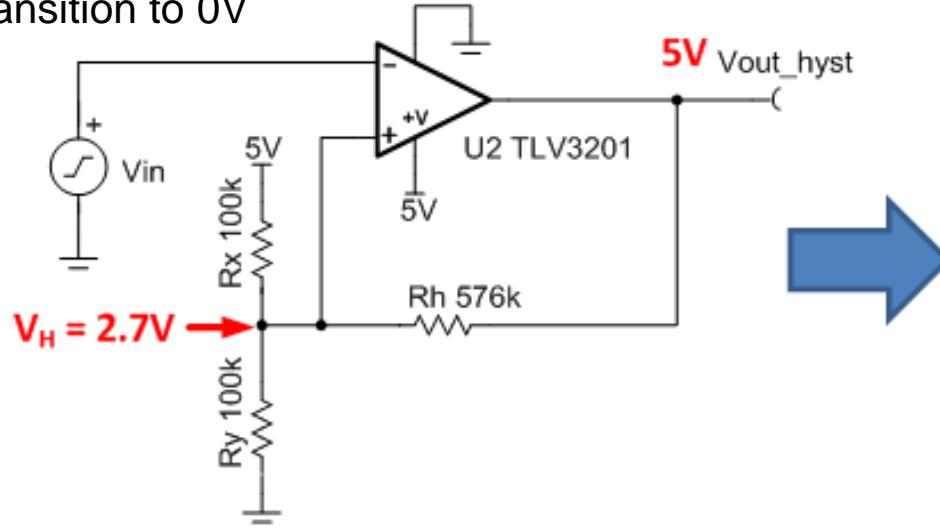
- V_{IN} applied to $-IN$
- V_{REF} applied to $+IN$

Input	Output
$V_{IN} > V_{REF}$	LOW (0)
$V_{IN} \leq V_{REF}$	HIGH (1)

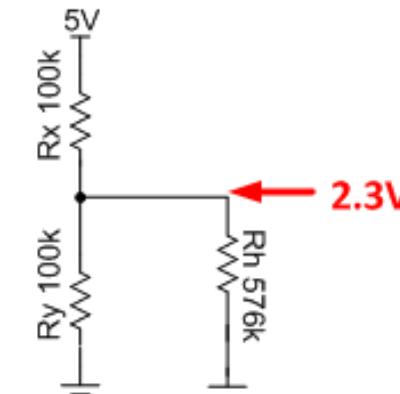
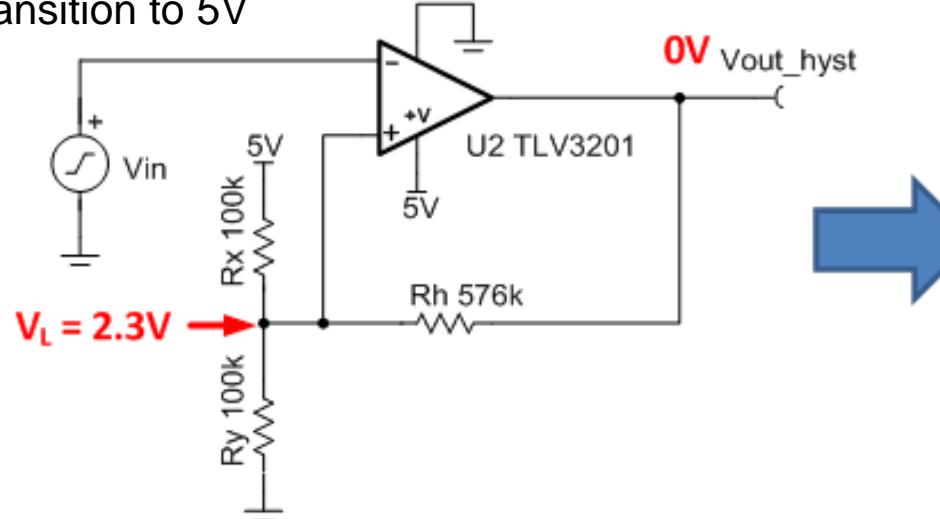


Reducing Noise Sensitivity with Hysteresis

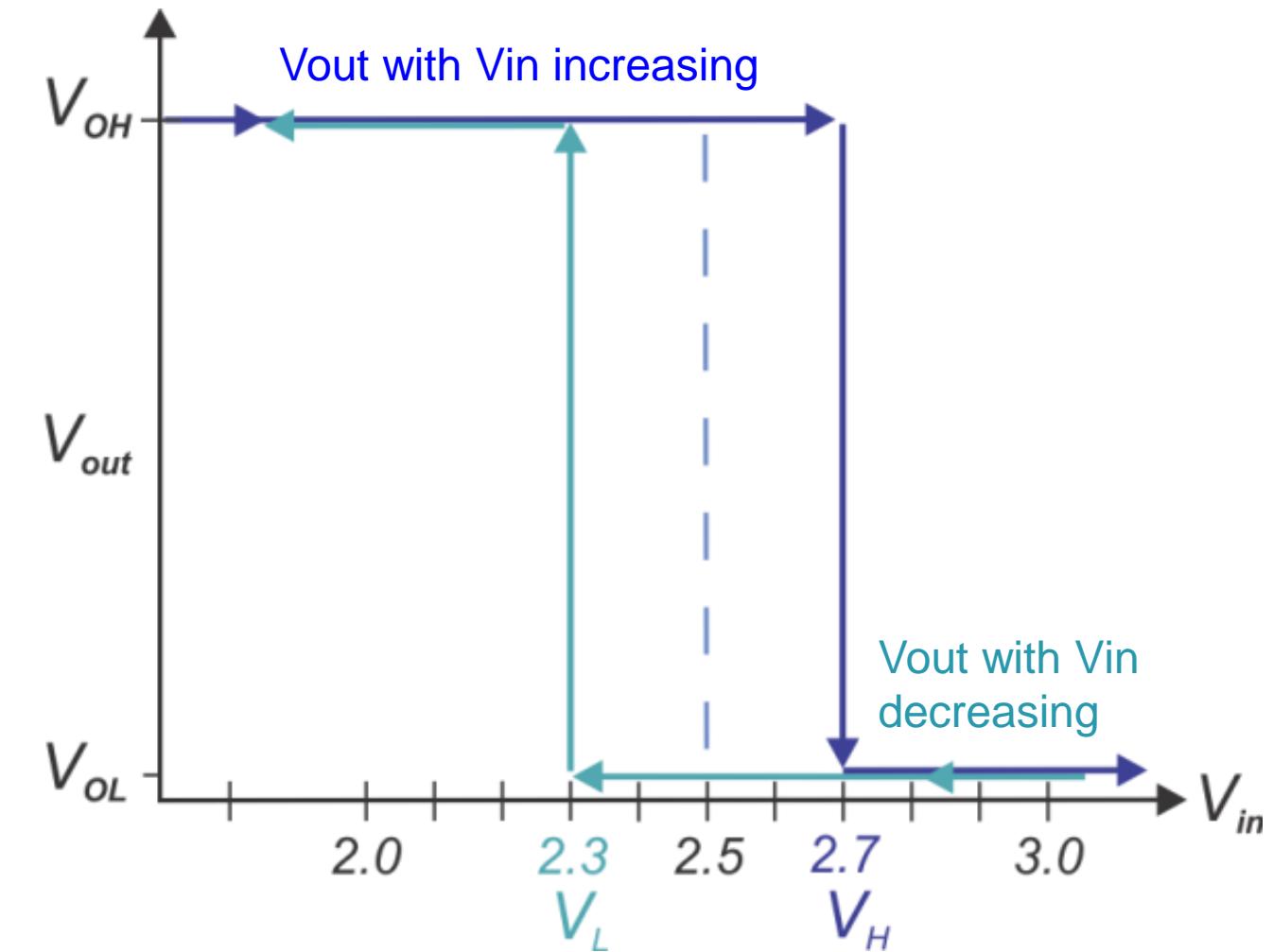
$V_{in} > 2.7V$ causes V_{out} to transition to 0V



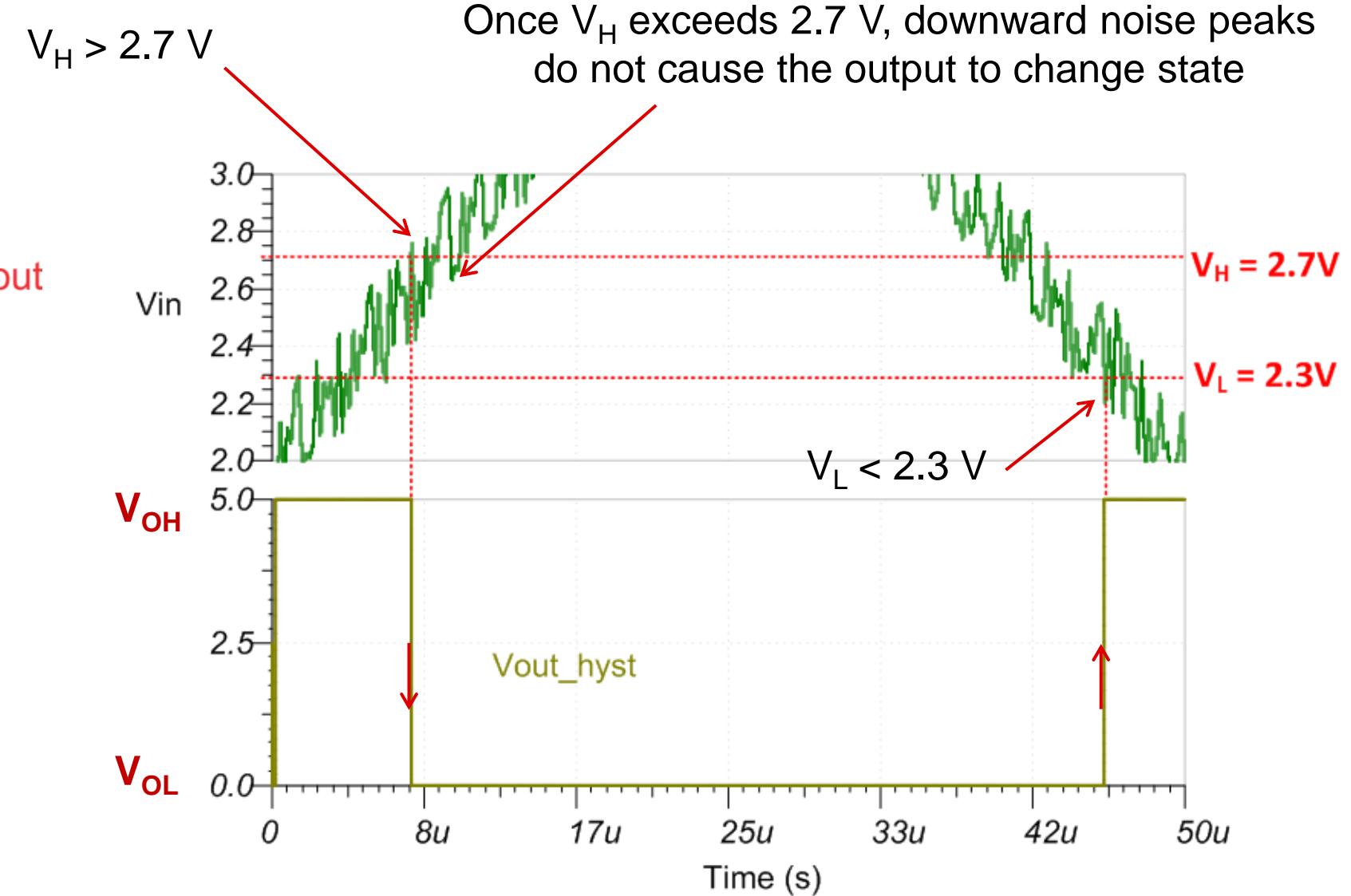
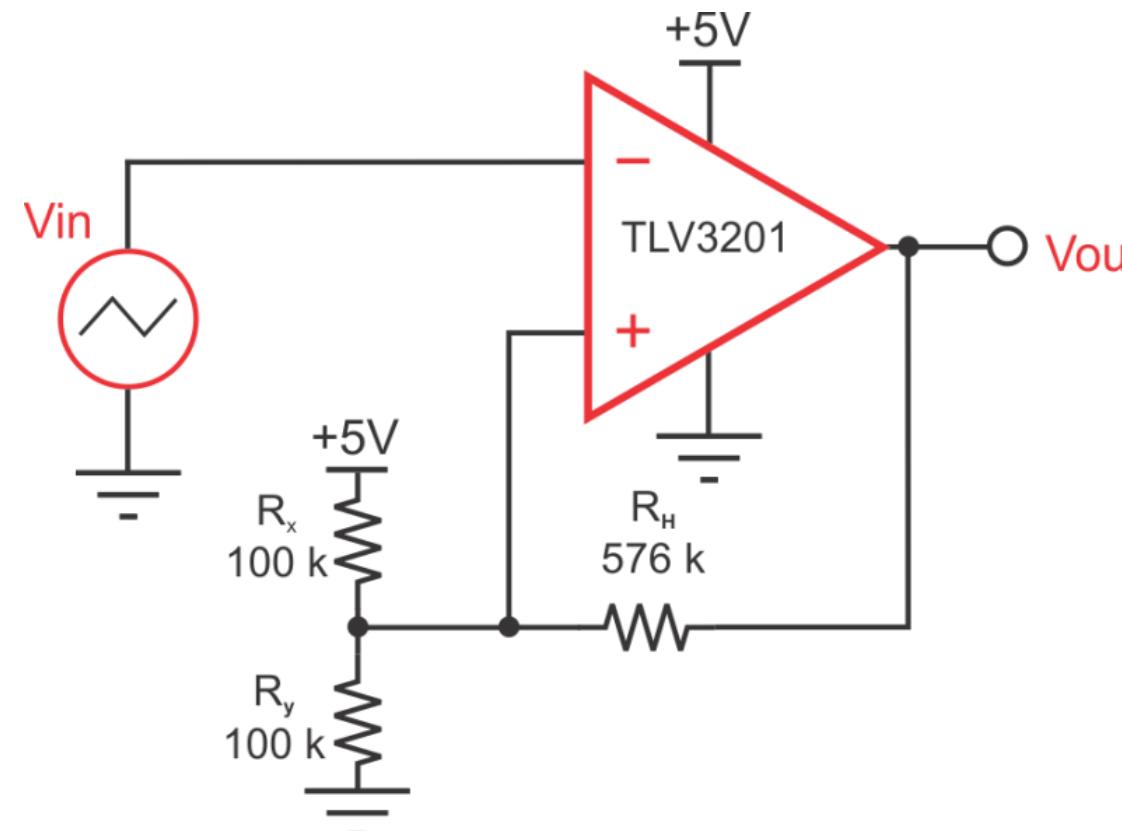
$V_{in} < 2.3V$ causes V_{out} to transition to 5V



V_{out} vs V_{in} with 400 mV of hysteresis



Reducing Noise Sensitivity with Hysteresis

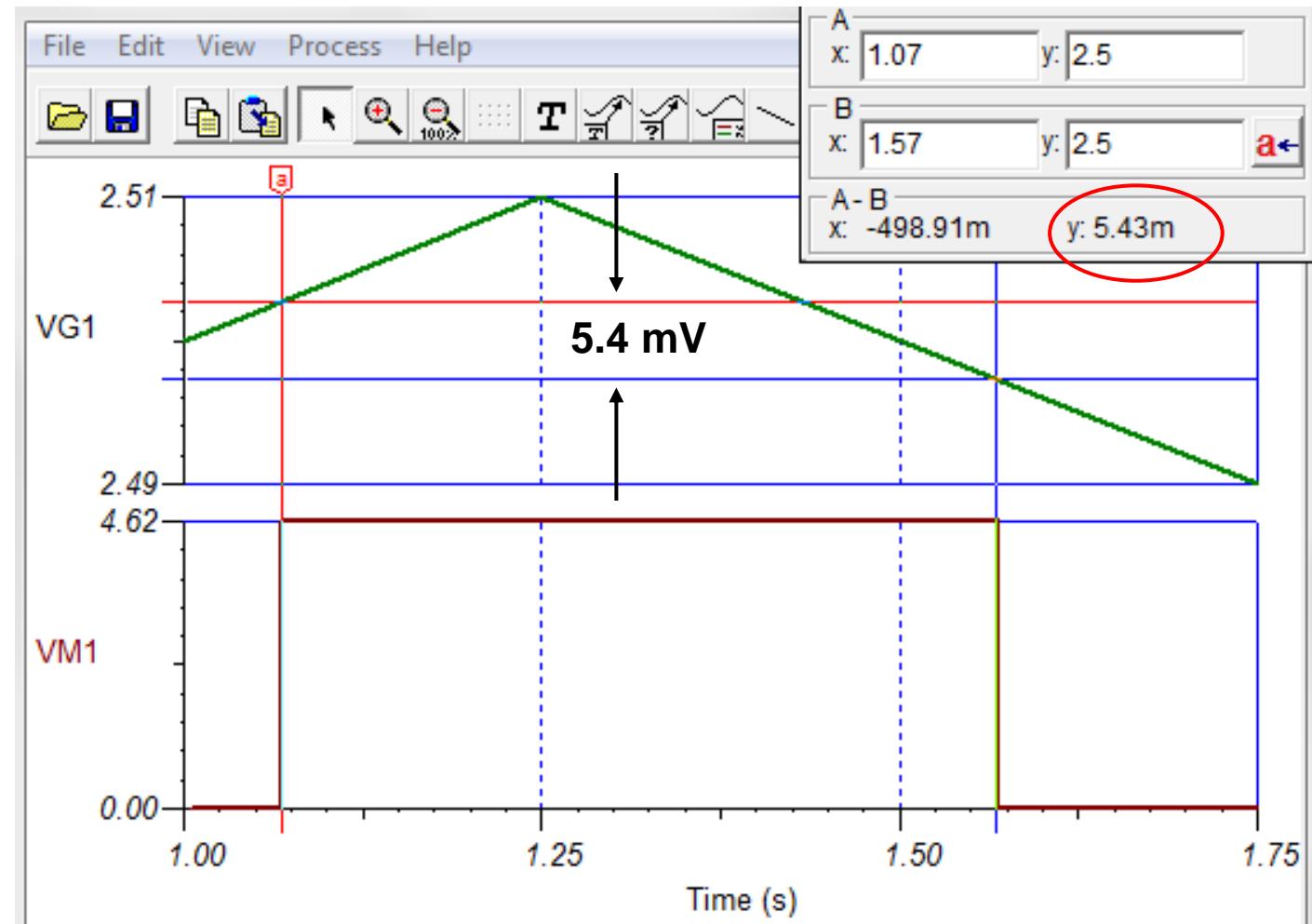


Some Comparators Have Built-in Hysteresis

TI comparators with built-in hysteresis:

- **TLV7011** – 350 ns micropower, push-pull output, V_{HYST} 4.2 mV typ.
- **TLV3202** – 40 ns micropower, push-pull output, V_{HYST} 1.2 mV typ.
- **TLV3501** – 4.5 ns high speed, push-pull output, V_{HYST} 6 mV typ.
- **TL712** – 25 ns high speed, differential open-collector, V_{HYST} 5 mV typ.
- **TL714** – 5 ns high speed, push-pull output, V_{HYST} 10 mV typical
- **TLV3691** – 24us, 75nA nanopower, push-pull output, V_{HYST} 17 mV typical
- **LMP7300** – μ Power, open-collector, positive and negative hysteresis set independently (0 to 110mV)

TLV3501 simulation model with built-in hysteresis

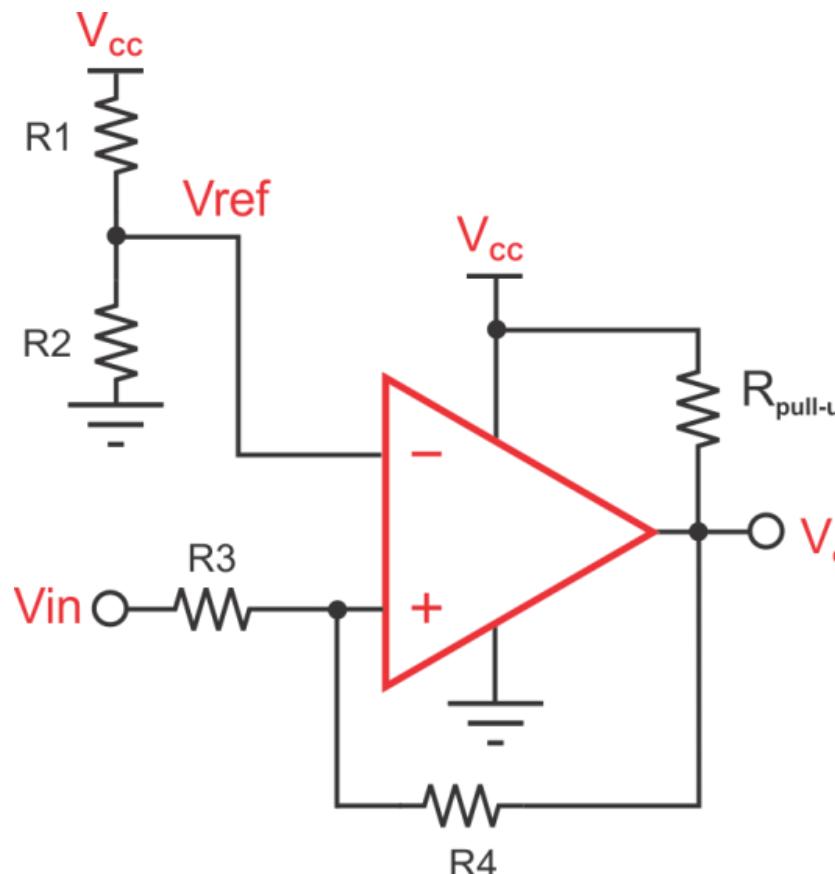


NOTE: External hysteresis can still be applied!

Hysteresis Design Procedure – Non-inverting

Design goals

- $V_{HYST} = 100 \text{ mV}$
- $V_{ref} = 2.5 \text{ V}$



Select starting values

$$V_{cc} = 5 \text{ V}, \quad V_{O(\max)} = 5 \text{ V}, \quad V_{O(\min)} = 50 \text{ mV}$$

$$R1 = 100 \text{ k}, \quad R_{pull-up} = 10 \text{ k}$$

Calculate remaining values

$$R2 = \frac{R1}{\left(\frac{V_{cc}}{V_{ref}} - 1\right)} = \frac{100 \text{ k}}{\left(\frac{5.0}{2.5} - 1\right)} = 100 \text{ k}$$

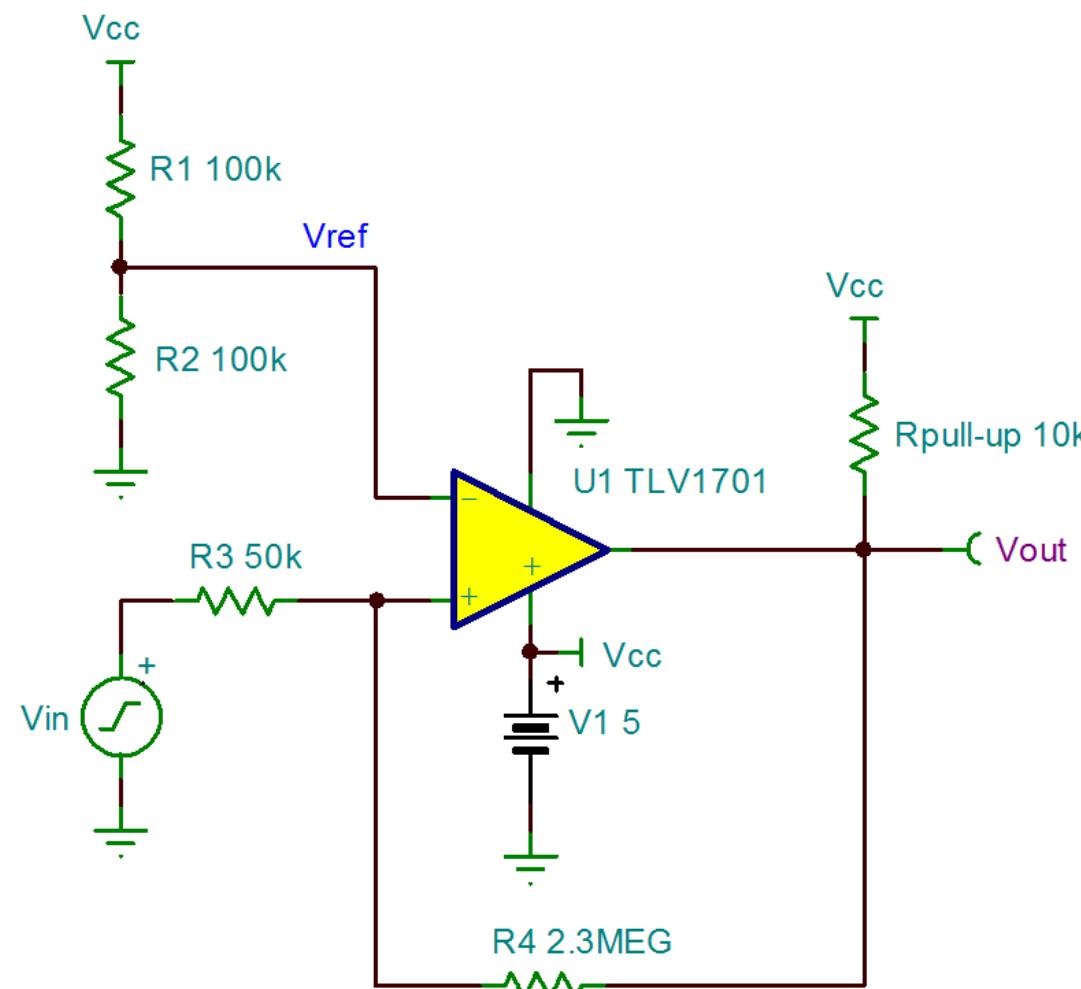
$$R3 = \frac{(R1 \cdot R2)}{(R1 + R2)} = \frac{(10^5 \cdot 10^5)}{(10^5 + 10^5)} = 50 \text{ k}$$

$$R4 = R3 \left[\frac{(V_{O(\max)} - V_{O(\min)})}{V_{HYST}} - 1 \right] = 5 \cdot 10^4 \left[\frac{(5.0 - 0.05)}{0.10} - 1 \right]$$

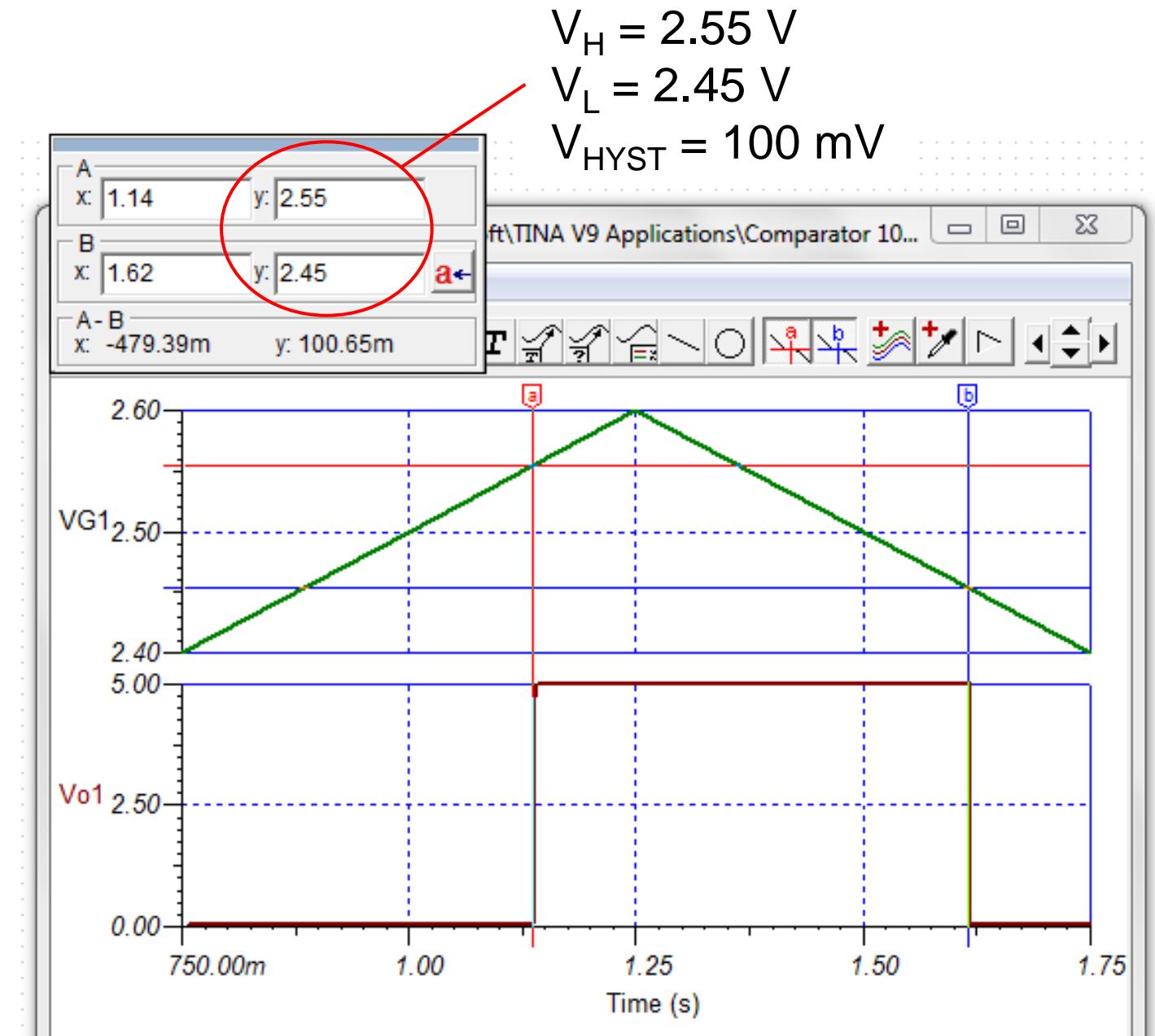
$$R4 = 2.43 \text{ MEG}$$

Make sure $R_{pull-up}$ is less than 1/10th the value of R4!

Hysteresis Verification with Simulation



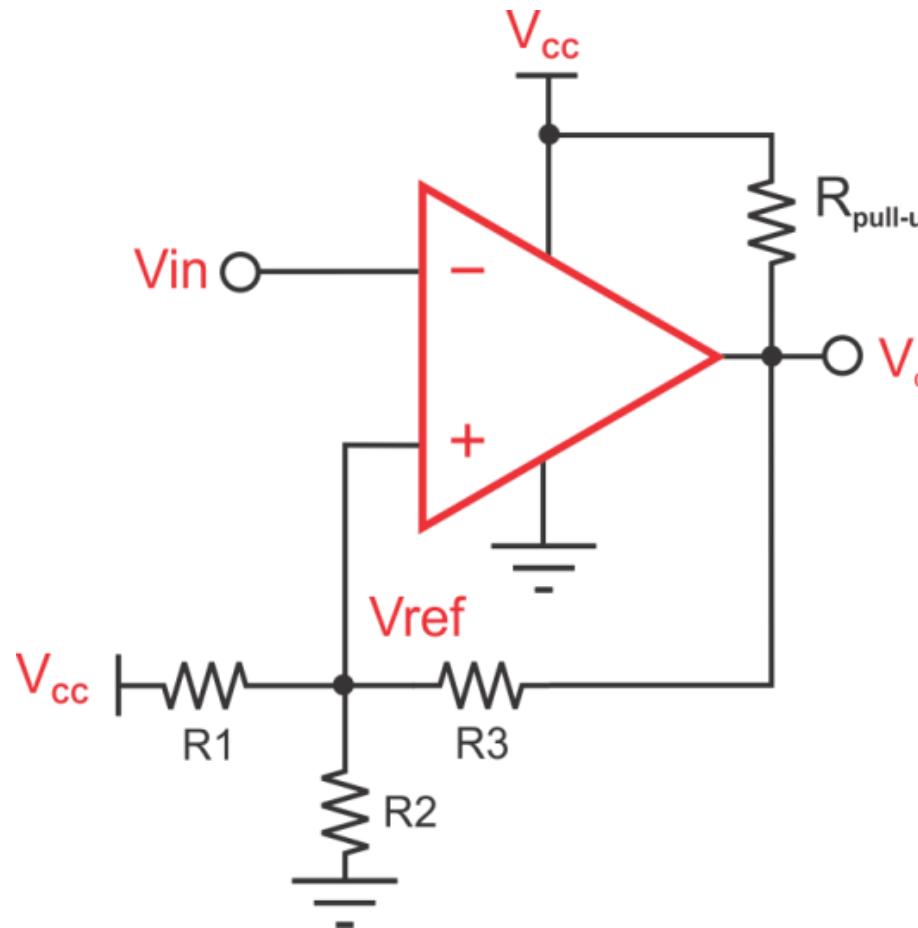
- Hysteresis accuracy improves when $R_{\text{pull-up}} < 0.1 * R_4$



Hysteresis Design Procedure – Inverting

Design goals

- $V_{HYST} = 50 \text{ mV}$
- $V_{ref} = 2.5 \text{ V}$



Select starting values

$$V_{cc} = 5 \text{ V}, \quad V_{O(\max)} = 5.0 \text{ V}, \quad V_{O(\min)} = 0.05 \text{ V}$$

$$R_1 = 10 \text{ k}, \quad R_{\text{pull-up}} = 10 \text{ k}$$

Calculate remaining values

$$R_2 = \frac{R_1}{\left(\frac{V_{cc}}{V_{ref}} - 1\right)} = \frac{10 \text{ k}}{\left(\frac{5.0}{2.5} - 1\right)} = 10 \text{ k}$$

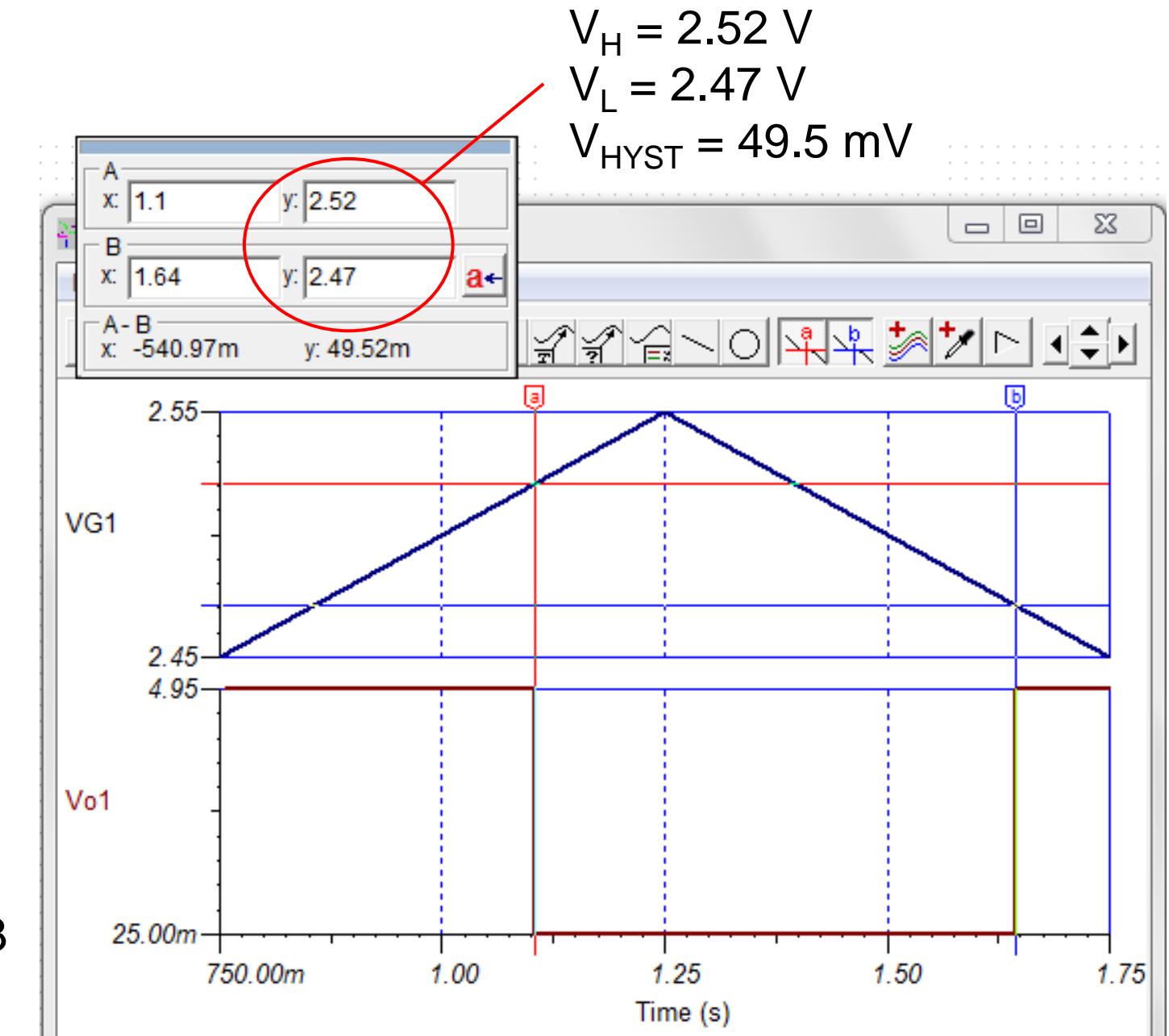
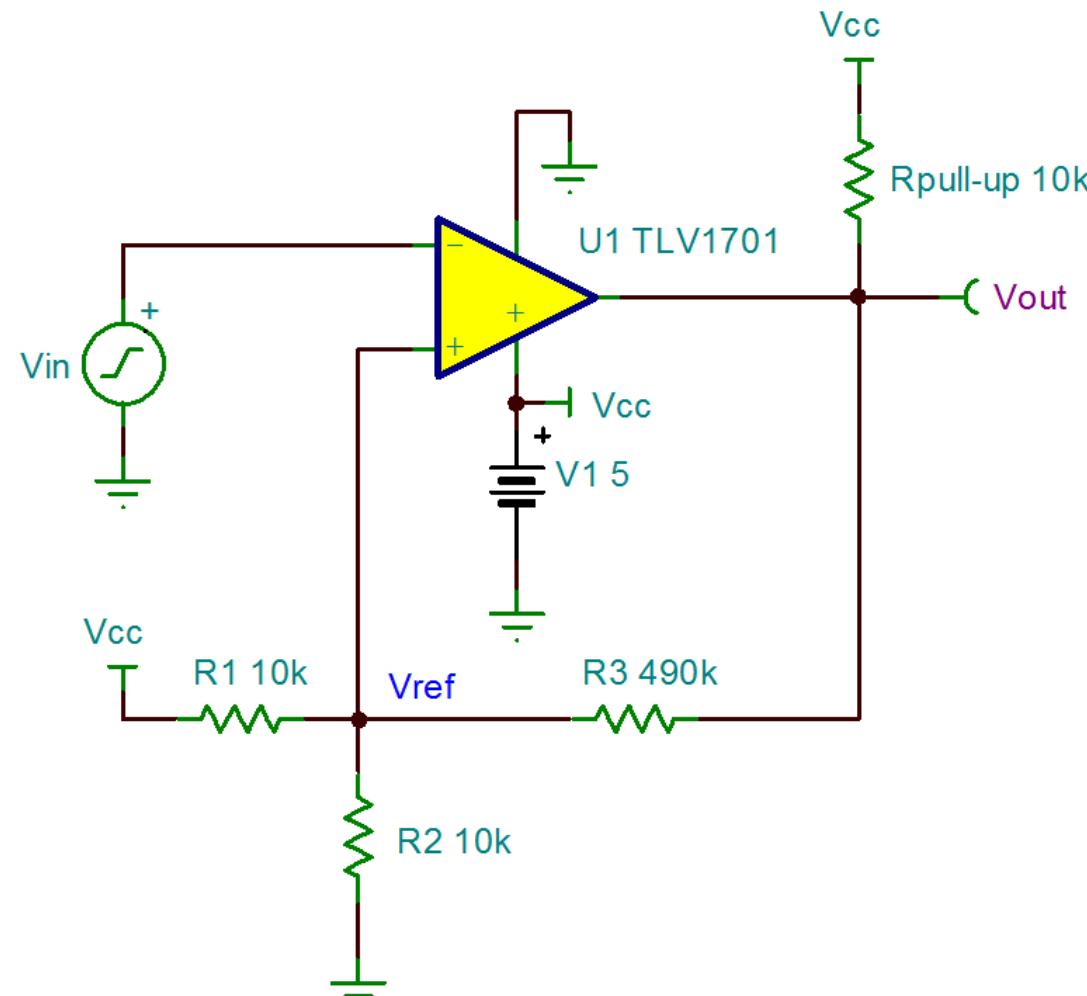
$$R_3 = \frac{(R_1 \cdot R_2)}{(R_1 + R_2)} \left[\frac{(V_{O(\max)} - V_{O(\min)})}{V_{HYST}} - 1 \right]$$

$$R_3 = \frac{(10^4 \cdot 10^4)}{(10^4 + 10^4)} \left[\frac{(5.0 - 0.05)}{0.05} - 1 \right] = 490 \text{ k}$$

Make sure $R_{\text{pull-up}}$ is less than 1/10th the value of R_3 !

Note that equations
are different from
previous non-inverting!!

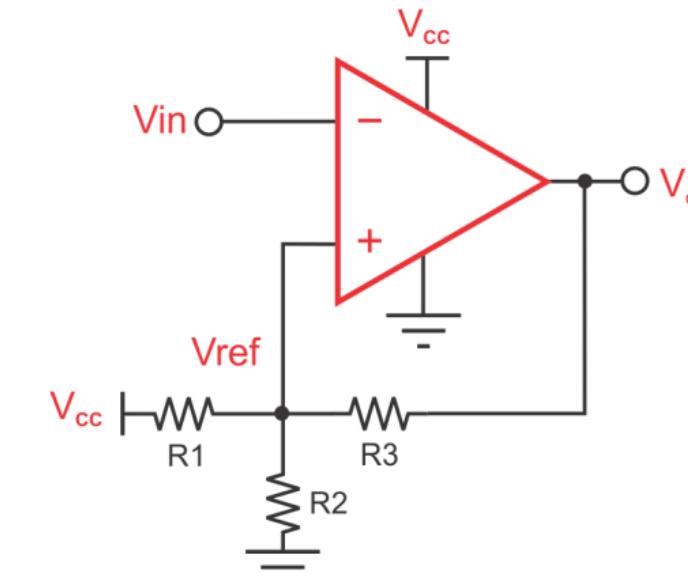
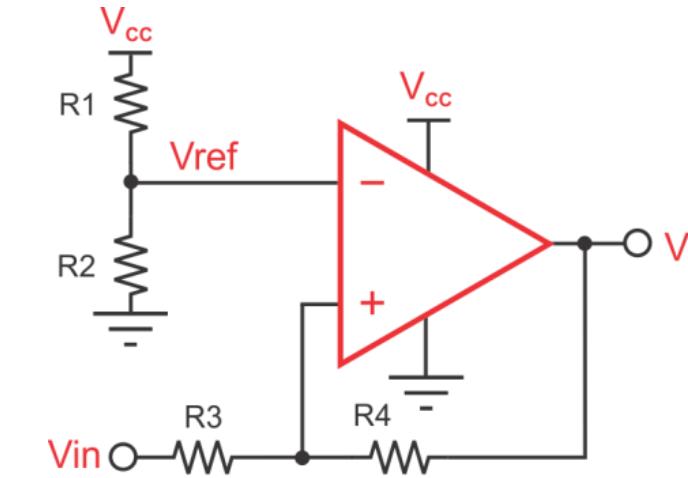
Hysteresis Verification with Simulation



- Hysteresis accuracy improves with $R_{\text{pull-up}} < 0.1 * R_3$
- Equations can be used for push-pull output too!

Hysteresis Design Procedure – Summary

- The resistor values affect either the reference voltage and/or the V_H and V_L levels. Their tolerances are an important factor in hysteresis accuracy.
- The feedback resistor is usually very high in value compared to the others. Its loading on the non-inverting input divider is usually minimal.
- Keeping $R_{\text{pull-up}}$ less than 10 % of the feedback resistor value assures more accurate V_H and V_L voltage levels.
- The inverting and non-inverting equations can also be used for push-pull output comparators if:
 - Remove $R_{\text{pull-up}}$ from the circuit
 - Use the datasheet *Output Voltage vs Output Current* curves to establish $V_{O(\max)}$ and $V_{O(\min)}$ from the V_{OH} and V_{OL} levels



Push-pull comparators with hysteresis applied