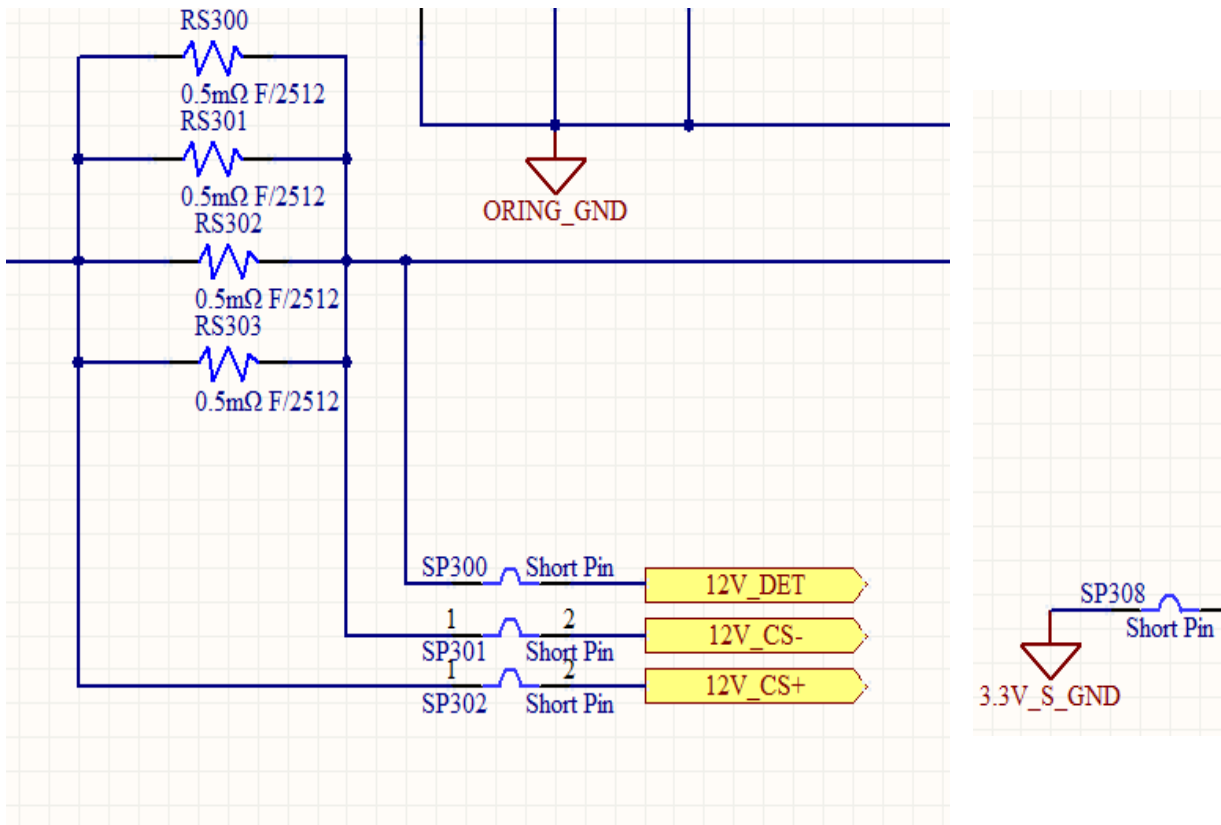


1、 Question

CFF800V5TT project is using IC INA214AIDCKR as CS, the output delay is different under different inp

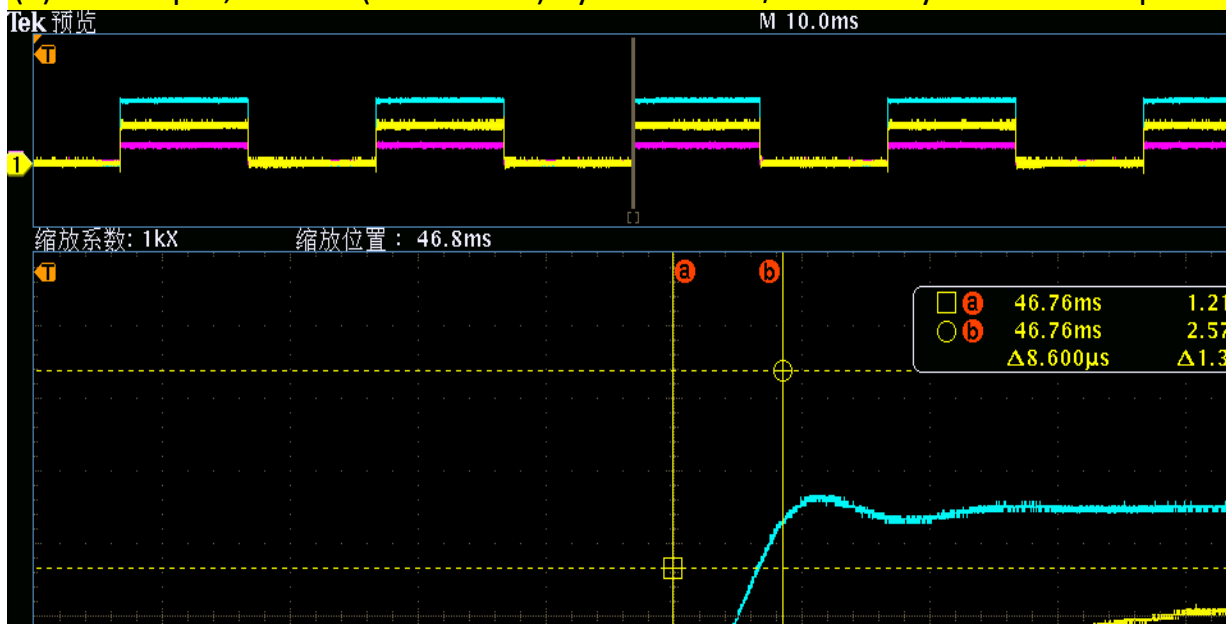
2、 schematic

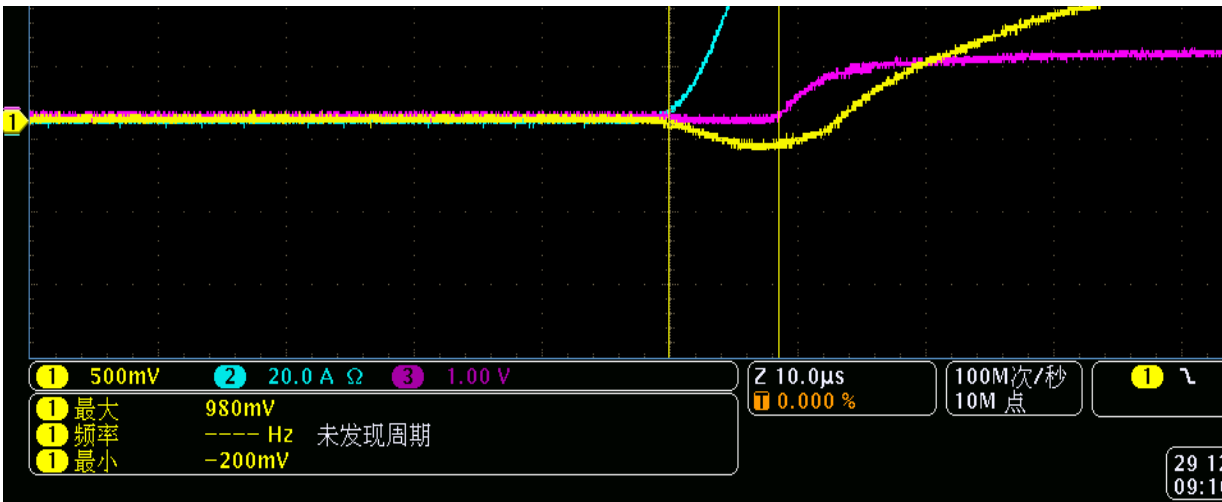


3、 Scope

(1)The tested waveform under different dynamic load test set-up. slew rate is

(a)12V output, 0-65.6A(100% Load) dynamic load, the delay between output cu

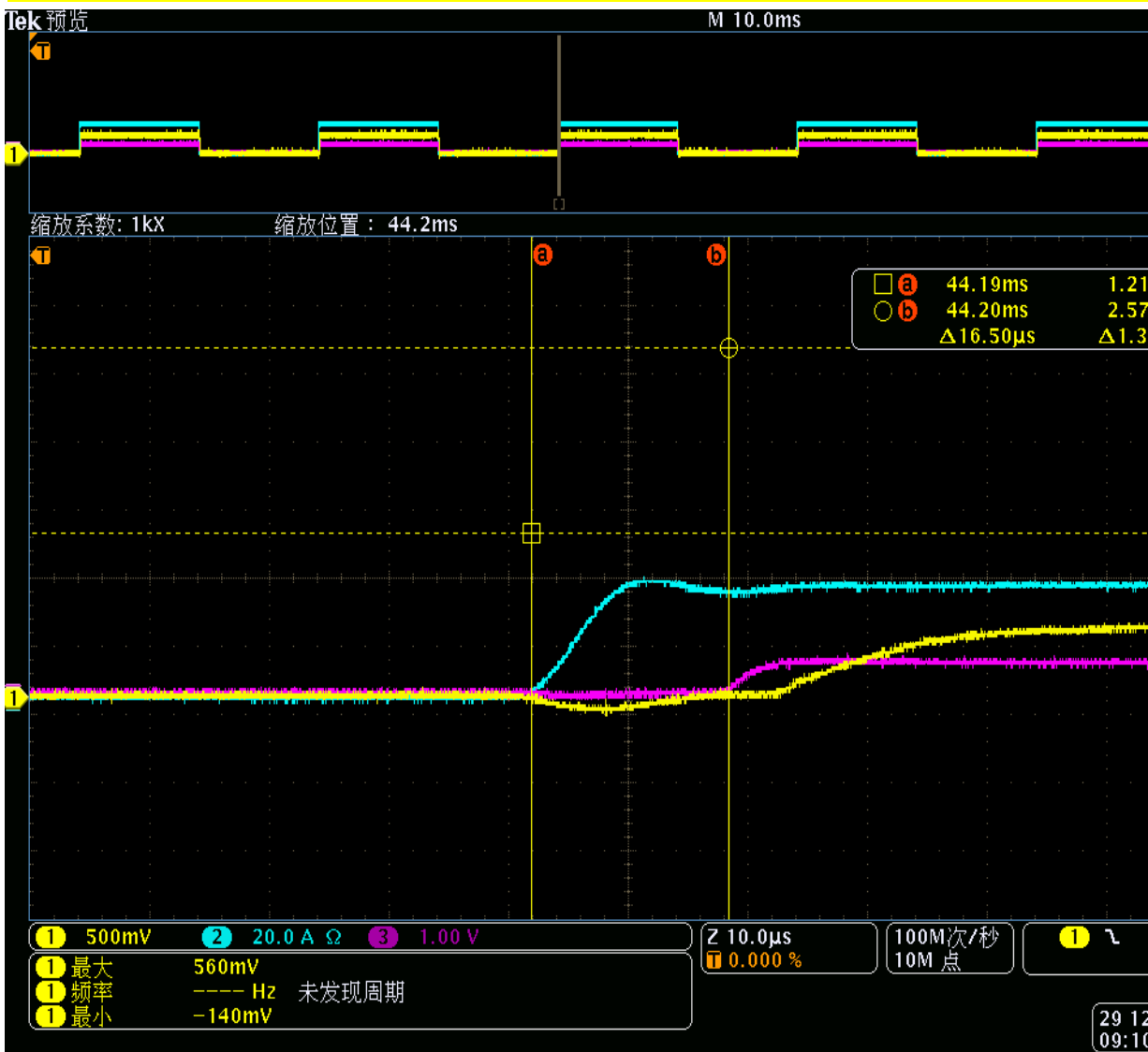




CH2:lout

CH3: 12VCS_OUT

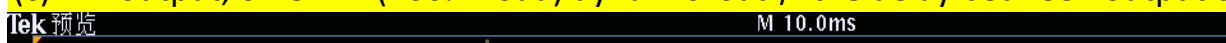
(b) 12V output, 0-32.8A(100% Load) dynamic load, the delay between output c

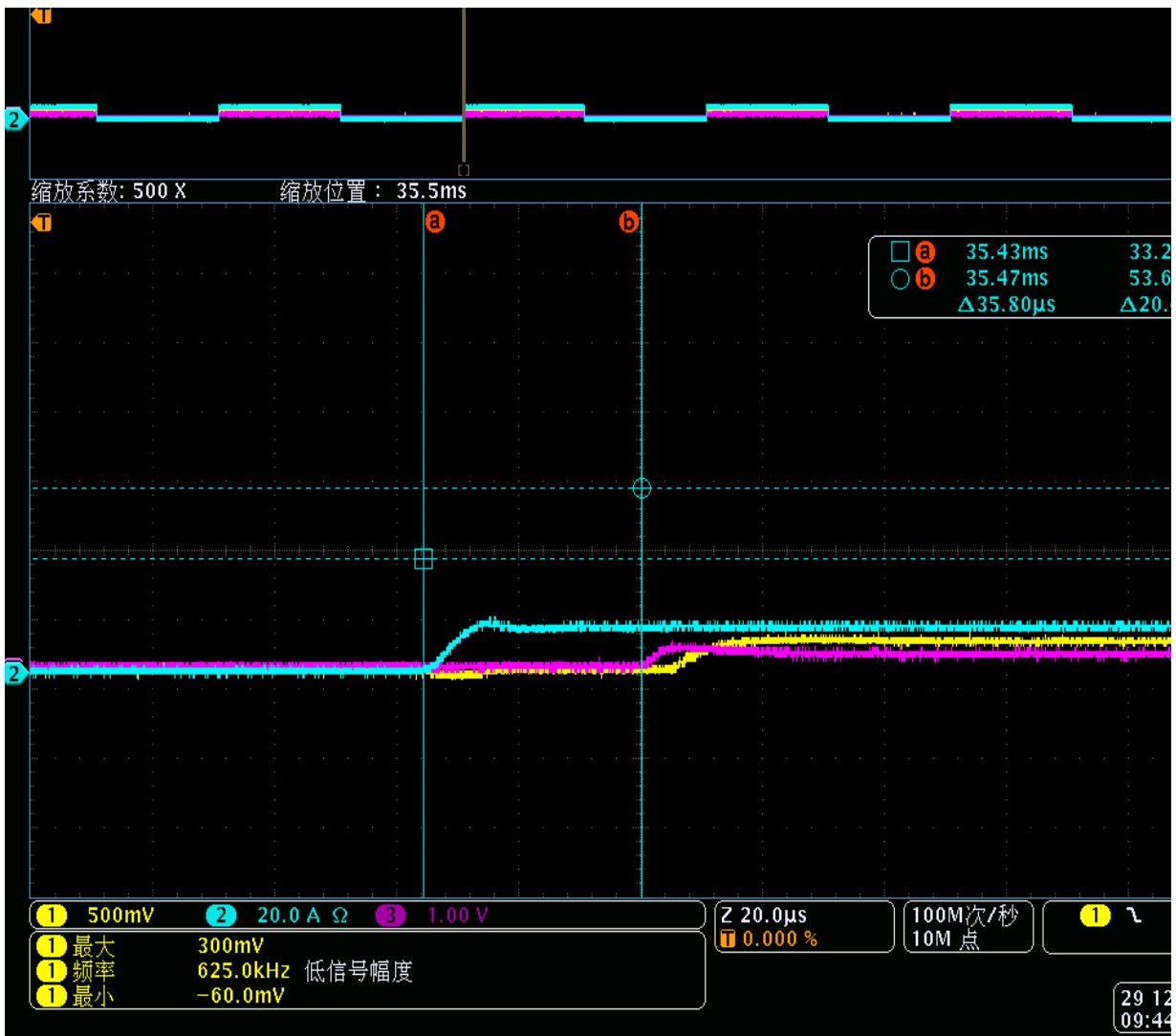


CH2:lout

CH3: 12VCS_OUT

(c) 12V output, 0-13.12A(100% Load) dynamic load, the delay between output c



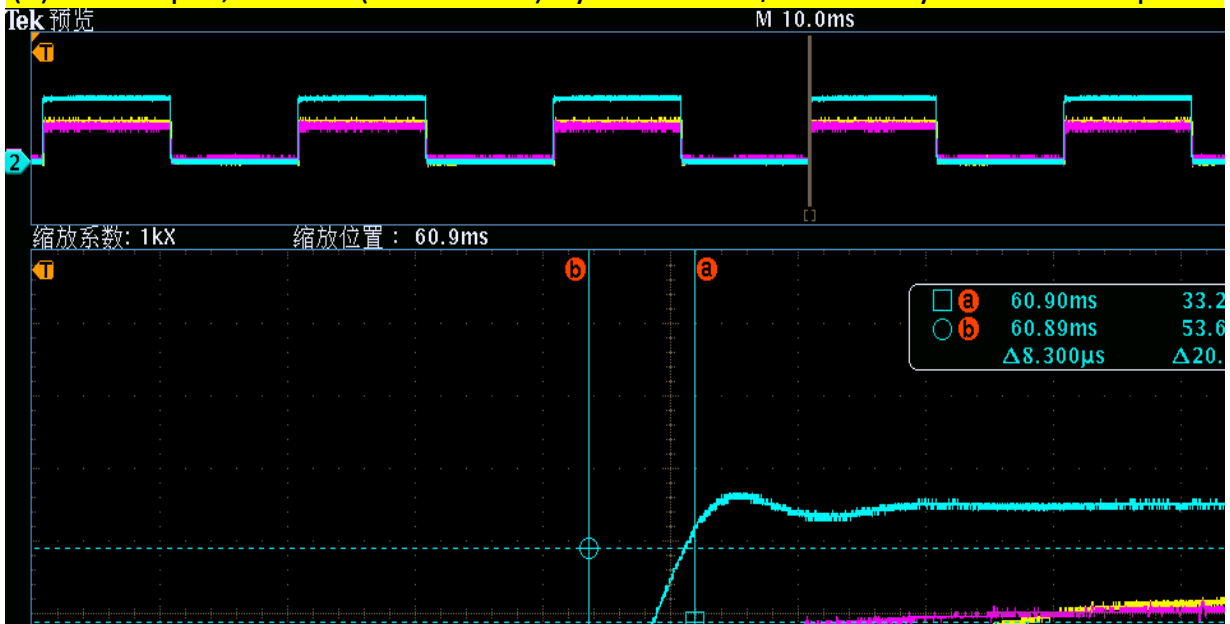


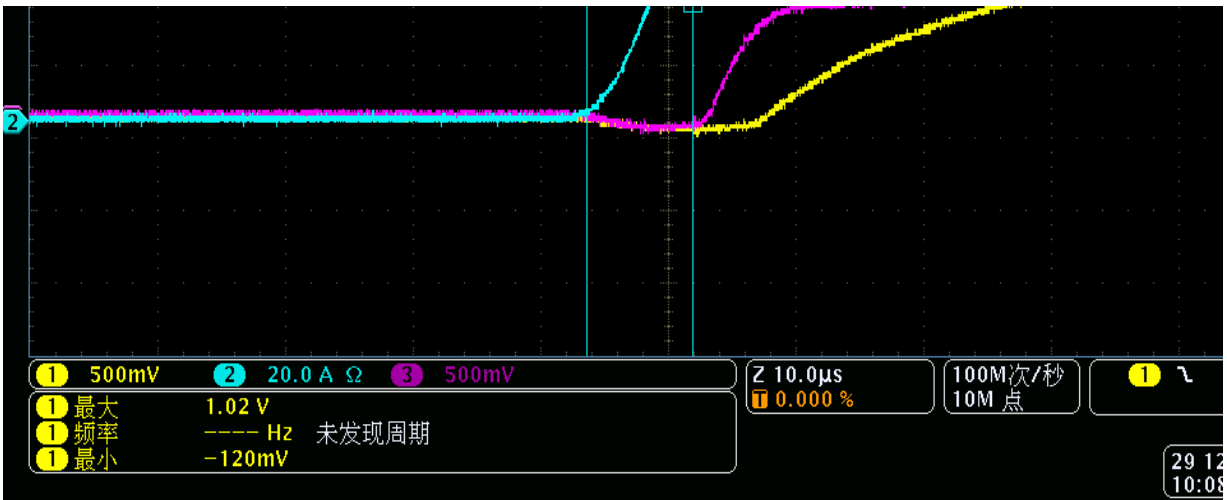
CH2:lout CH3: 12VCS_OUT

(2)Removed C304, the waveform under different test set-up

Removed INA214AIDCKR input filter cap C304

(a)12V output, 0-65.6A(100% Load) dynamic load, the delay between output cu

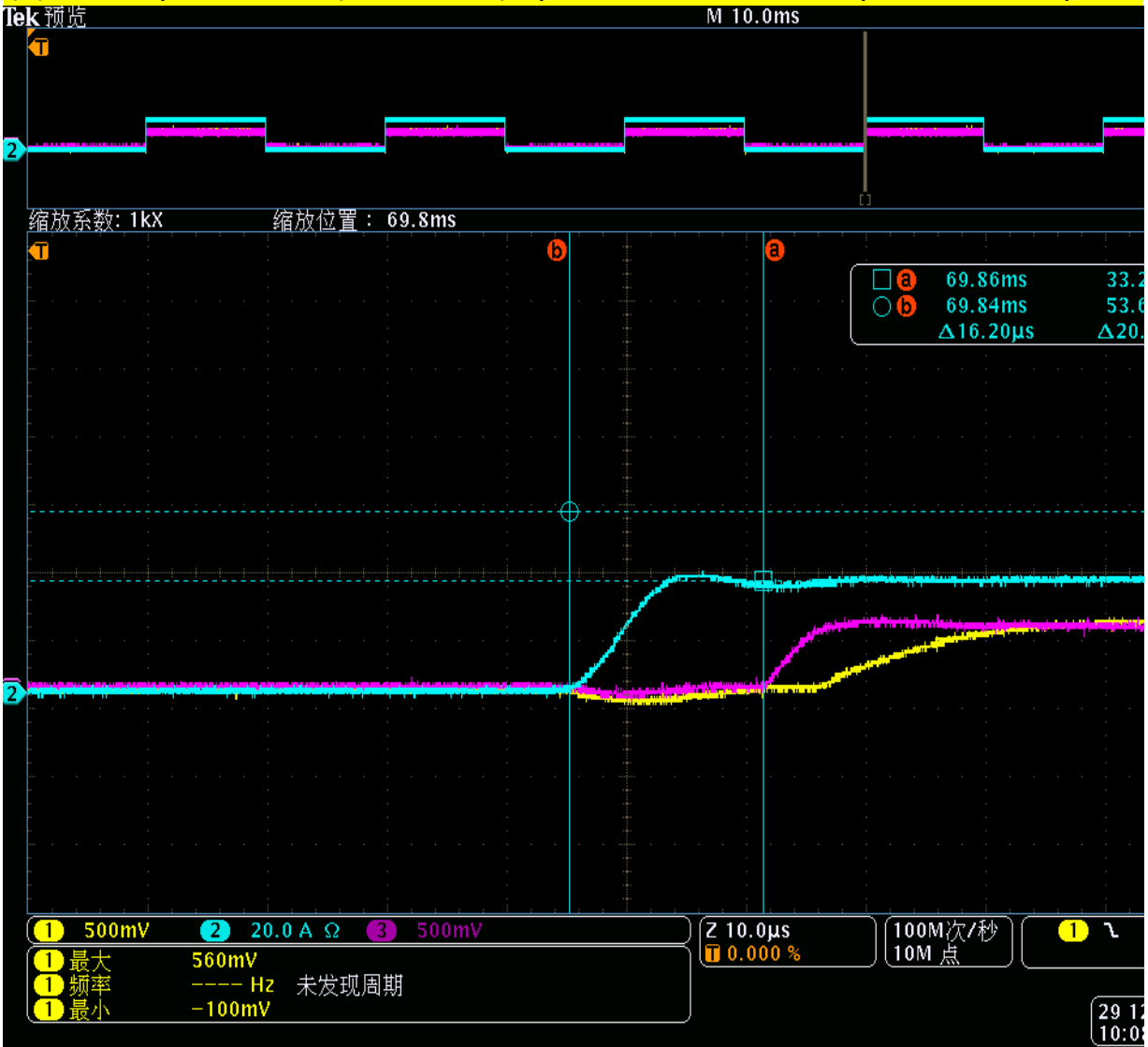




CH2:lout

CH3: 12VCS_OUT

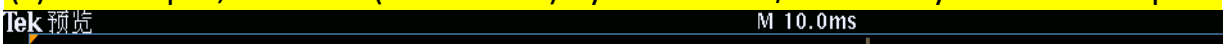
(b)12V output, 0-32.8A(100% Load) dynamic load, the delay between output c

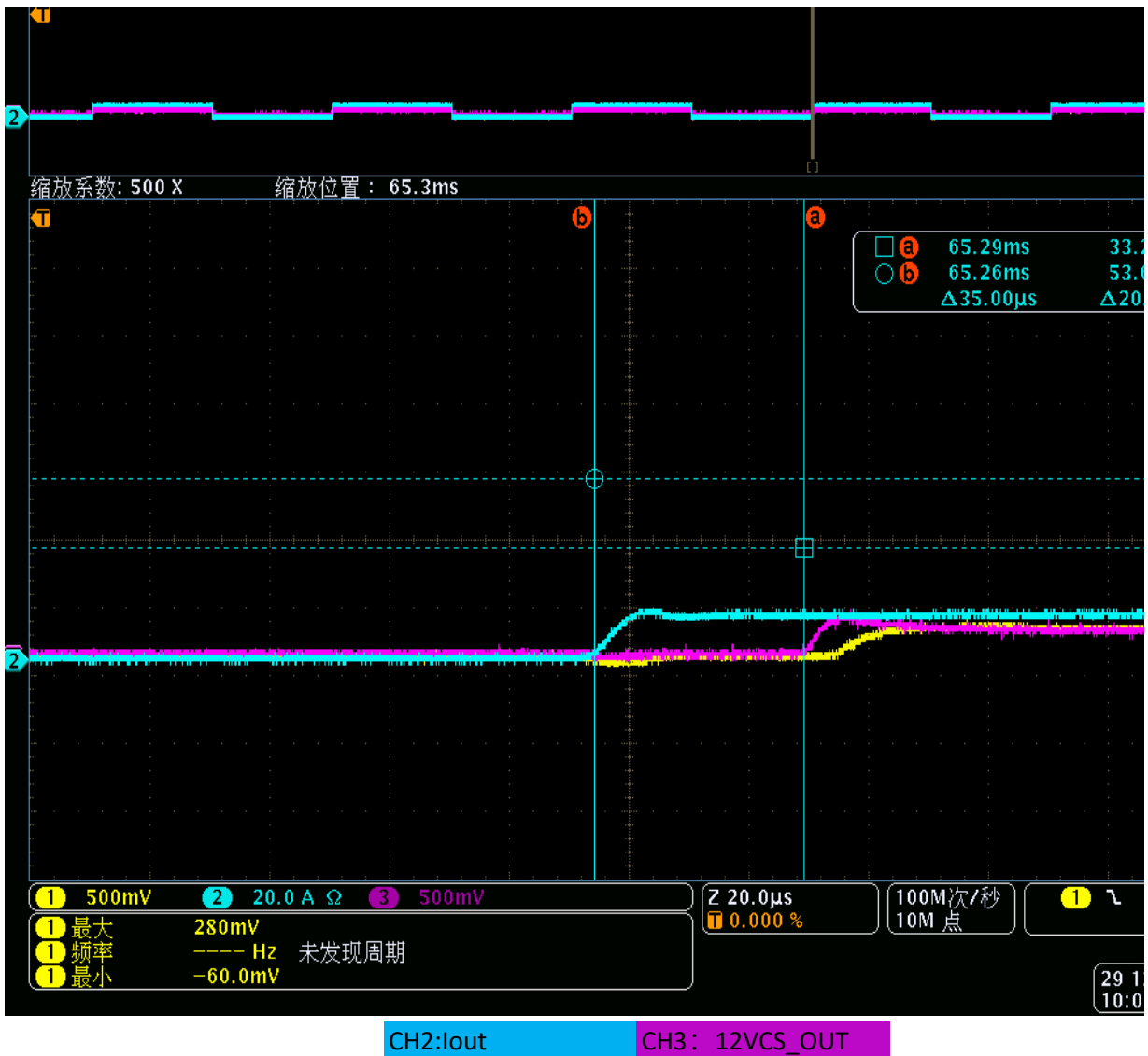


CH2:lout

CH3: 12VCS_OUT

(c)12V output, 0-13.12A(100% Load) dynamic load, the delay between output c





Conclusion: C304 will not impact the delay. The delay under different dynamic

4, Question

why the dealy of INA214AIDCKR is defferent under different dynamic load.

