

◆ Circuit

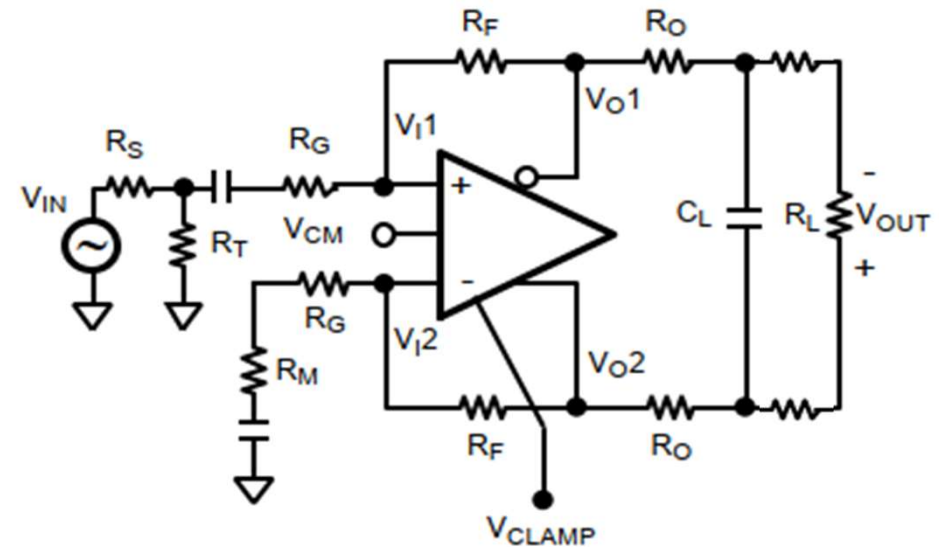
SINGLE SUPPLY OPERATION

$V_+ = 5V$, $V_- = 0V(\text{GND})$

$V_{cm} = 2.5V$

$V_{clamp} = 3.25V$

Tested with an recommended circuit configuration. ⇒
(figure58 of the device data sheet)

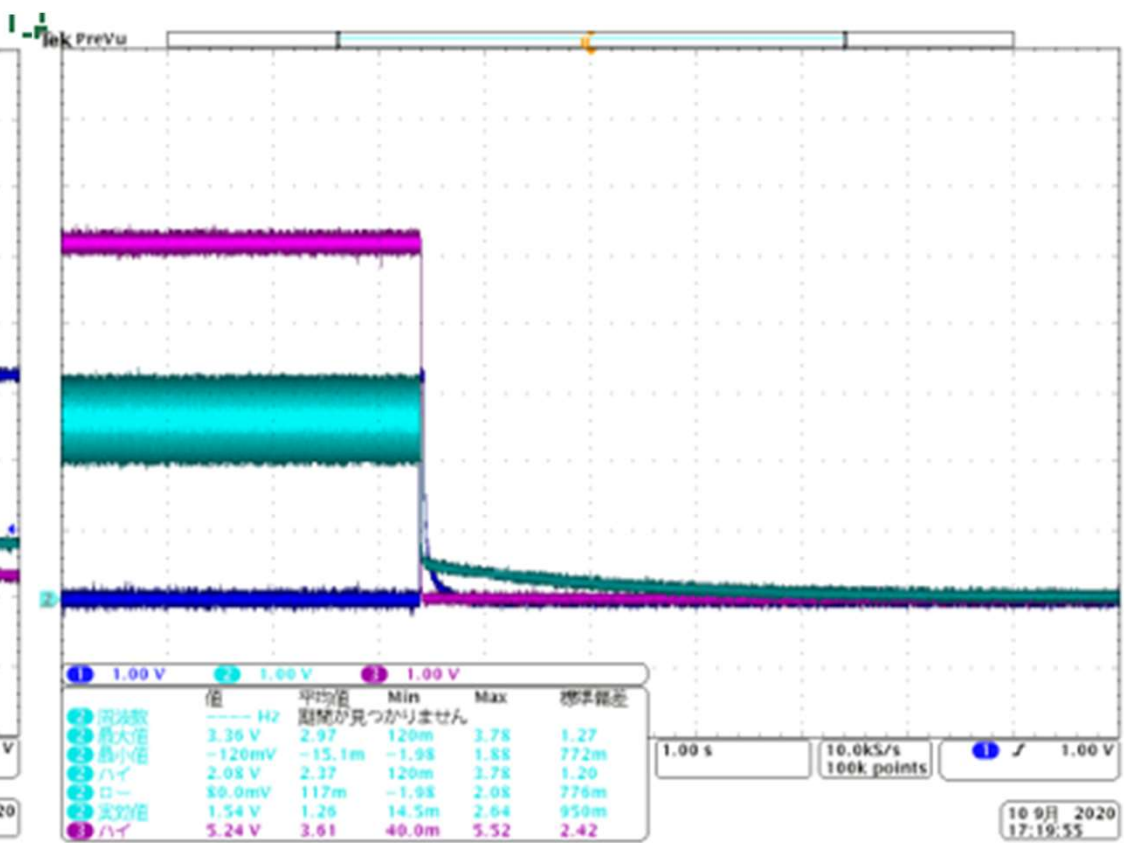
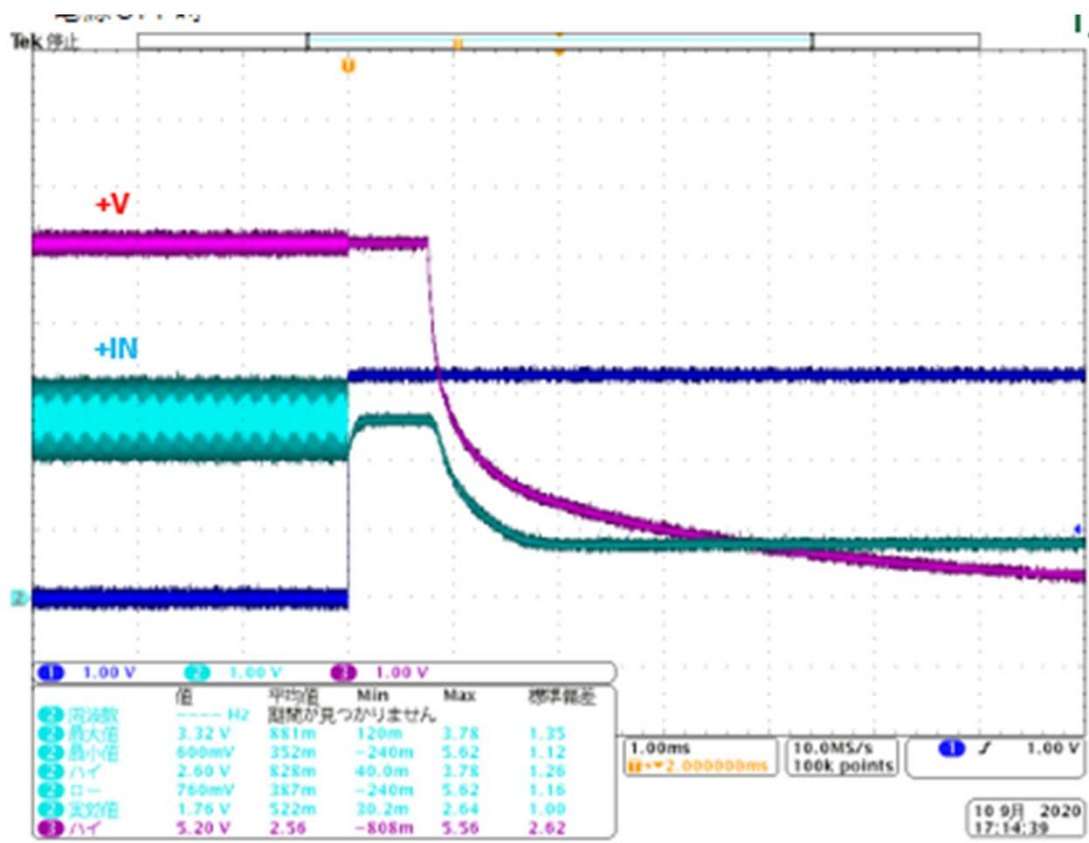


"Vi" does not become 0V even when the power is turned off. (even if V_+ is 0V)

Could you refer to the next page for the measured waveforms?

◆ Question

- Could you tell me the reason why the voltage@Vi doesn't reach 0.0V? (About 0.8V is applied.)
- Could you tell me the possible failure modes due to the remaining voltage?
- would you tell me the reason that If there is no problem to remain voltage?



1.00ms/div
ch3 : V+
ch2 : Vi+

1.00s/div
ch3 : V+
ch2 : Vi+