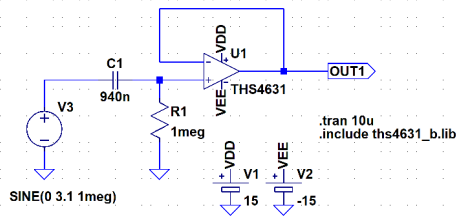


■ Reference Information

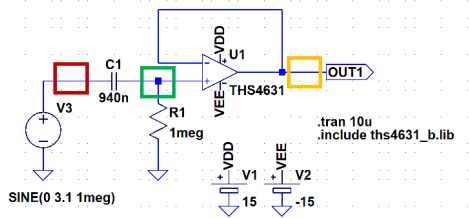
● Circuit Description

- The output of the signal generator passes through the first-order HPF (cutoff frequency: about 0.2 Hz) consisting of C1 and R1, and is input to the buffer amplifier U1.
- From the signal generator, input a sine wave(2.2Vrms, DC offset voltage 0V, frequency 1kHz or 1MHz).



● Behaving differently than expected

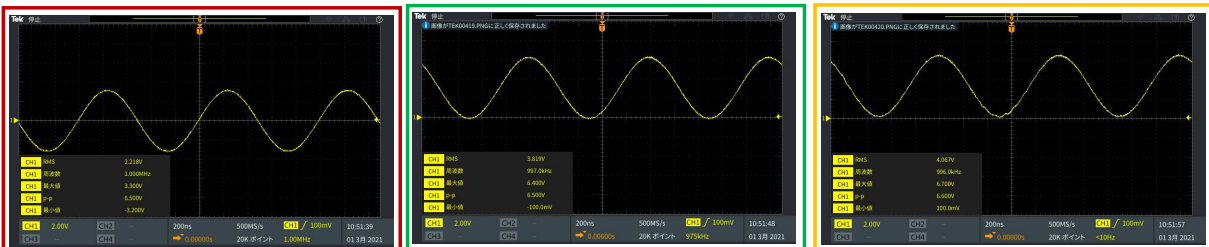
- I have checked the operation of the above circuit on a board made based on the circuit diagram above.
- When the frequency of the input signal was 1 kHz, there was no superimposition of DC offset voltage at each part of the circuit.
- When the frequency of the input signal is 1MHz, a DC offset voltage of about 3V is added at the green square of the circuit. At this time, when the DC current in the green square is measured using the DCI function of the DMM, about 3uA is flowing.



Input signal: 2.2Vrms/1kHz/sine wave



Input signal: 2.2Vrms/1MHz/sine wave



the end