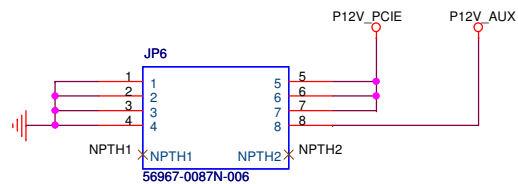
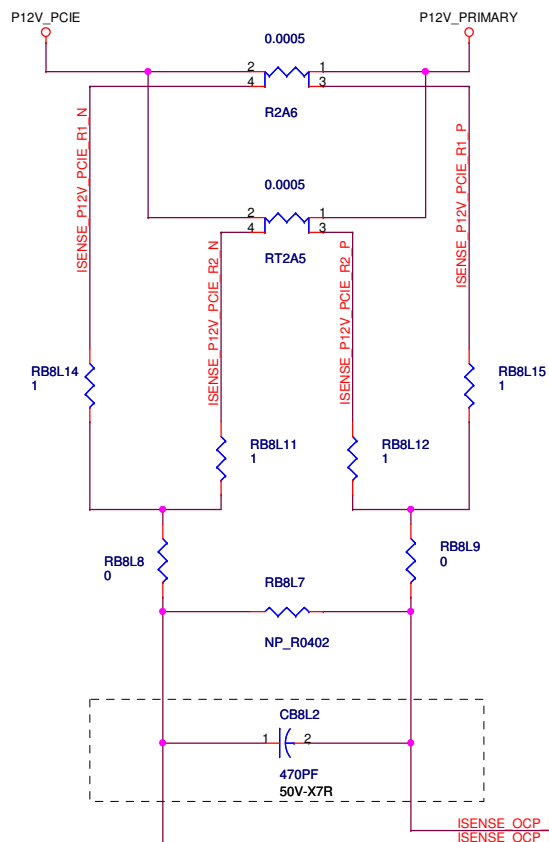


See Intel's reference circuit page 397



CAD NOTE:

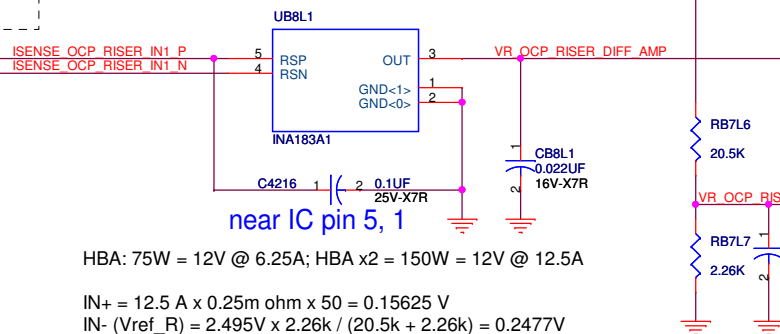
PLACE RESISTORS CLOSE TO
RISER PICPWR CONN

CAD NOTE:

MINIMIZE TRACE DISTANCE ON 2X 1 OHM RESISTOR NODE
CONTROL SAME TRACE RESISTANCE ON P AND N CHANNELS
KEEP TRACE RESISTANCE OF SENSE P < 1 OHM

CAD NOTE:

PLACE 1 OHM RESISTOR CLOSE TO DE SHUNT
USE KELVIN CONNECTION ON ALL SENSE LINES



near IC pin 5, 1

HBA: 75W = 12V @ 6.25A; HBA x2 = 150W = 12V @ 12.5A

IN+ = 12.5 A x 0.25m ohm x 50 = 0.15625 V

IN- (Vref_R) = 2.495V x 2.26k / (20.5k + 2.26k) = 0.2477V

SHDN (normal): (IN+) < (IN-) => Low

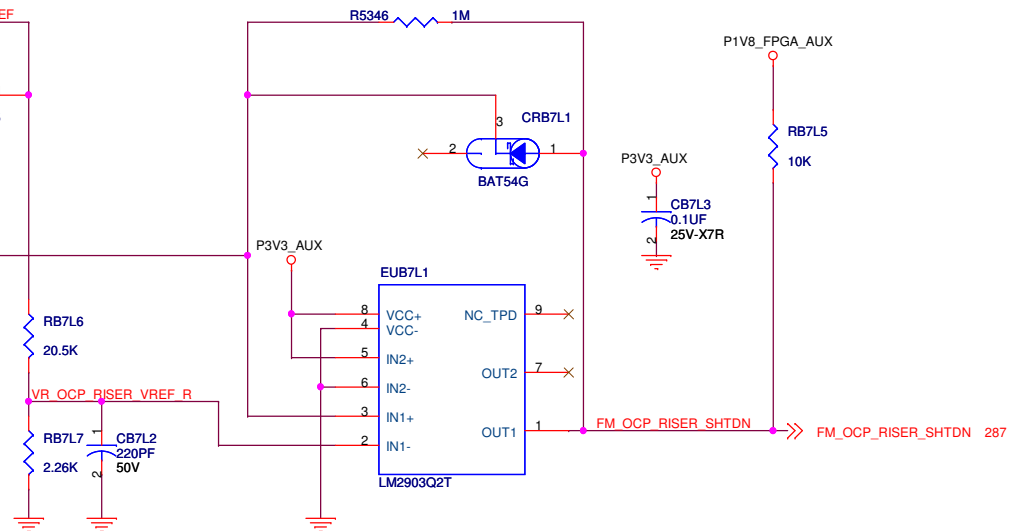
SHDN (abnormal): Example HBA x2 = 20A

IN+ = 20 x 0.25m ohm x 50 = 0.25V

SHDN (abnormal): (IN+) > (IN-) => High

INA183A1: 50V/V; Vsense = 0.124V / (50V/V) = 4.96mV

I(trip) = 4.96mV / (0.5m ohm // 0.5m ohm) = 4.96 / 0.25 = 19.84 A



Confidential Level:	
(1-- Mainboard; 2-- Others)	
Draw.:	<Variant Name>