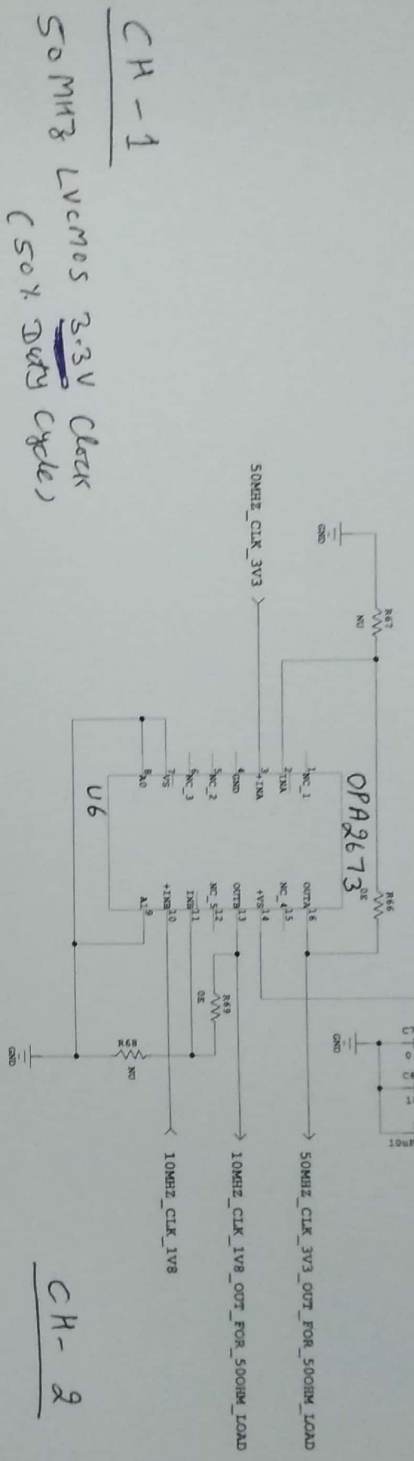
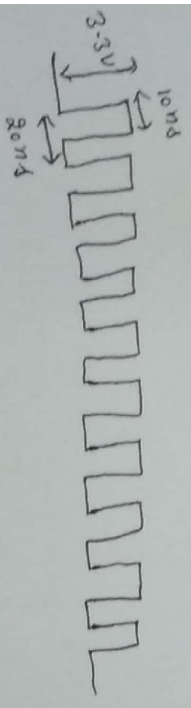


OpAMP operating at single supply 1.8V.

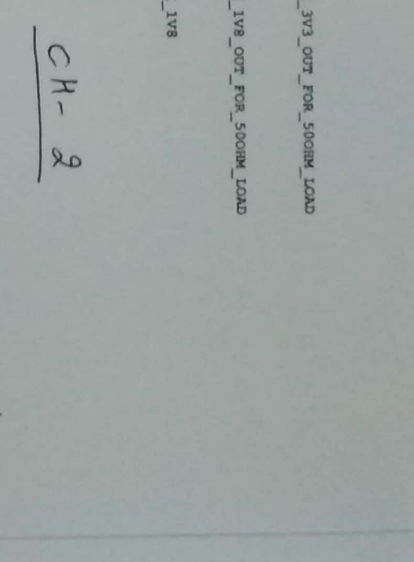


CH-1

I/P  $\Rightarrow$  50MHz LVCMOS 3.3V Clock  
(50% Duty Cycle)

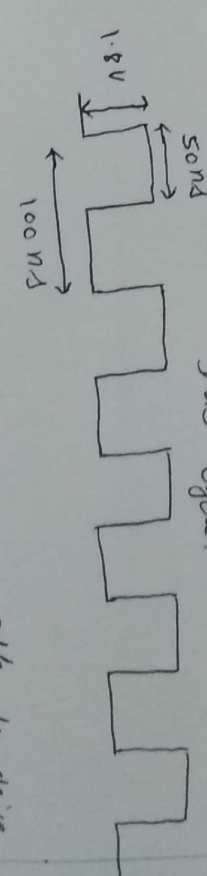


O/P : Output should be able to drive 50 ohm load & level should be LVCMOS 3.3V with 50% Duty cycle clock. We need some o/p AS of I/P.



CH-2

I/P : 10MHz LVCMOS 1.8V Clock with 50% Duty cycle.



O/P : Output should be able to drive 50 ohm load & level should be LVCMOS 1.8V with 50% Duty cycle. We need some o/p AS of I/P.