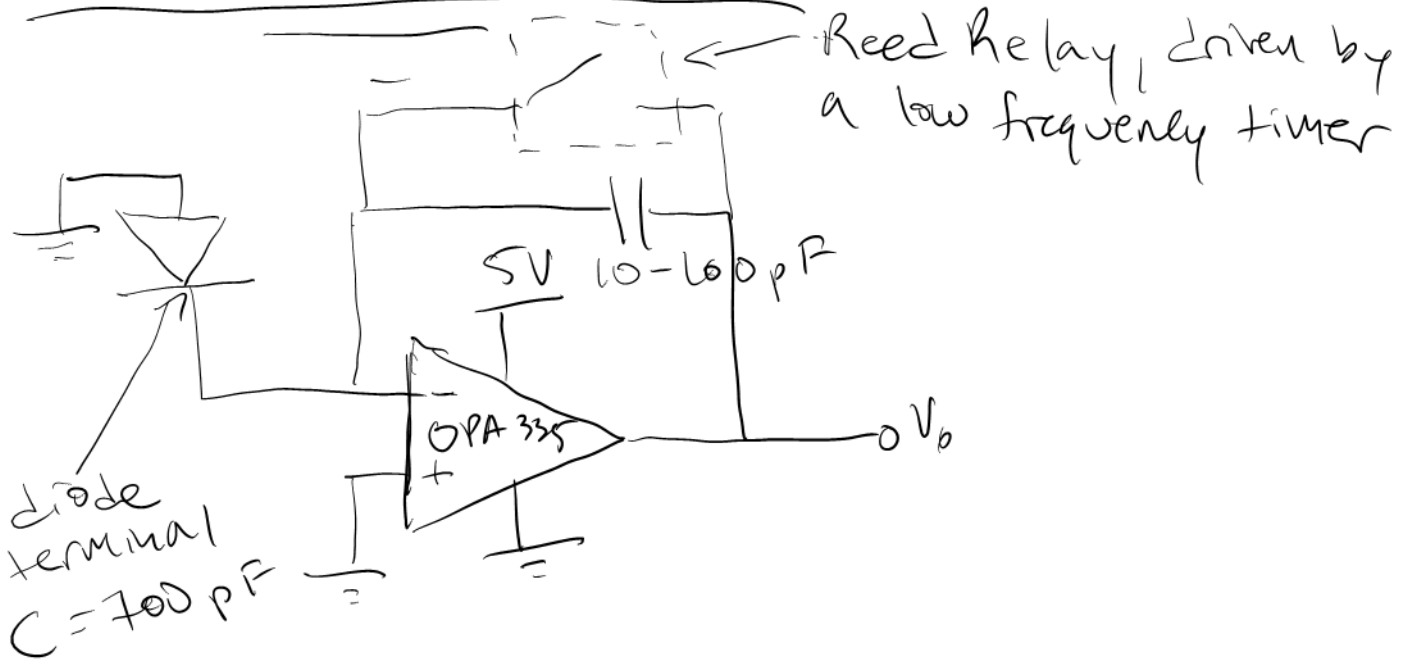


# Circuit Schematic



Rough sketch of Output @ 0 light input

