OPA365 test waveforms

Note: The output waveforms are with no signal and no common mode voltage.

• Output waveform referenced to GND at output GND pad





• C12 GND terminal waveform (reference to the same GND pad as output waveform)

-	KEYS TECHNO			D:	50-X 3054T,	, MY56310	514,04	.08.2016	5071801:	Tue Aug	29 11:10	:50 2023
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• C12 3V terminal waveform (reference to the same GND pad as output waveform)

4	KEYS TECHNO	DLOGIES		D:	50-X 3054T	, MY563	10514, (04.08.2	0160718	301: Tue	Aug	29 11:11	:25 2023
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													-7uV
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测	量菜单								测量	窗口: 自幸	胧择		
+	源 阈值类型: 2 最大电平		[类型: ★由平	添加测量		设置			清除测量值		统计	信息	
			<u>цк.</u>	~~	4		15	and the second se				10 No.	

• Opamp VCC terminal waveform (reference to the same GND pad as output waveform)



• Voltage across 1nF chip MLCC on top of Opamp





After removing this 1nF chip MLCC on top of Opamp.

• Opamp VCC terminal waveform (reference to the same GND pad as output waveform)



Opamp VEE terminal waveform (reference to the same GND pas as output waveform)



Note: The 1nF chip MLCC on top of Opamp was added when I first saw the spike like noise on the supply pin. I knew the power decoupling is not well laid out. After the 1nF capacitor is added, the spike like noise is gone from the voltage at VCC pin. But I had not measured the voltage across the 1nF capacitor. I just noticed this time that the voltage across 1nF capacitor is still not good enough. See below figure for information of this extra decoupling capacitor.



Below picture shows how I measured the voltage across this extra decoupling capacitor.

