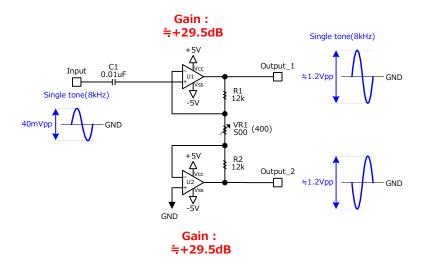
OPA4187IPWR



In the schematic above, if the input level is 40mVpp, is it possible that the output is about 1.2Vpp(about +29.5dB)?

OPA187, OPA2187, OPA4187

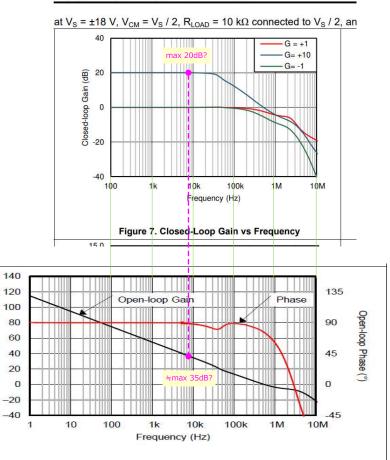
120

20

-20-40

Open-loop Gain (dB)

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I am confused about the loop gain. In the schematic on the left, which is applicable, open loop gain or closed loop gain?

☑ 6. Open-Loop Gain and Phase vs Frequency

max +20dB? or ≒max +35dB?