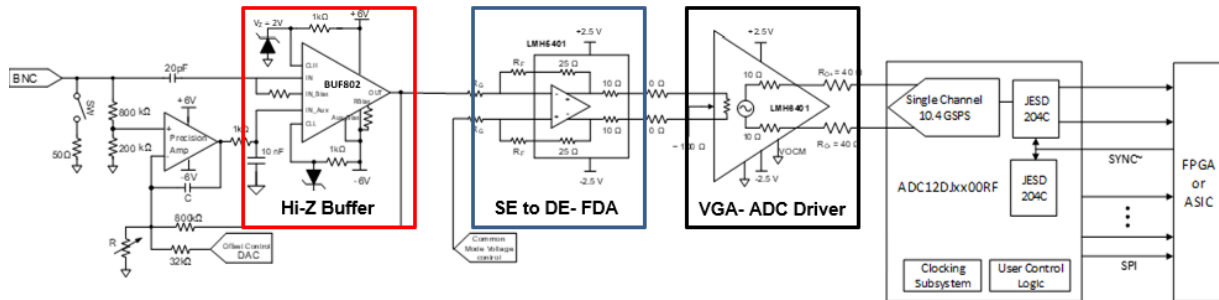




To reliably capture high-frequency signals and fast transient pulses, wide-bandwidth data acquisition systems like oscilloscopes require high-performance analog front-end (AFE) signal chains with an Input Buffer, Single Ended to Differential (SE to DE) conversion & VGA ADC driver that must be able to:

- Support 1 VPP signals (at least) to ensure a high signal-to-noise ratio.
- Support DC to 500 MHz of high input impedance (Hi-Z) to prevent loading of the device under test.
- Offer low noise and distortion to maintain high signal fidelity.
- Deliver high DC precision

Generic >1-2GHz Scope Block Diagram:



Generic 500MHz Scope Block Diagram:

