SonoDAQ-POE

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Schematics DevBoard

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Revision History

初始版本	——>	目标版本	日期	设计者	描述	修改说明

Title: COVER		
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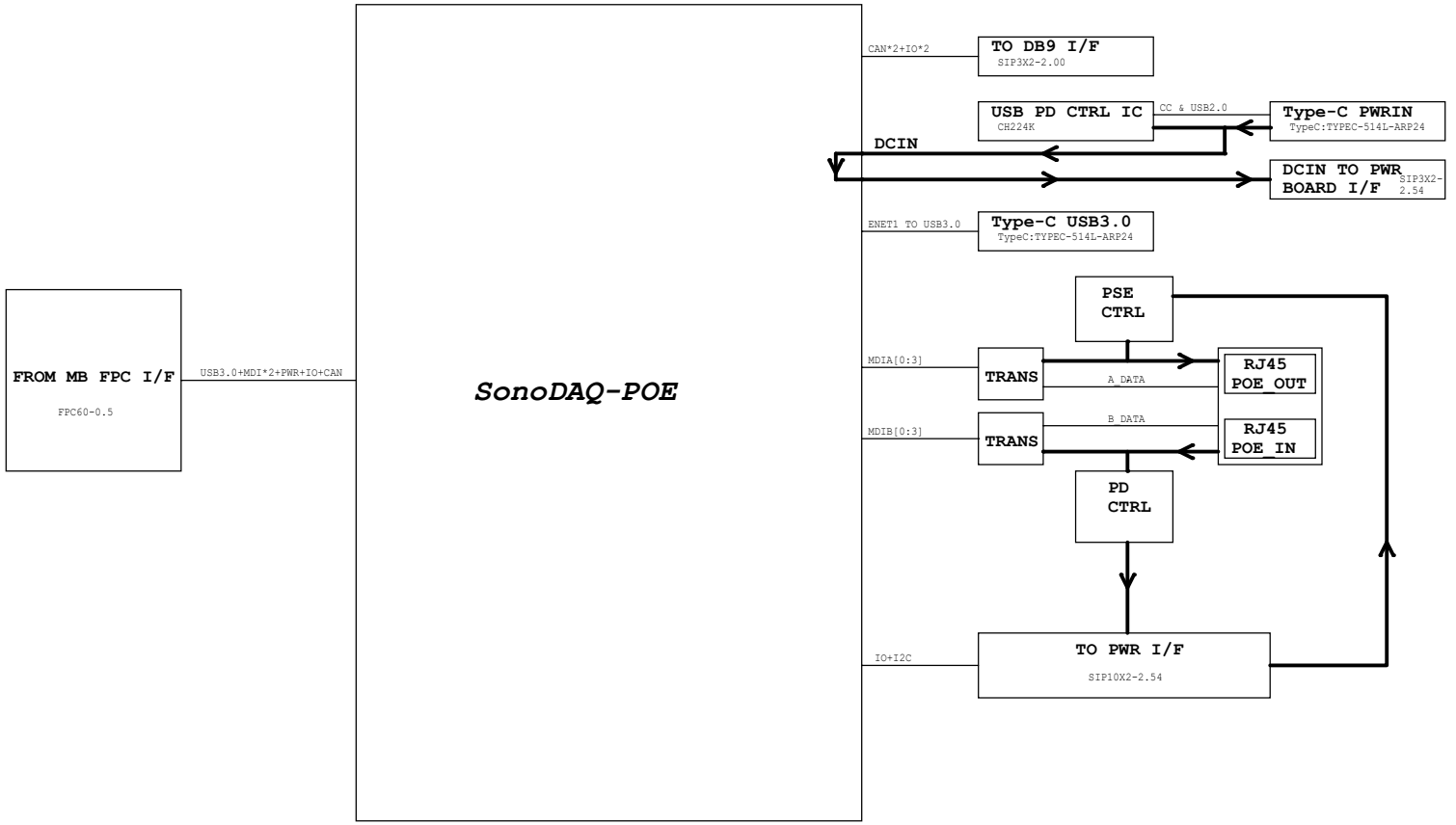
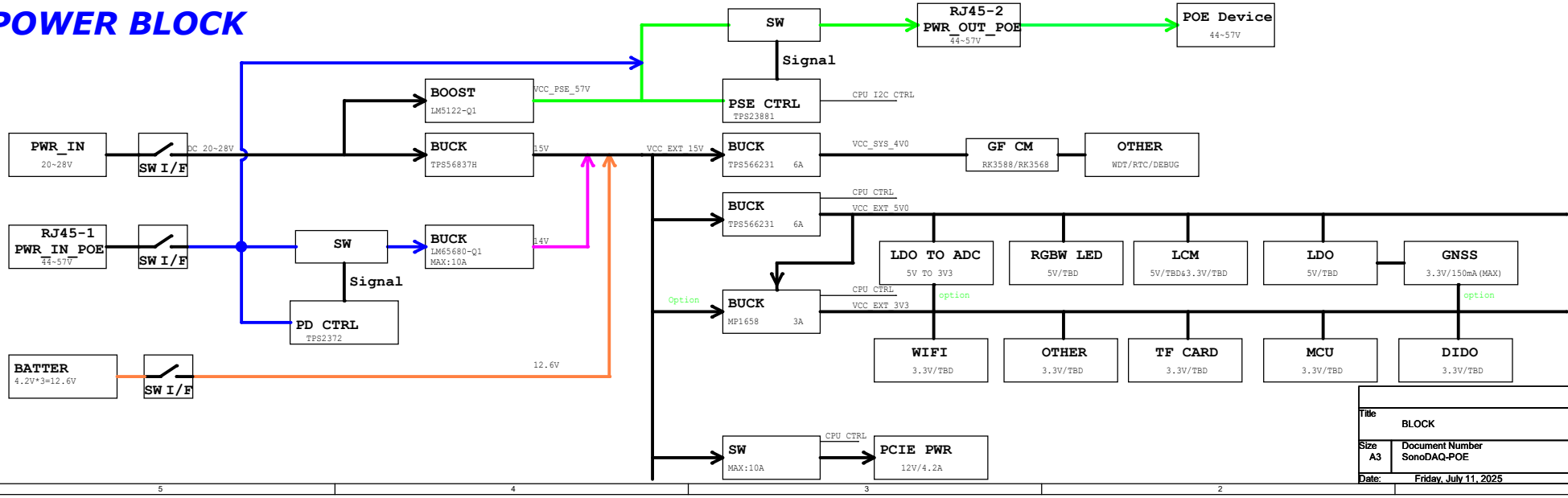
SYS BLOCK



POWER BLOCK



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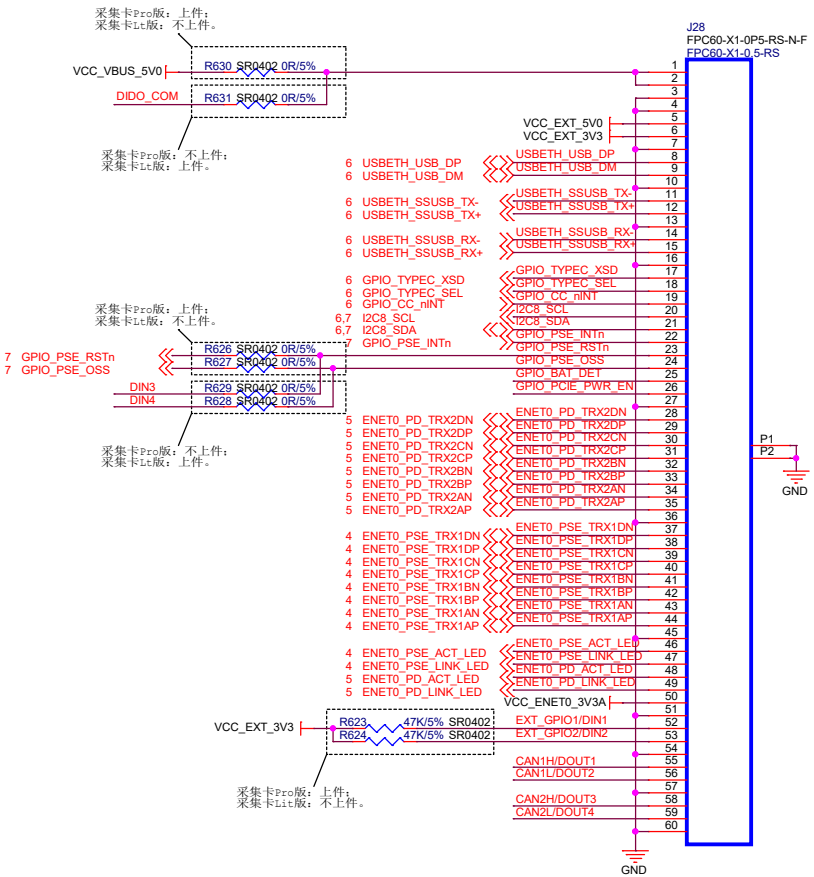
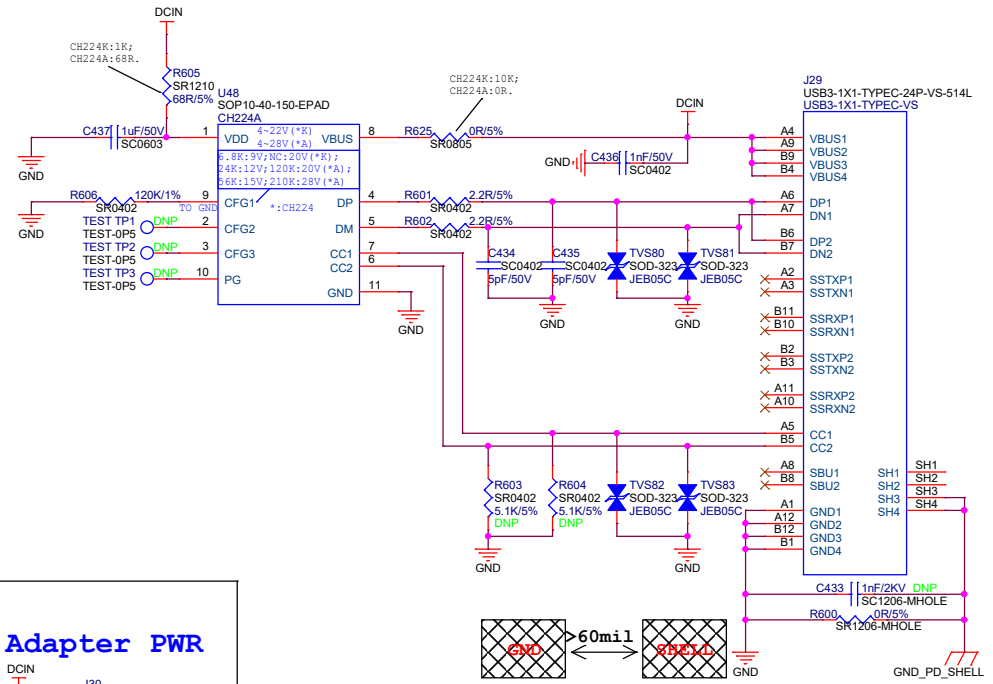
INTERFACE

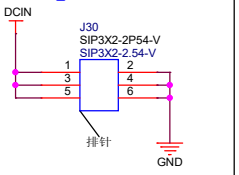
ENET+USB+CTRL



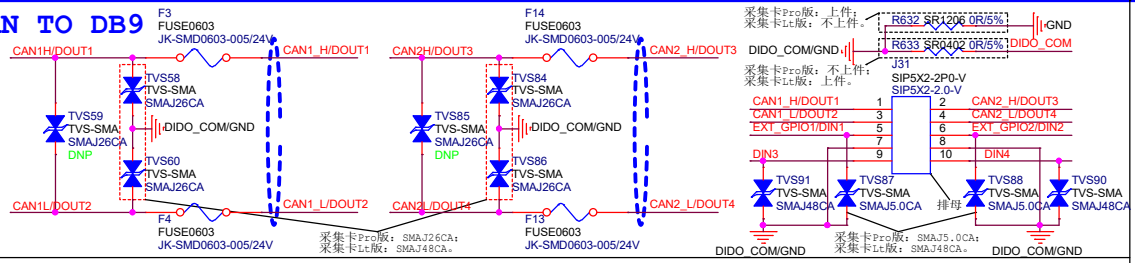
USB PD



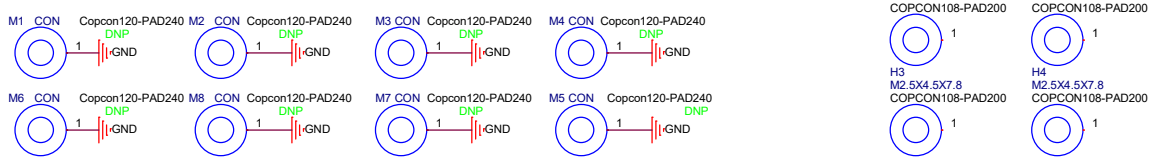
Adapter PWR



CAN TO DB9

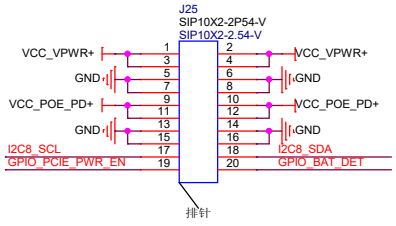


Manufacture Hole

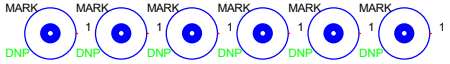


POE PWR IN/OUT

排针



PWR



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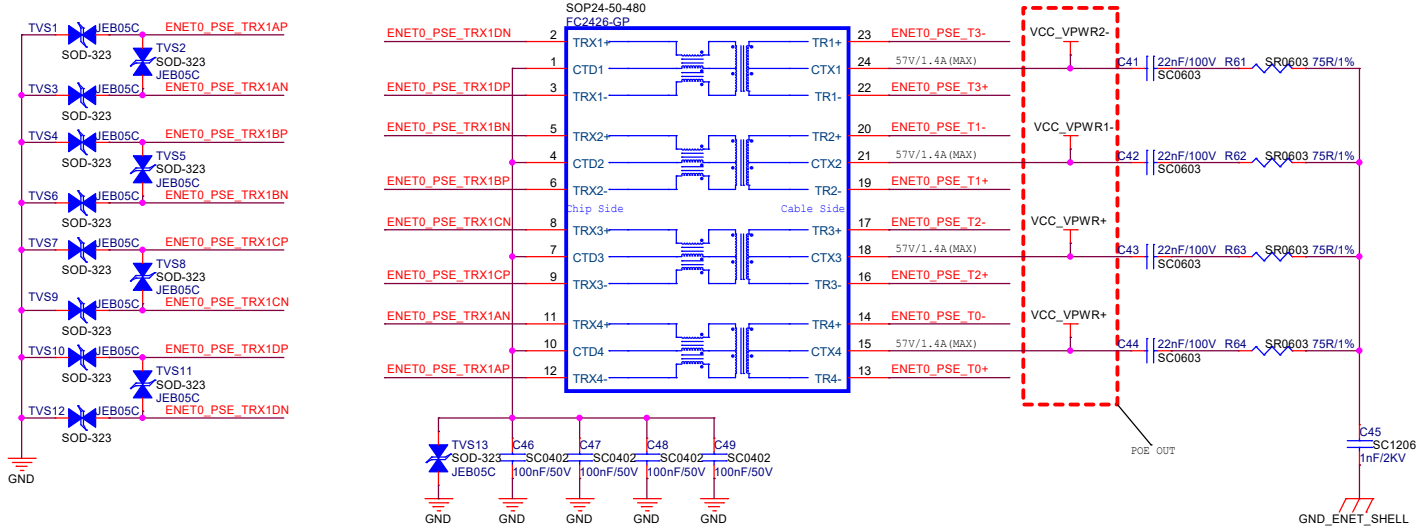
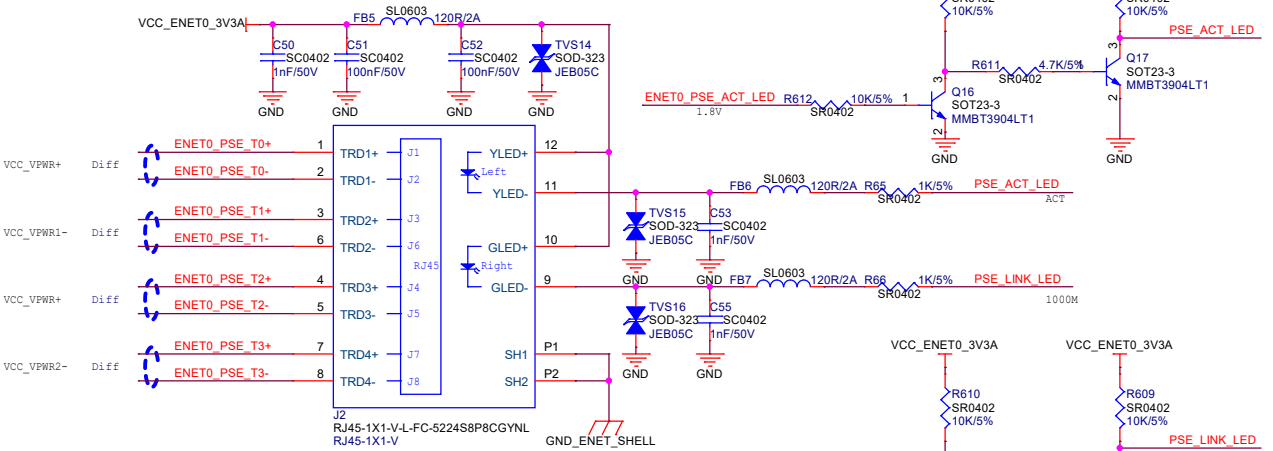
ENET0_1000M-PSE_RJ45

Transformer



3 ENET0_PSE_TRX1AP → ENET0_PSE_TRX1AP
3 ENET0_PSE_TRX1AN → ENET0_PSE_TRX1AN
3 ENET0_PSE_TRX1BP → ENET0_PSE_TRX1BP
3 ENET0_PSE_TRX1BN → ENET0_PSE_TRX1BN
3 ENET0_PSE_TRX1CP → ENET0_PSE_TRX1CP
3 ENET0_PSE_TRX1CN → ENET0_PSE_TRX1CN
3 ENET0_PSE_TRX1DP → ENET0_PSE_TRX1DP
3 ENET0_PSE_TRX1DN → ENET0_PSE_TRX1DN
3 ENET0_PSE_ACT_LED → ENET0_PSE_ACT_LED
3 ENET0_PSE_LINK_LED → ENET0_PSE_LINK_LED

PSE_RJ45



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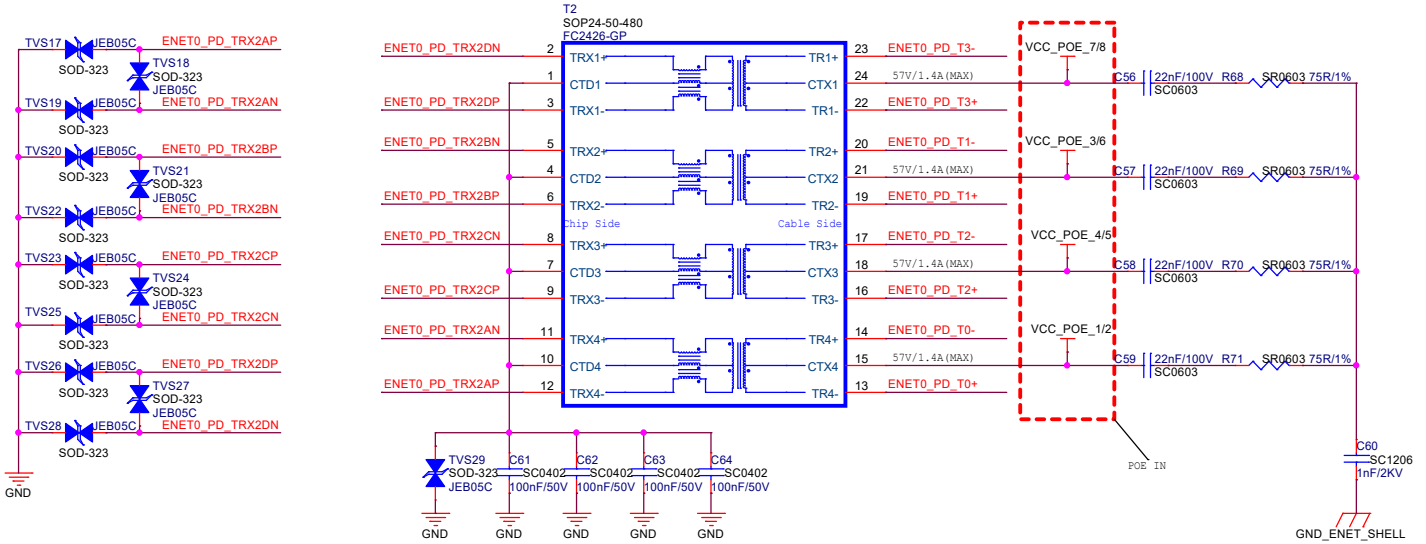
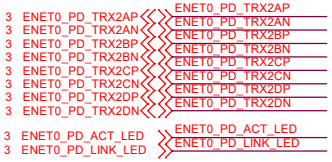
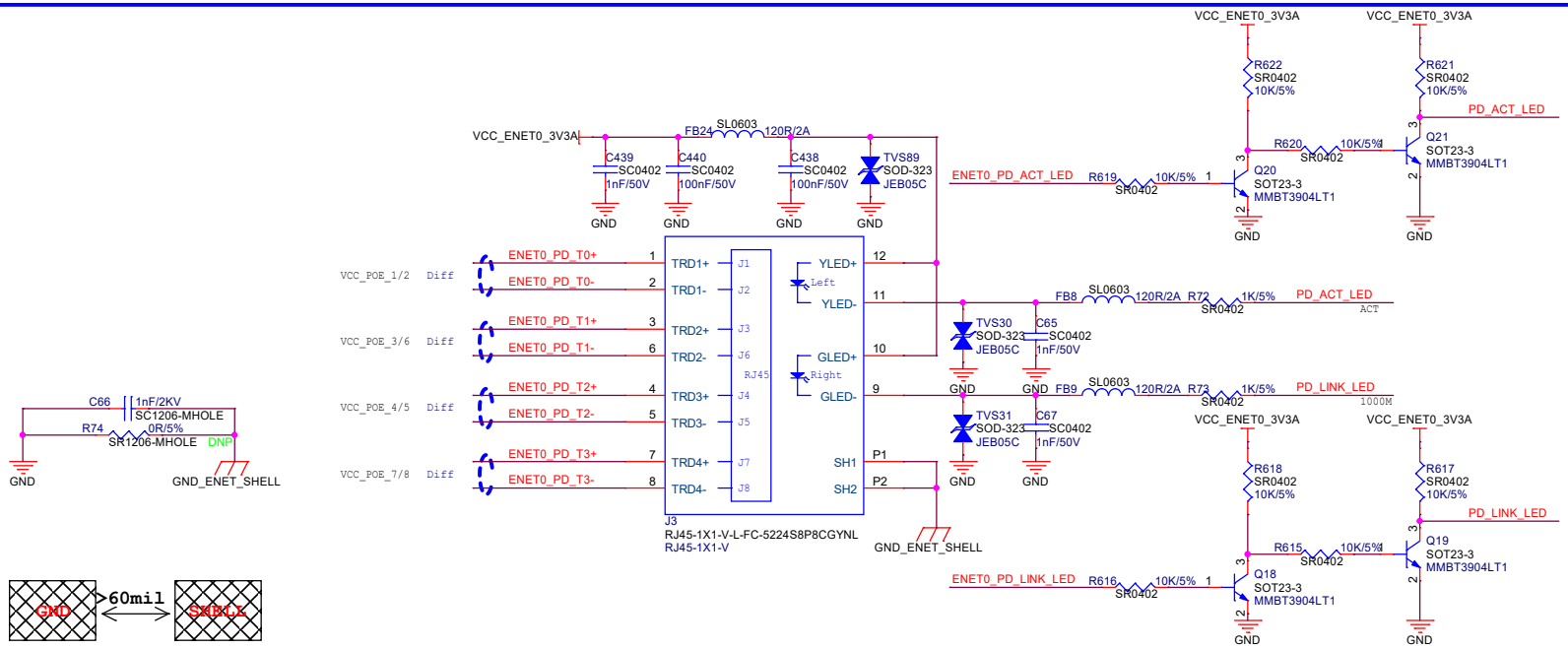
ENET0_1000M-PD_RJ45

Transformer





PD_RJ45



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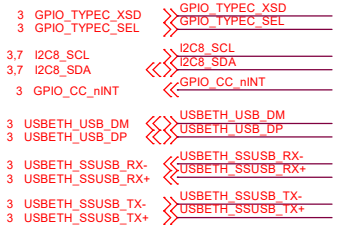
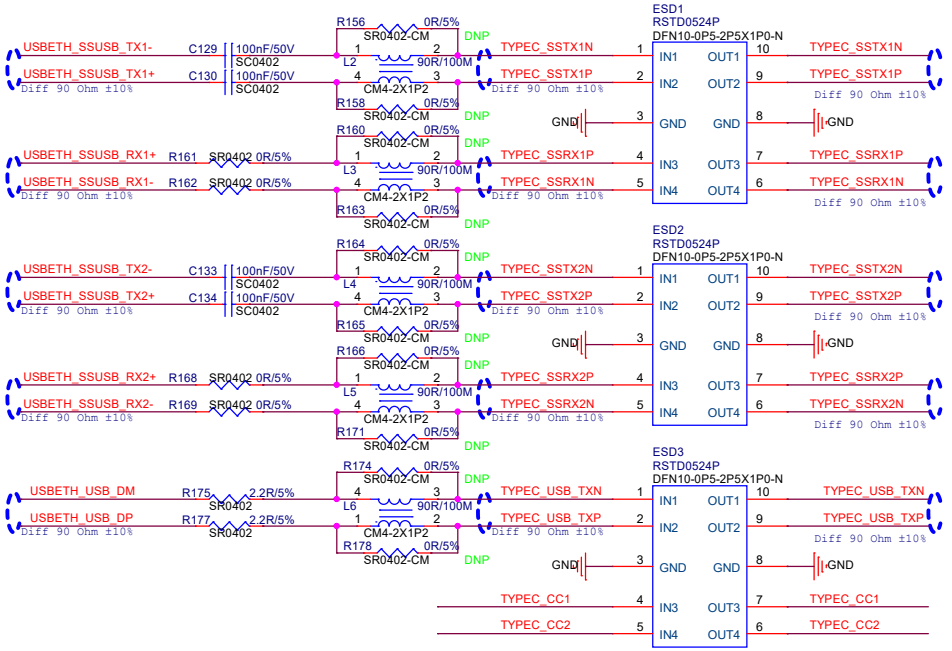
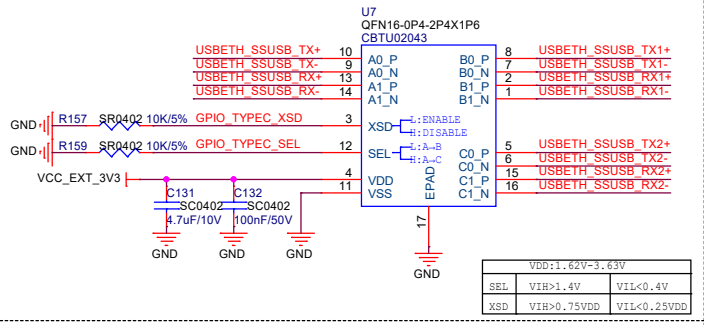
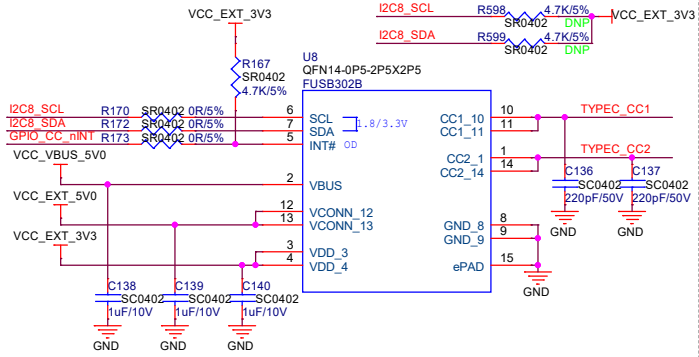
USB3_0-TO-ENET1

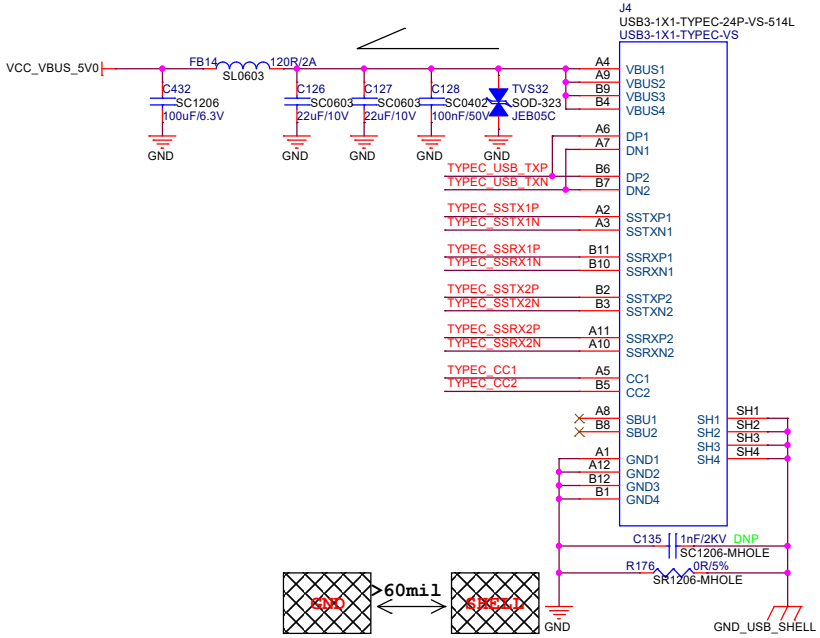




USB3.0 I/F







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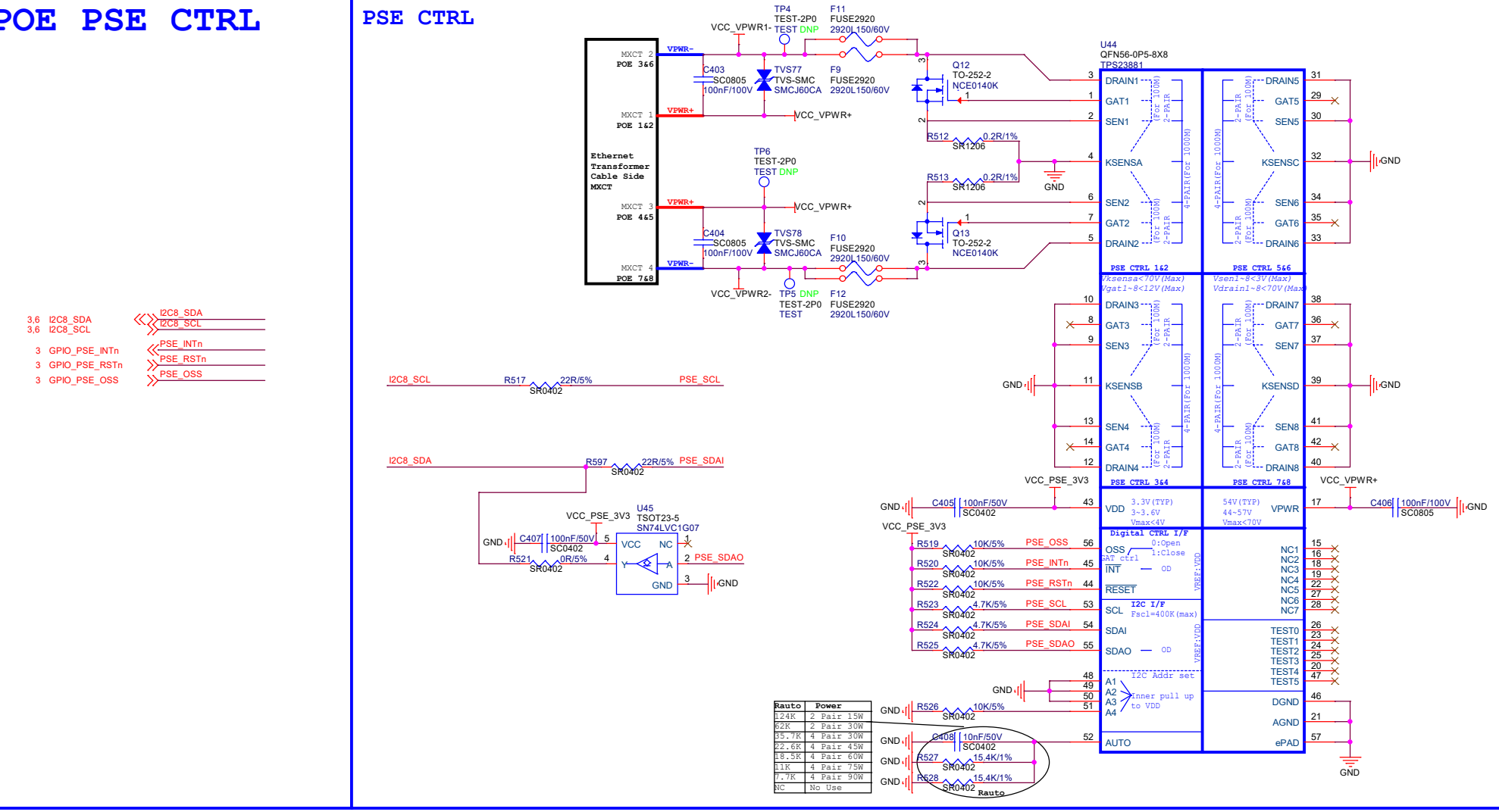
POE PSE CTRL

PSE CTRL



Rauto	Power
124K	2 Pair 15W
62K	2 Pair 30W
35.7K	4 Pair 30W
22.6K	4 Pair 45W
18.5K	4 Pair 60W
11K	4 Pair 75W
7.7K	4 Pair 90W
NC	No Use

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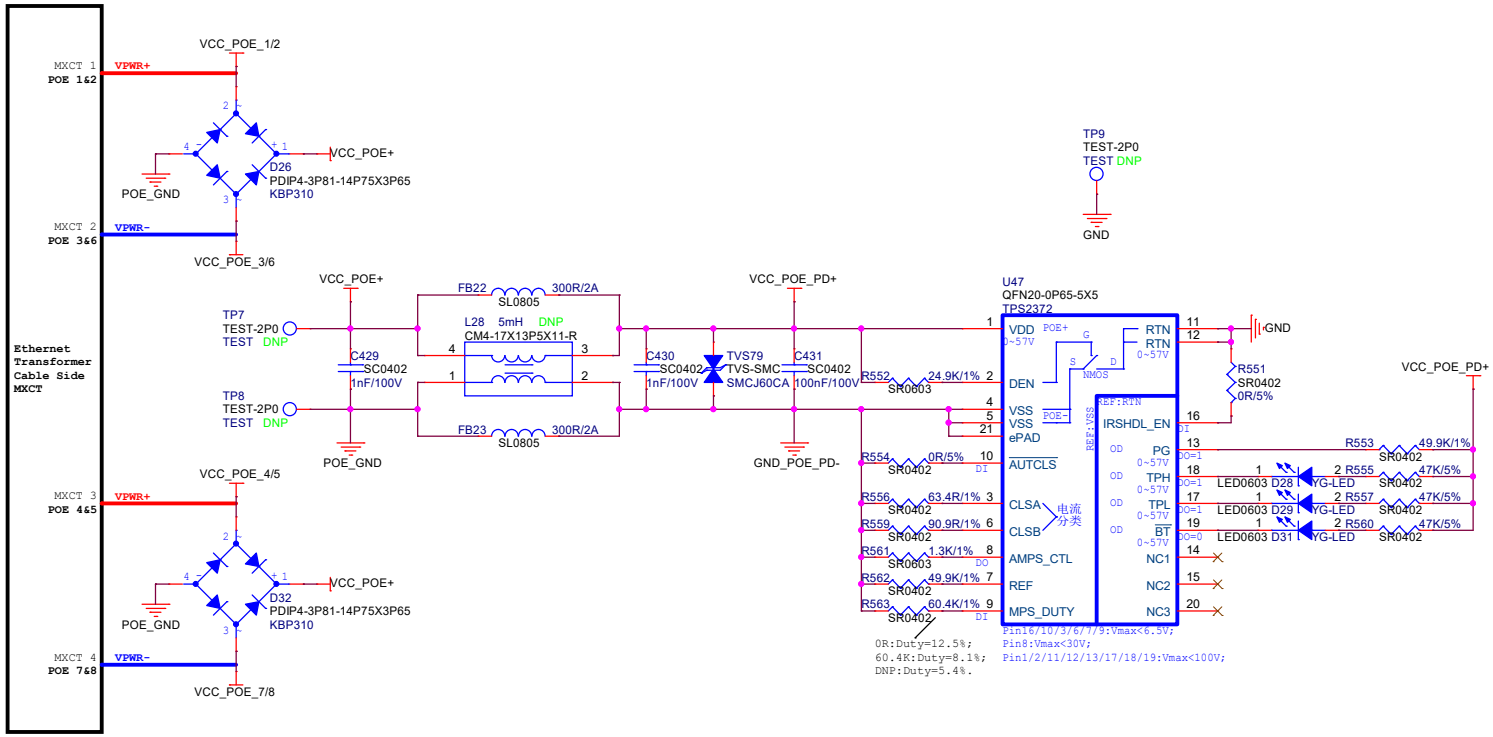
POE PD CTRL

PD CTRL



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