Electrical Characteristics ±5 V (continued)

V_S = ±5 V, R_L = 100 Ω, A_V = 2 V/V, R_F = 575 Ω, T_J = 25 °C, unless otherwise specified.

	PARAMETER	TEST CONDITIONS(1)		MIN	TYP	MAX	UNIT
ICC	Supply Current(2)	No Load			13	16	mA
			-40°C ≤ T_J ≤ 85°C			18	
	Supply Current Disabled(2)	$\overline{\text{ENABLE}}$ > 2 V			4.7	6.2	mA
			-40°C ≤ T_J ≤ 85°C			6.3	
	Supply Current Shutdown	SHUTDOWN > 2 V			1.8	2.5	mA
			-40°C ≤ T_J ≤ 85°C			2.6	
VIH	Logic High Threshold(2)	Select & Enable Pins (SD & $\overline{\text{EN}}$)		2.0			V
VIL	Logic Low Threshold(2)	Select & Enable Pins (SD & $\overline{\text{EN}}$)				0.8	V
IiL	Logic Pin Input Current Low(3)	Logic Input = 0 V Select & Enable Pins (SD & $\overline{\text{EN}}$)		−3.3	−1		µA
			-40°C ≤ T_J ≤ 85°C	−9			
IiH	Logic Pin Input Current High(3)	Logic Input = 2.0 V, Select & Enable Pins (SD & $\overline{\text{EN}}$)			47	68	µA
			-40°C ≤ T_J ≤ 85°C			72.5	
MISCELLANEOUS PERFORMANCE							
RIN+	Input Resistance				5		kΩ
CIN	Input Capacitance				0.8		pF
ROUT	Output Resistance	Output Active, ($\overline{\text{EN}}$ and SD < 0.8 V)			0.04		Ω
ROUT	Output Resistance	Output Disabled, ($\overline{\text{EN}}$ or SD > 2 V)			3000		Ω
COUT	Output Capacitance	Output Disabled, ($\overline{\text{EN}}$ or SD > 2 V)			3.1		pF
VO	Output Voltage Range	No Load		±3.54	±3.7		V
			-40°C ≤ T_J ≤ 85°C	±3.53			
VOL		R_L = 100 Ω		±3.18	±3.5		V
			-40°C ≤ T_J ≤ 85°C	±3.17			
CMIR	Input Voltage Range			±2.5	±2.6		V
IO	Linear Output Current(2)(3)	V_{IN} = 0 V		+60	±80		mA
				-70			
			-40°C ≤ T_J ≤ 85°C	+50			
			-40°C ≤ T_J ≤ 85°C	−60			
ISC	Short Circuit Current(4)	V_{IN} = ±2 V, Output Shorted to Ground			±230		mA

(4) The maximum output current (I_{OUT}) is determined by the device power dissipation limitations (The junction temperature cannot be allowed to exceed 150°C). See the *Power Dissipation* for more details. A short circuit condition should be limited to 5 seconds or less.

k Ohm oder M Ohm??