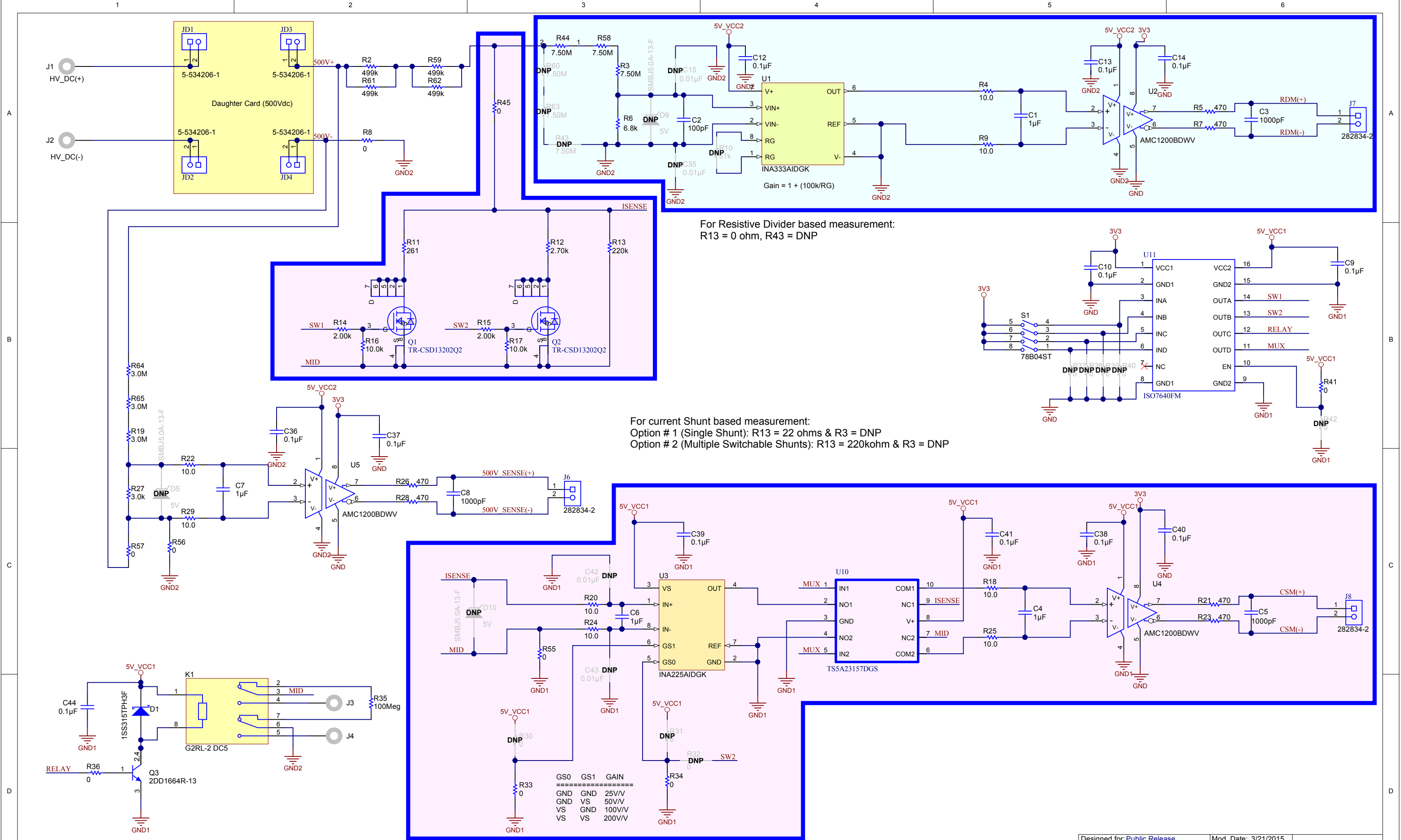


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Number: TIDA-00440MB Rev: E2	Mod. Date: 3/21/2015
SVN Rev: Not in version control	Project Title: Insulation Monitoring Circuit
Drawn By: Sanjay Pithadia	Sheet Title: Block Diagram
Engineer: Sanjay Pithadia	Assembly Variant: 001
	File: Pg1-Cover_SchDoc
	Sheet: 1 of 5
	Size: B
	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>

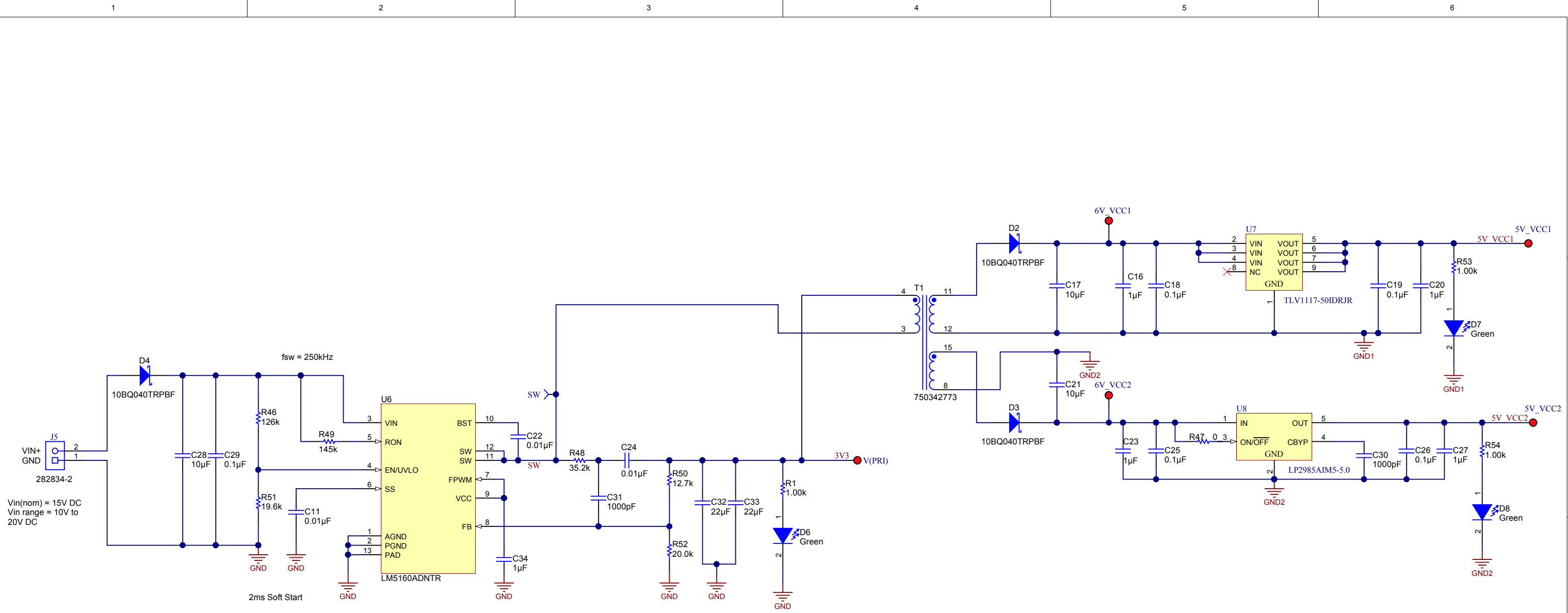


For Resistive Divider based measurement:  
R13 = 0 ohm, R43 = DNP

For current Shunt based measurement:  
Option # 1 (Single Shunt): R13 = 22 ohms & R3 = DNP  
Option # 2 (Multiple Switchable Shunts): R13 = 220kohm & R3 = DNP

GS0	GS1	GAIN
GND	GND	25V/V
GND	VS	50V/V
VS	GND	100V/V
VS	VS	200V/V

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## FLYBUCK POWER SUPPLY

Vin(nom) = 15V DC  
Vin range = 10V to 20V DC

2ms Soft Start

fsw = 250kHz

SW

3V3 V(PRI)

6V\_VCC1

5V\_VCC1

5V\_VCC2

Designed for: Public Release	Mod. Date: 3/21/2015
Project Title: Insulation Monitoring Circuit	
Sheet Title: Power Supply	
Number: TIDA-00440MB Rev: E2	Sheet: 3 of 5
SVN Rev: Not in version control	File: Pg3.SchDoc
Drawn By: Sanjay Pithadia	Size: B
Engineer: Sanjay Pithadia	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>

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H1 NY PMS 440 0025 PH    H2 NY PMS 440 0025 PH    H3 NY PMS 440 0025 PH    H4 NY PMS 440 0025 PH

FID1    FID2    FID3    FID4    FID5    FID6

PCB Number: TIDA-00440MB  
PCB Rev: E2

PCB  
LOGO  
Texas Instruments

DANGER HIGH VOLTAGE  
  
DANGER HIGH VOLTAGE

Label Table

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1  
PCB Label  
Size: 0.65" x 0.20 "


ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: TIDA-00440MB Rev: E2	Designed for: Public Release	Mod. Date: 3/19/2015
SVN Rev: Not in version control	Project Title: Insulation Monitoring Circuit	Sheet Title:
Drawn By:	Assembly Variant: 001	Sheet: 5 of 5
Engineer: Sanjay Pithadia	File: Pg4_Hardware_ANSI-B_SchDoc	Size: B
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