TAS2564YBGR – Debug Update

14th January, 2020

TAS2564YBGR | Software Shutdown Current

- Device shows high current on VBAT under software shutdown
  - 3mA current is observed on VBAT.

- TI has root-caused the current to shutdown current inside Boost controller.
  - During shutdown, one signal is getting stuck(in design during power down) which can cause this leakage.
  - Workaround for this is given in next slide

TAS2564YBGR | Software Shutdown Current

- TI recommends to use a software reset to prevent the current.
  - Software reset will reset the configuration and ensure no current is observed.
  - AVDD (if needs to be disabled), should be done after software reset.
  - Configuration for next power up can be done immediately after power down.
  - This will save time in next power up.

TAS2564YBGR | XML Update

- Changes recommended in XML will be shared separately.

End of Slides