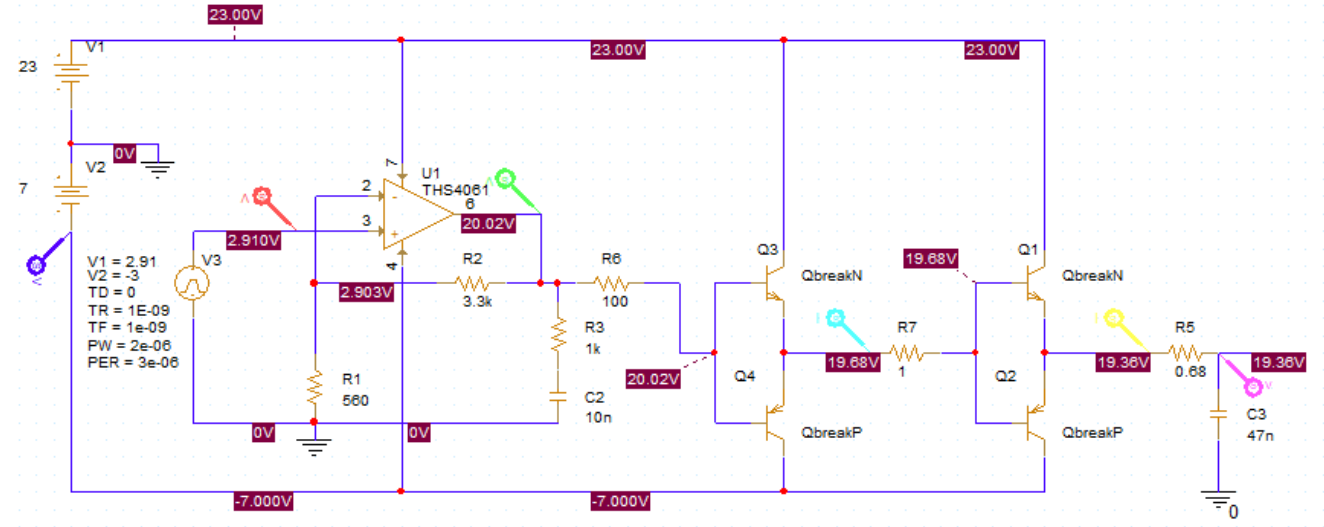
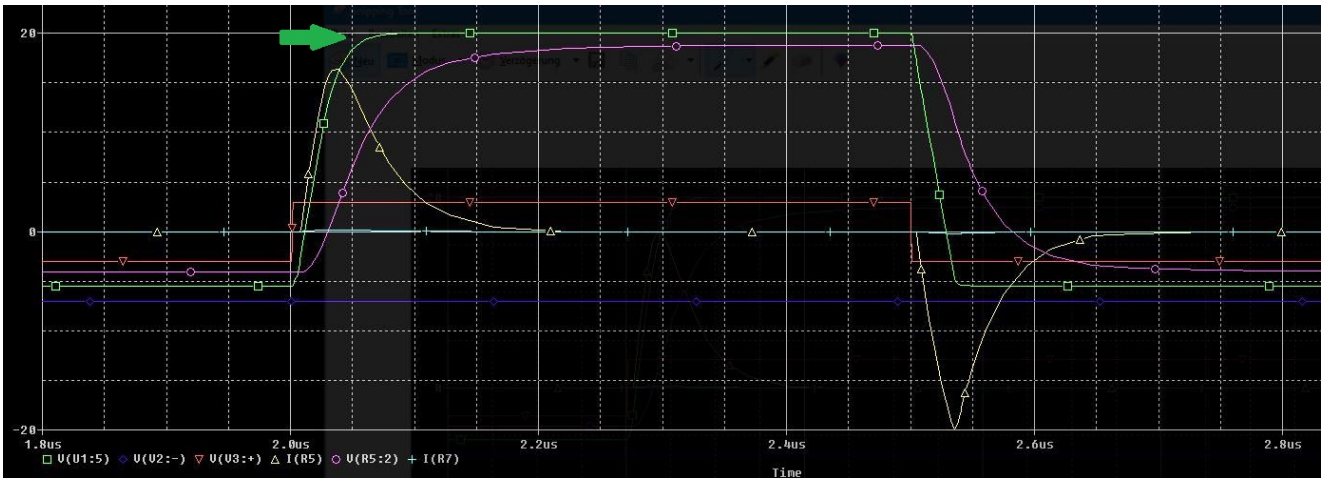
THS4061

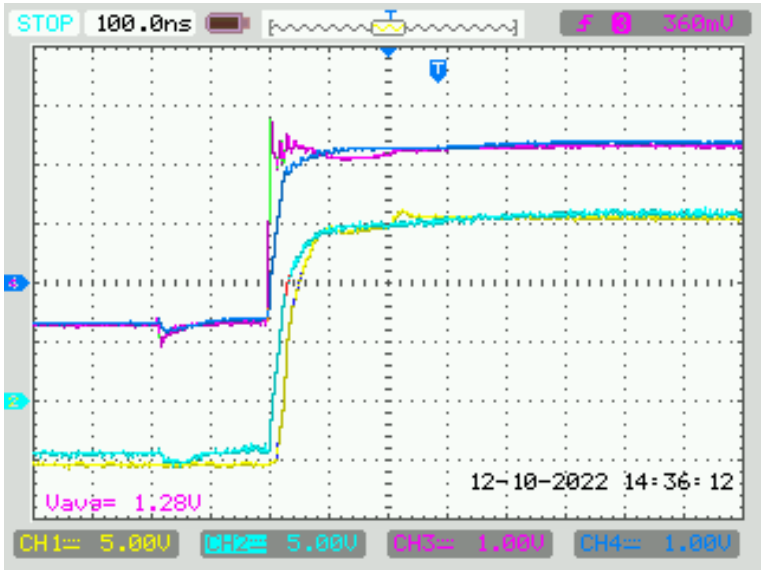
Measurements

10. Dec. 2022

Simulation with TI-PSpice:







Measurements:

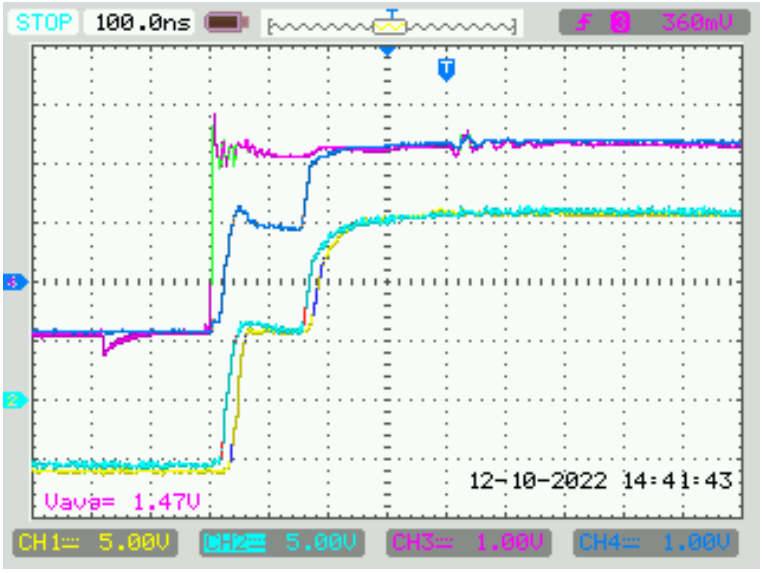
10. Dec. 2022:
_143612:

CH1: Buffer-Out
CH2: OpAmp-Out (Pin6)
CH3: OpAmp-In_P (Pin3)
CH4: OpAmp-In_M (Pin2)

Normal operation:
➔ „immediate“ and linear response of OpAmp-Out to Input-Signal = ok !

THS4061

Measurements



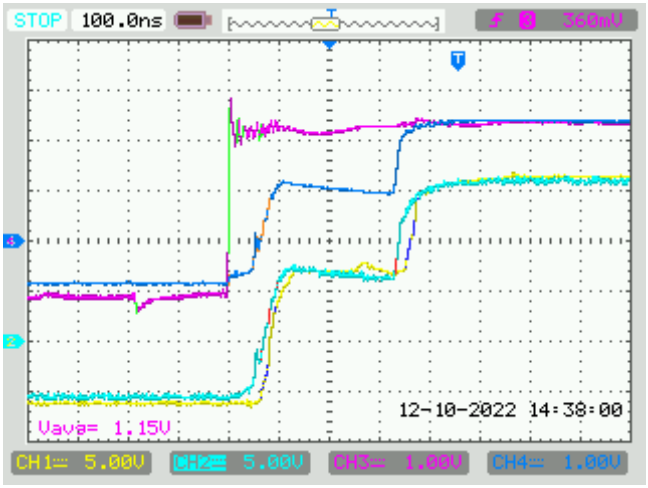
_144143:

With slightest overriding the OpAmp differential input:

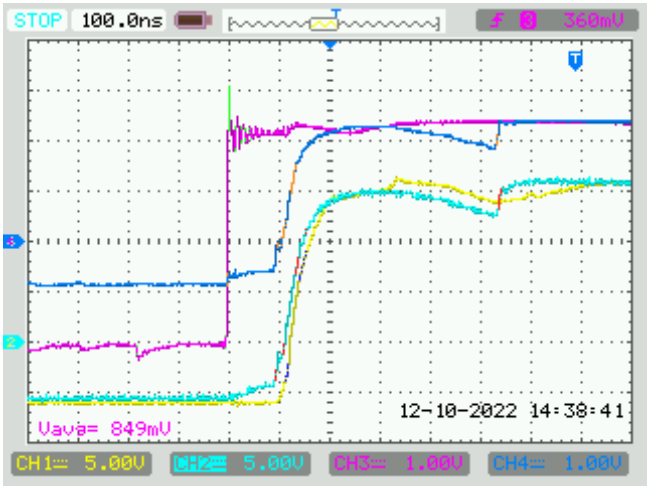
CH1: Buffer-Out
CH2: OpAmp-Out (Pin6)
CH3: OpAmp-In_P (Pin3)
CH4: OpAmp-In_M (Pin2)

➔ Although the OpAmp input has no visible differential input voltage the **output is not ok !**

Further examples for false output voltages:



_143800: with ca. -0,2V differential input voltage



_143841: with ca. -1,1V differential input voltage

In case of a “high” negative differential OpAmp input voltage then there is a false output signal for up to 500ns after a positive going slope.

➔ This effect is not shown at all in the TI-PSpice simulation.