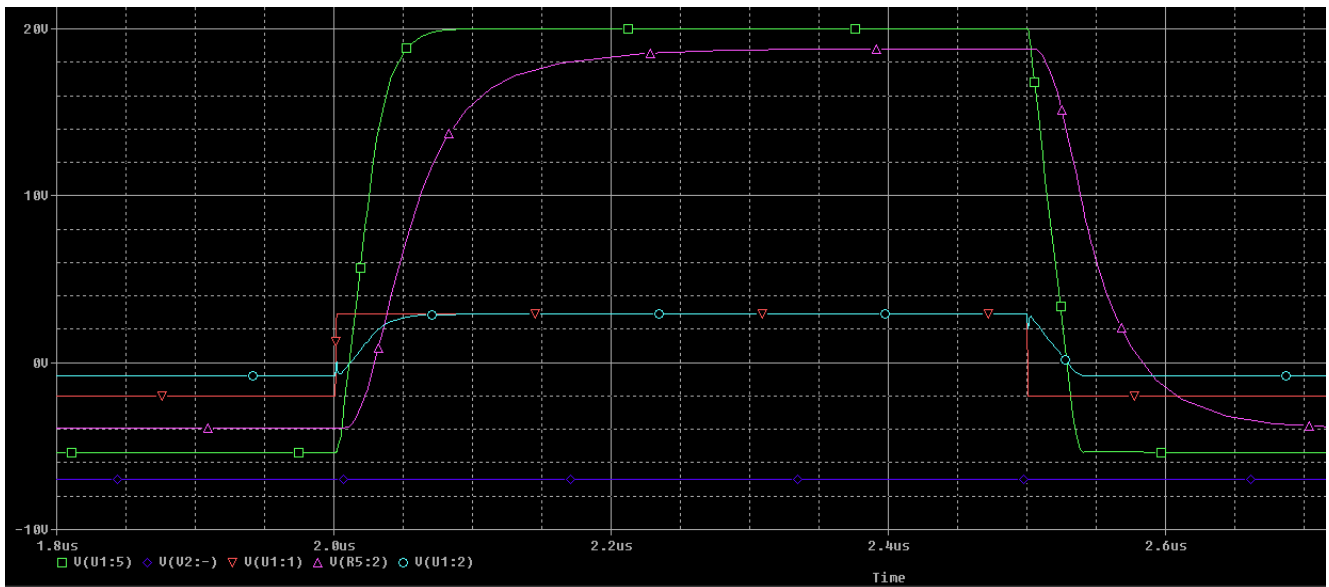
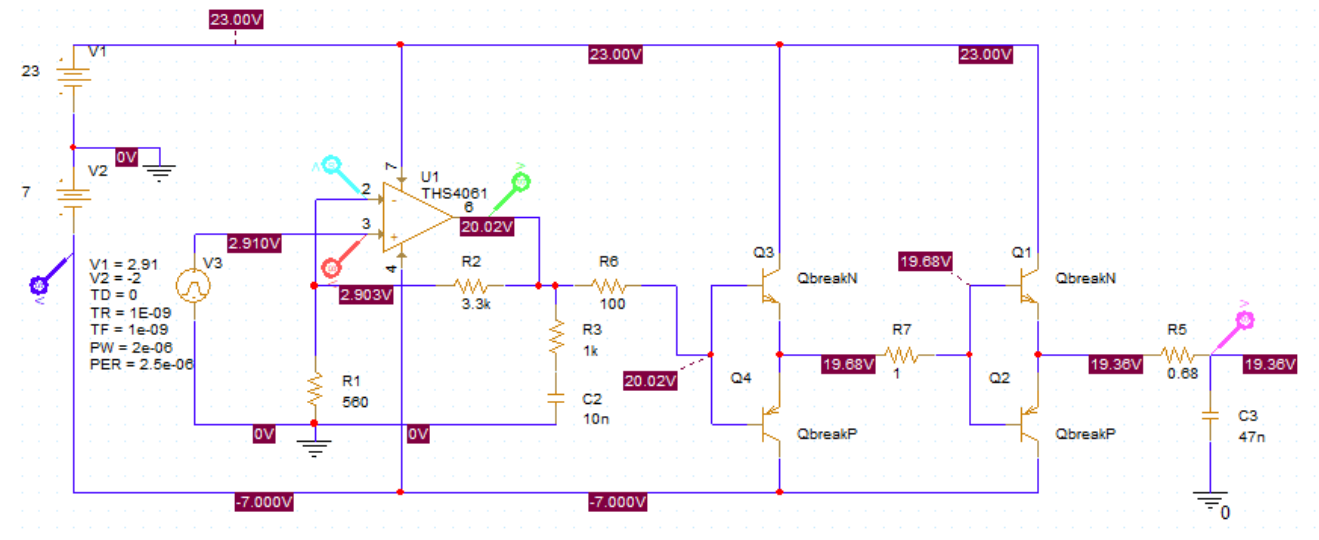


THS4061

Measurements

23. Dec. 2022

Simulation with TI-PSpice:



The amplifier has an gain of $(1+3k3/560)=6.89$

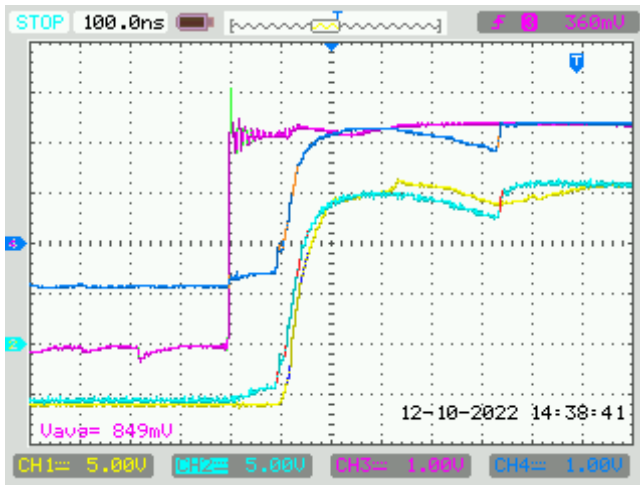
With an input voltage of -2V the output voltage should be -13.8V, but this cannot be since the neg. power supply is only -7V → thus there is an override of the input with a high differential voltage of ca. -1,2V.

This situation corresponds good to the measurement _143841

THS4061

Measurements

Measurements:



10. Dec. 2022:

_143841: with ca. -1,1V differential input voltage

Graph:

CH1: Buffer-Out

CH2: OpAmp-Out (Pin6)

CH3: OpAmp-In_P (Pin3)

CH4: OpAmp-In_M (Pin2)

Simulation:

→ V(R5:2) pink

→ V(U1:5) green

→ V(U1:1) red

→ V(U1:2) lightblue

In case of a "high" negative differential OpAmp input voltage then there is a false output signal for up to 500ns after a positive going slope.