



Acquisition / conversion time on ADC (LTC2326-16)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
f_{SMPL}	Maximum Sampling Frequency				250	ksps
t_{CONV}	Conversion Time		1.9		3	μs
t_{ACQ}	Acquisition Time	$t_{ACQ} = t_{CYC} - t_{HOLD}$ (Note 11)	3.460			μs
t_{HOLD}	Maximum Time between Acquisitions				540	ns
t_{CYC}	Time Between Conversions		4			μs

Internal Reference on ADC (LTC2326-16)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{REFIN}	Internal Reference Output Voltage		2.043	2.048	2.053	V
	V_{REFIN} Temperature Coefficient	(Note 14)		2	20	ppm/ $^{\circ}C$
	REFIN Output Impedance			15		$k\Omega$
	V_{REFIN} Line Regulation	$V_{DD} = 4.75V$ to $5.25V$		0.08		mV/V
	REFIN Input Voltage Range	(REFIN Overdriven) (Note 5)	1.25		2.4	V