

[illegible]

Hardware Monitor

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LAYOUT:
C1175 close to Q1008

+3.3V Is monitored by P1M4 (VCC)

FAN SPD = 1
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DZ/DZ :Bottom

Hardware Version
:00001 => AB

I2C address :

Pin 13	Pin 14	Address
0	L	0x2c
0	H	0x2d
1	S	0x2e

Divder due to Vccp limit is 3.0V

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