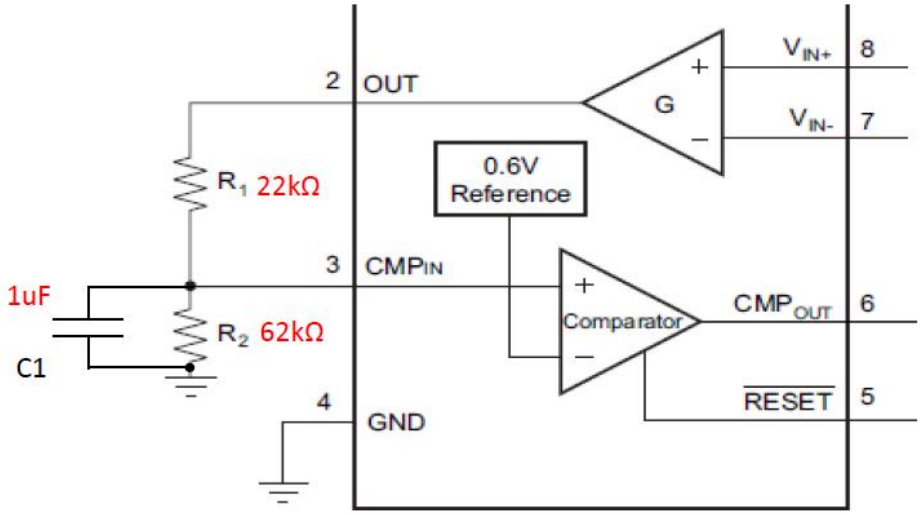
Question about INA200

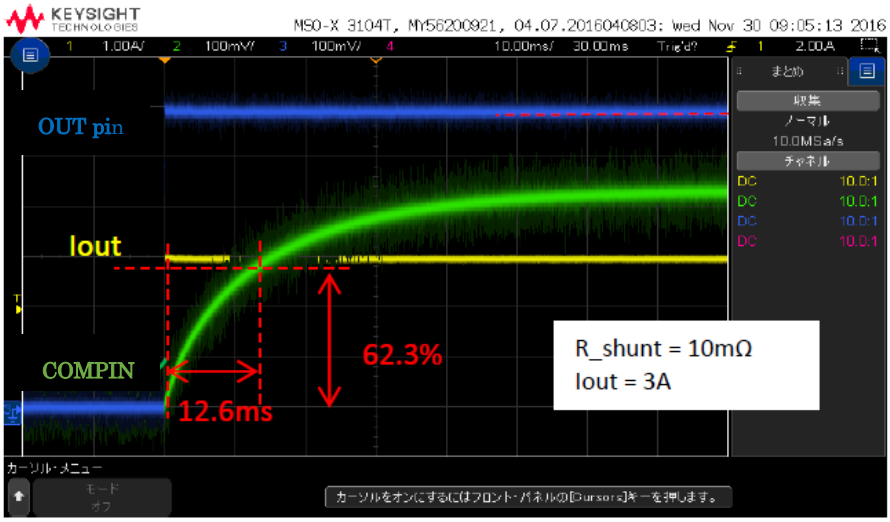
1. Current situation

When a capacitor is add to CMPIN pin, rising time is not estimated value.



The rising time is estimated CR constant of (R1//R2)xC1, but actual is faster.

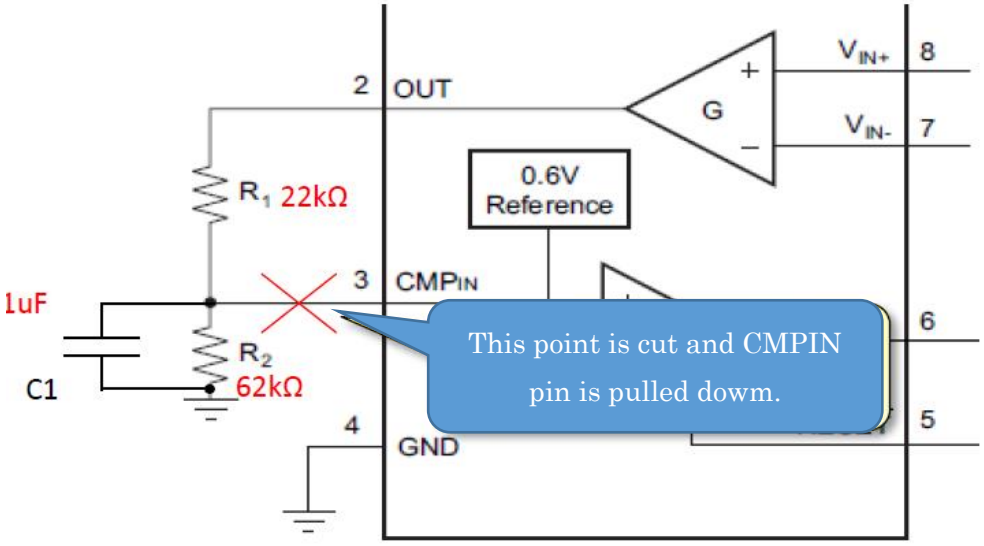
Calculation is (22kohm//62kohm)x1uF=16.2ms, an actual value is 12.6ms

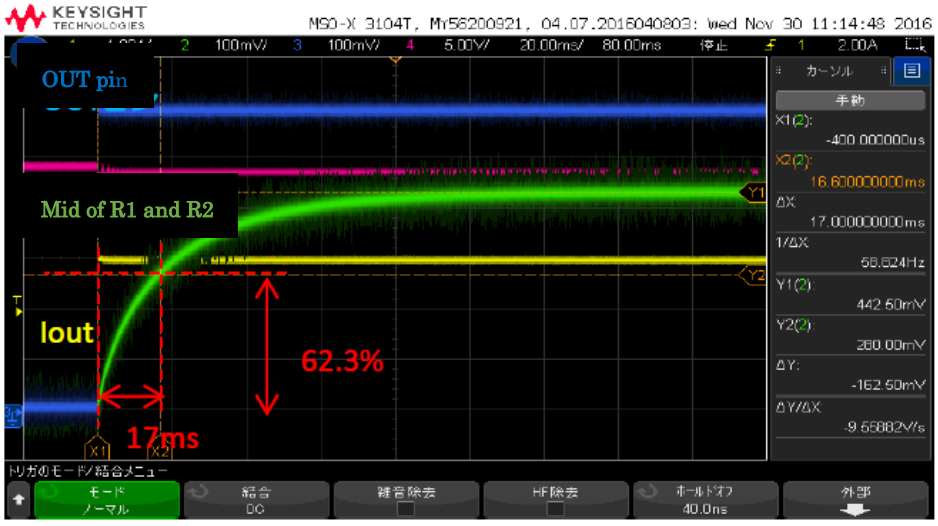


It can't be explained even if they are considered deviation (+/- 10%) of capacitor and resistor.

16.2msx0.9=14.6ms > 12.6ms

If it is disconnected between CMPin and C1, rising time isworked out as expected.





The current may be output as source current, the spec of bias current is small enough than the current through divide resistor.

The current through divide resistor = 600mV/(22kohm+64kohm)=7.1uA

The bias current of CMPIN = 15nA(described in datasheet)

INPUT BIAS CURRENT (2)				
Input bias current, VMP_{in} PIN		0.005	10	nA
Input bias current, VMP_{in} PIN, vs temperature	T_A = –40°C to 125°C		15	nA

2. Question

Is there any input current except input bias current. (For example, constant current for hysteresis.)
If so, could you advise me typ and max value.
If not, what reason is considerable about this phenomenon?