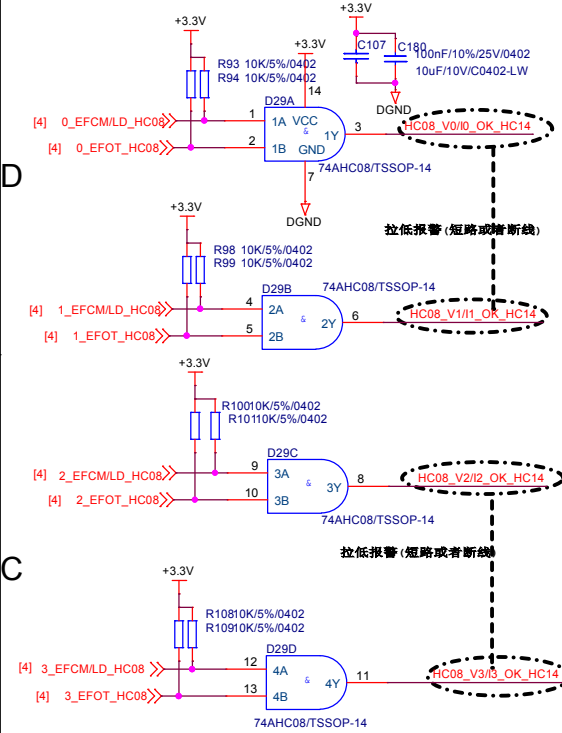
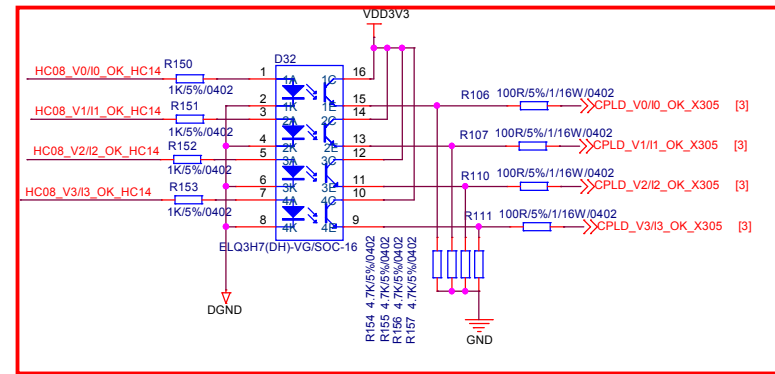


短路或断线检测电路

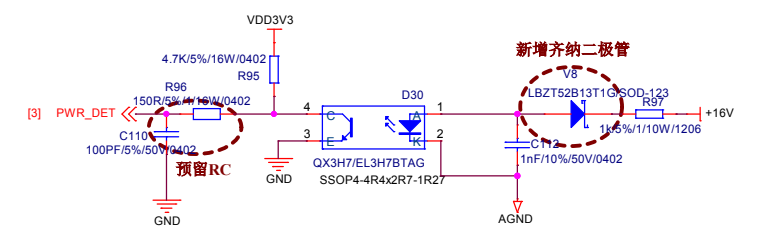


XTR305输出信号相与后在经隔离之后给CPLD

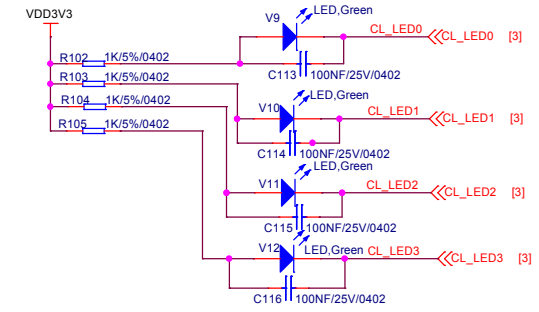
隔离
D32 CPLD输入信号隔离



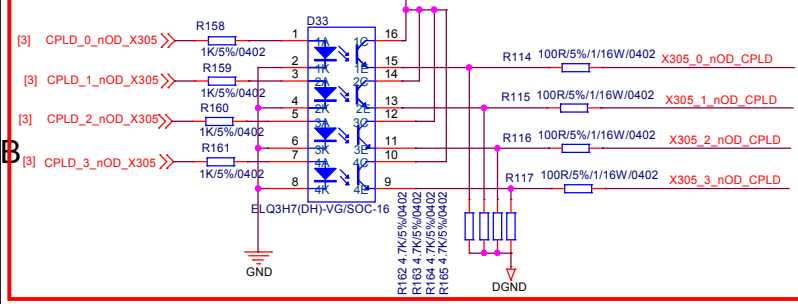
电源检测



通道显示灯

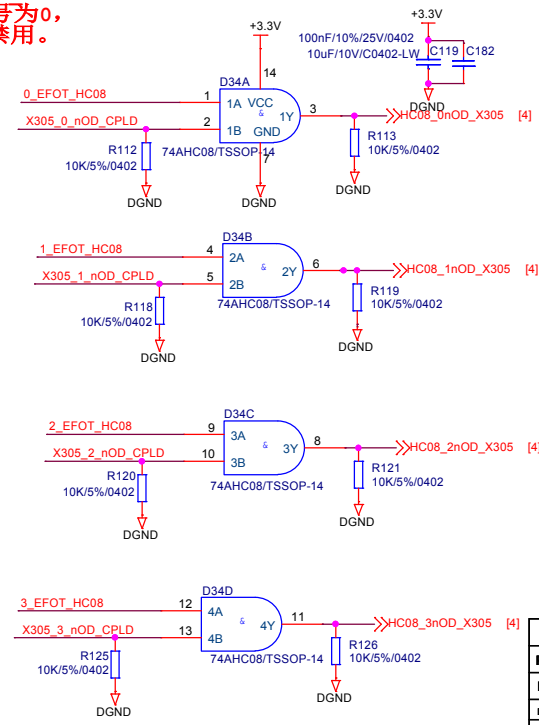


隔离



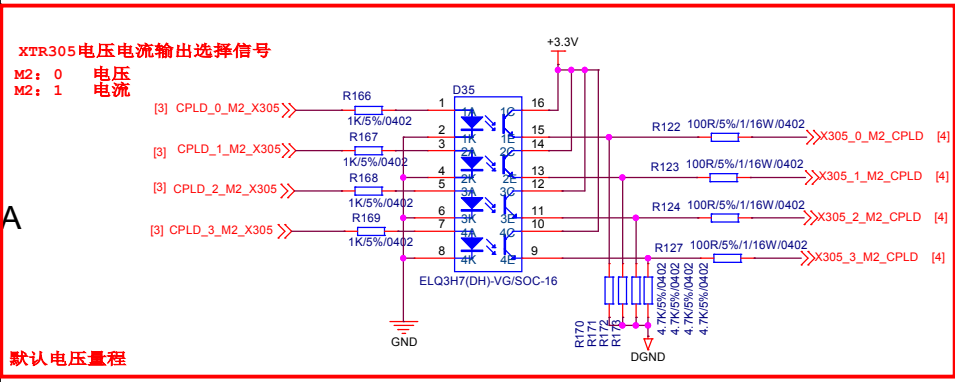
X305 0_nOD CPLD和XTR305输出的0_EFOT_HC08过热信号相与之后，若2个中有一个信号为0，HC08_0nOD_X305为0，则XTR305输出被禁用。

D33, D35 CPLD输出信号隔离



- CPLD_0_M2_X305
- CPLD_1_M2_X305
- CPLD_2_M2_X305
- CPLD_3_M2_X305
- CPLD_V0/I0_OK_X305
- CPLD_V1/I1_OK_X305
- CPLD_V2/I2_OK_X305
- CPLD_V3/I3_OK_X305

靠近CPLD引脚放置



默认电压量程

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隔离电路 5
CPLD与DAC之间隔离

4

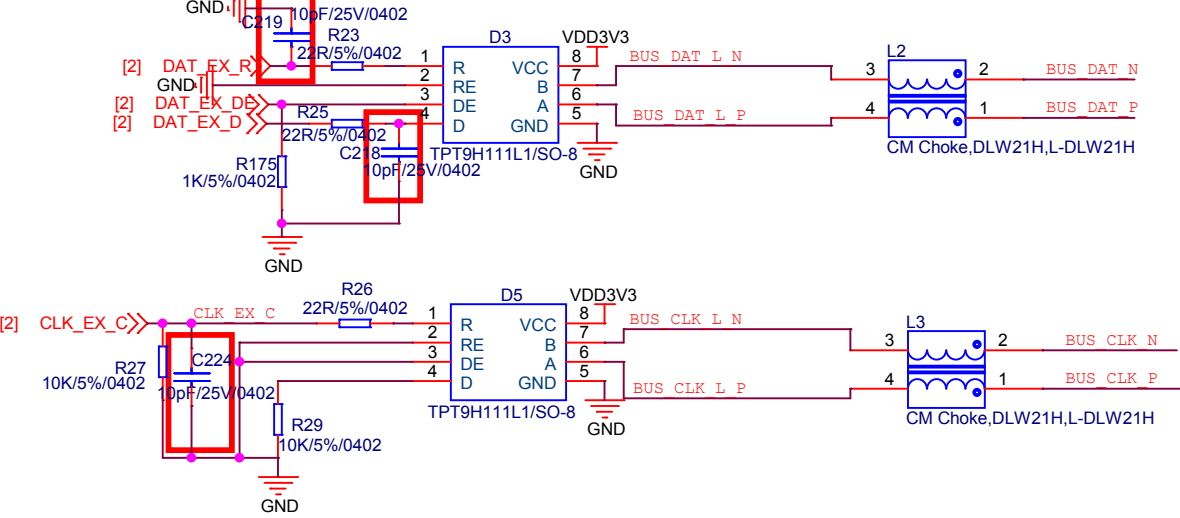
202

对外端子信

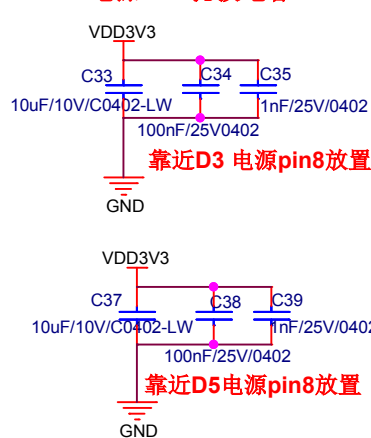
输出电路

EP在底层铺-16V的铜，开窗

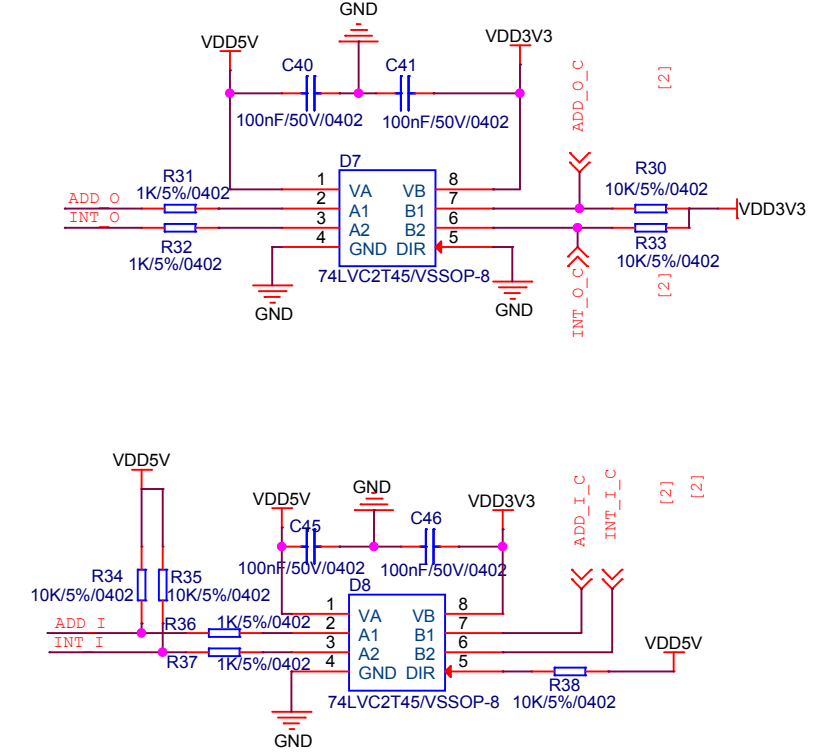
MLVDS线路驱动和收发



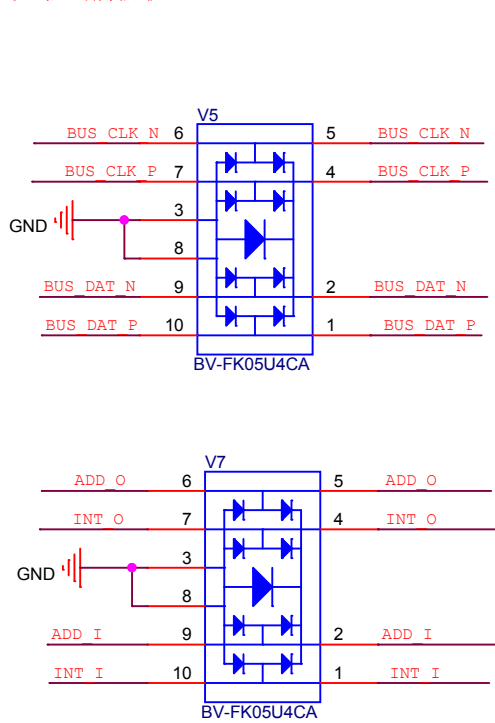
电源3.3V滤波电容



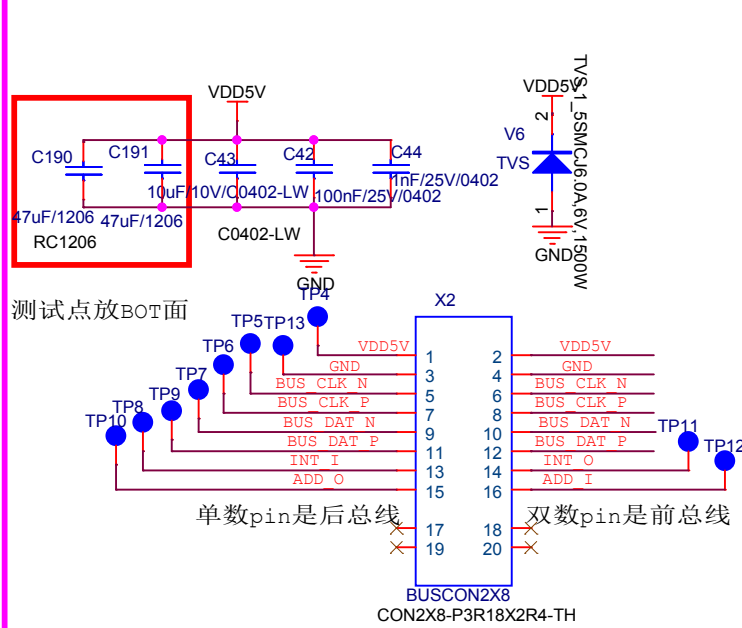
总线双电源电平转换



总线通信引脚防护

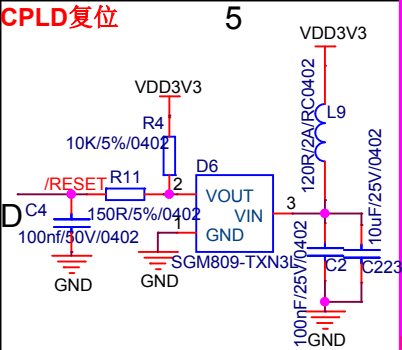


总线接口



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CPLD复位

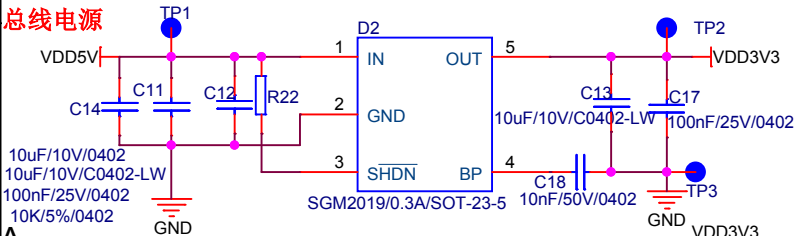


CPLD_0_M2_X305 >> CPLD_0_M2_X305 [5]
CPLD_1_M2_X305 >> CPLD_1_M2_X305 [5]
CPLD_2_M2_X305 >> CPLD_2_M2_X305 [5]
CPLD_3_M2_X305 >> CPLD_3_M2_X305 [5]
CPLD_0_nOD_X305 >> CPLD_0_nOD_X305 [5]
CPLD_1_nOD_X305 >> CPLD_1_nOD_X305 [5]
CPLD_2_nOD_X305 >> CPLD_2_nOD_X305 [5]
CPLD_3_nOD_X305 >> CPLD_3_nOD_X305 [5]
[5] CPLD_V0/I0_OK_X305 >> CPLD_V0/I0_OK_X305
[5] CPLD_V1/I1_OK_X305 >> CPLD_V1/I1_OK_X305
[5] CPLD_V2/I2_OK_X305 >> CPLD_V2/I2_OK_X305
[5] CPLD_V3/I3_OK_X305 >> CPLD_V3/I3_OK_X305
[5] PWR_DET >> PWR_DET

B

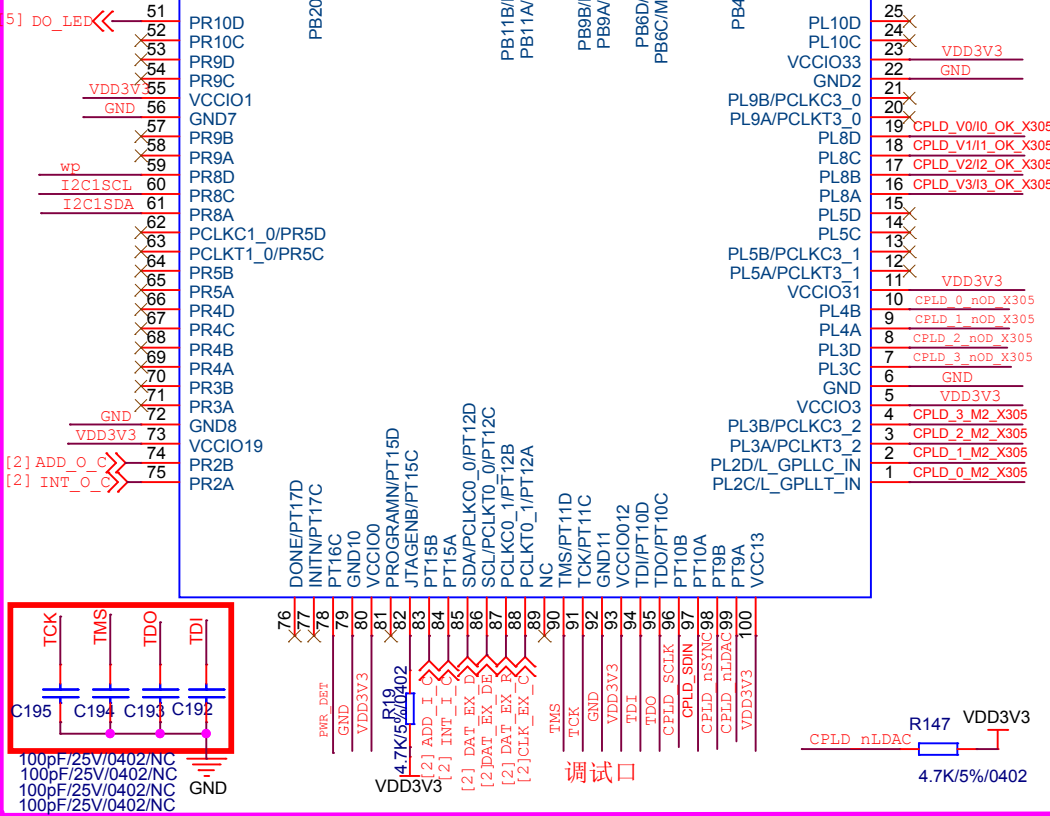
CPLD_SCLK >> CPLD_SCLK [4]
CPLD_nSYNC >> CPLD_nSYNC [4]
CPLD_nLDAC >> CPLD_nLDAC [4]
CPLD_SDIN >> CPLD_SDIN [4]

总线电源

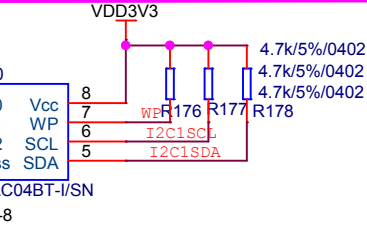


A

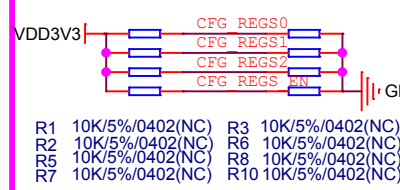
CPLD



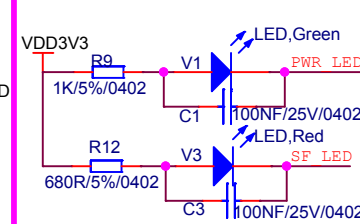
调试口



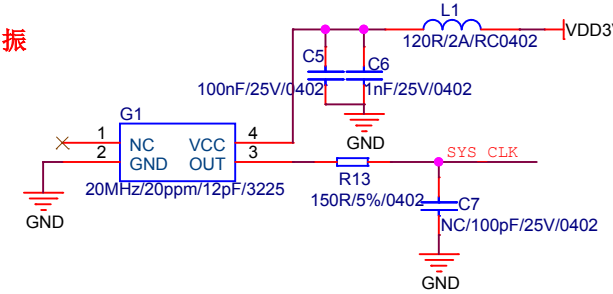
硬件版本配置 预留



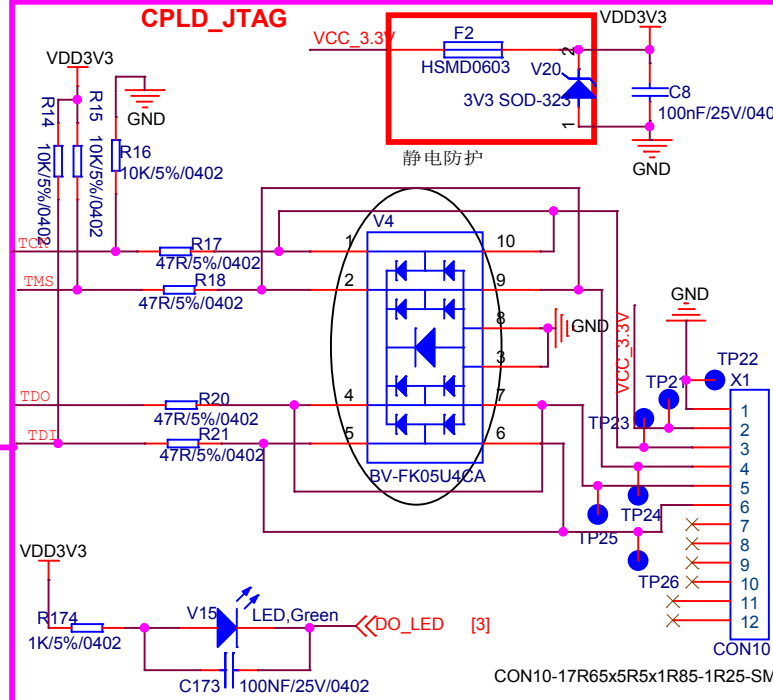
LED



CPLD晶振



CPLD_JTAG

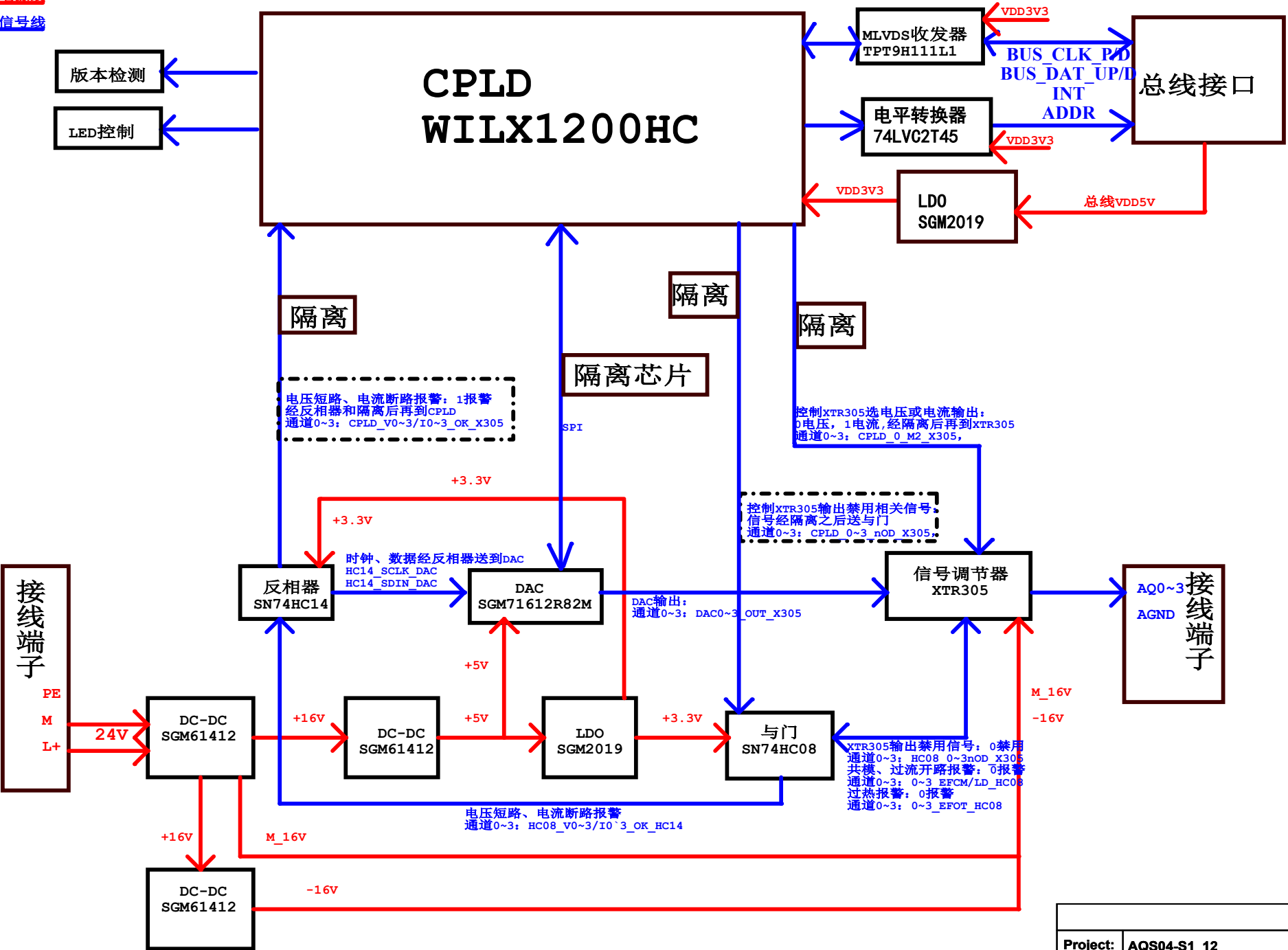


Download 显示灯



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红色为电源线
蓝色为信号线



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