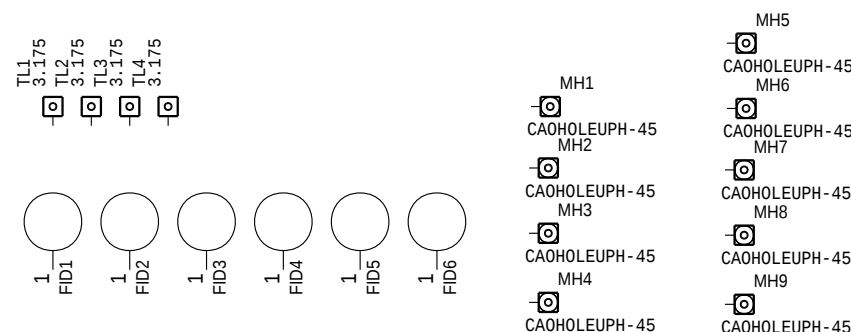
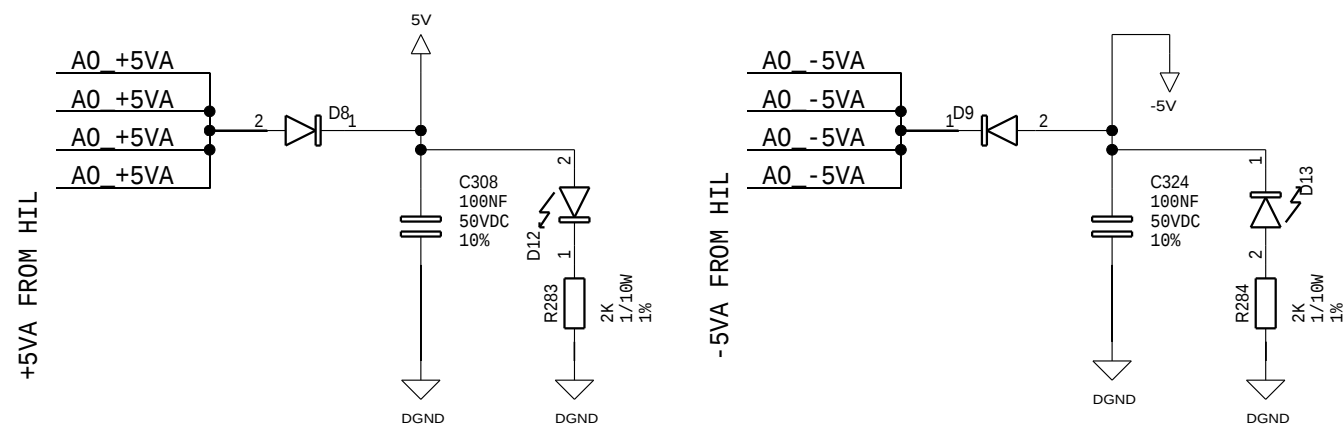
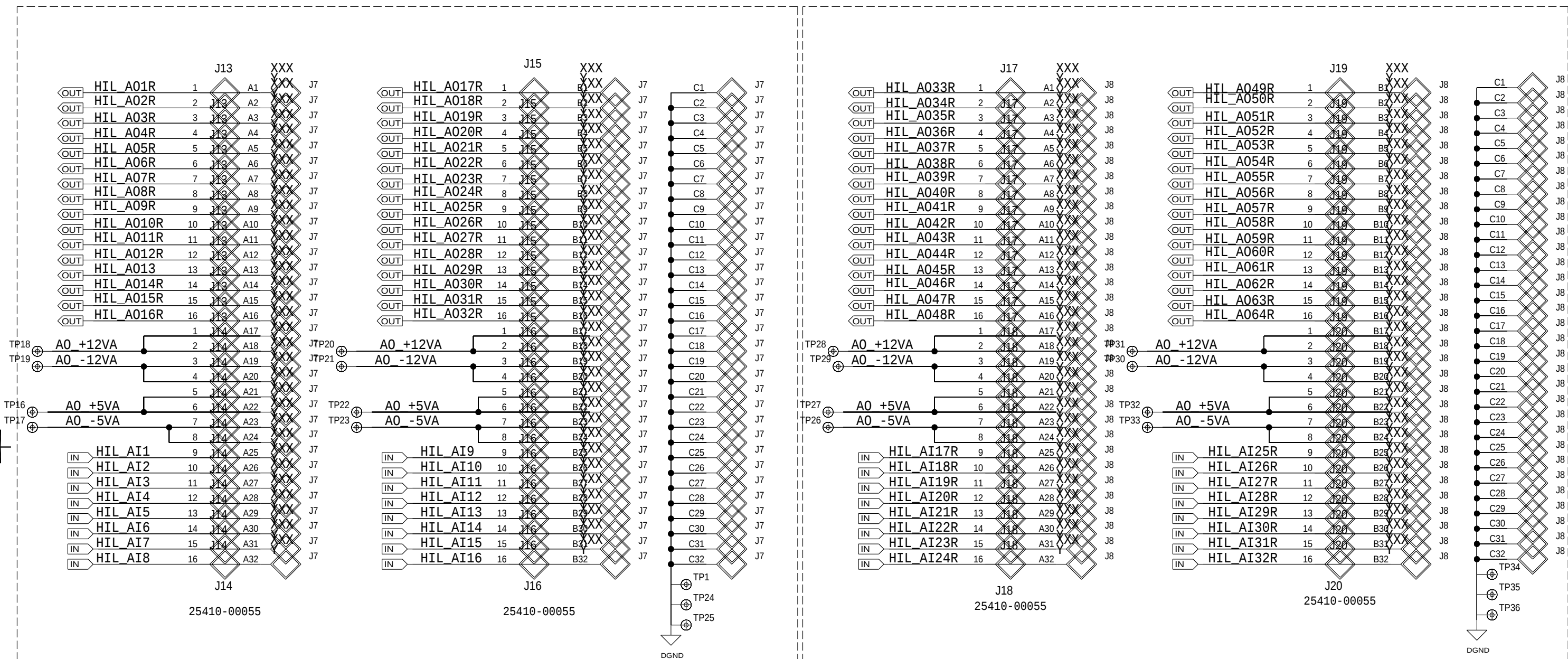


TYPHOON HIL ANALOG IO

A1-A16 AND B1-16 ARE OUT FROM TYPHOON

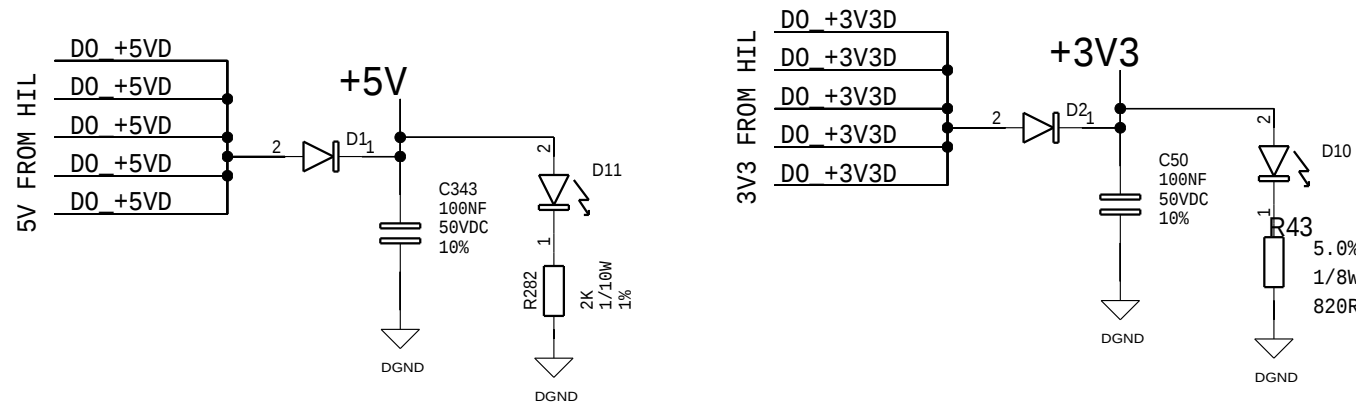
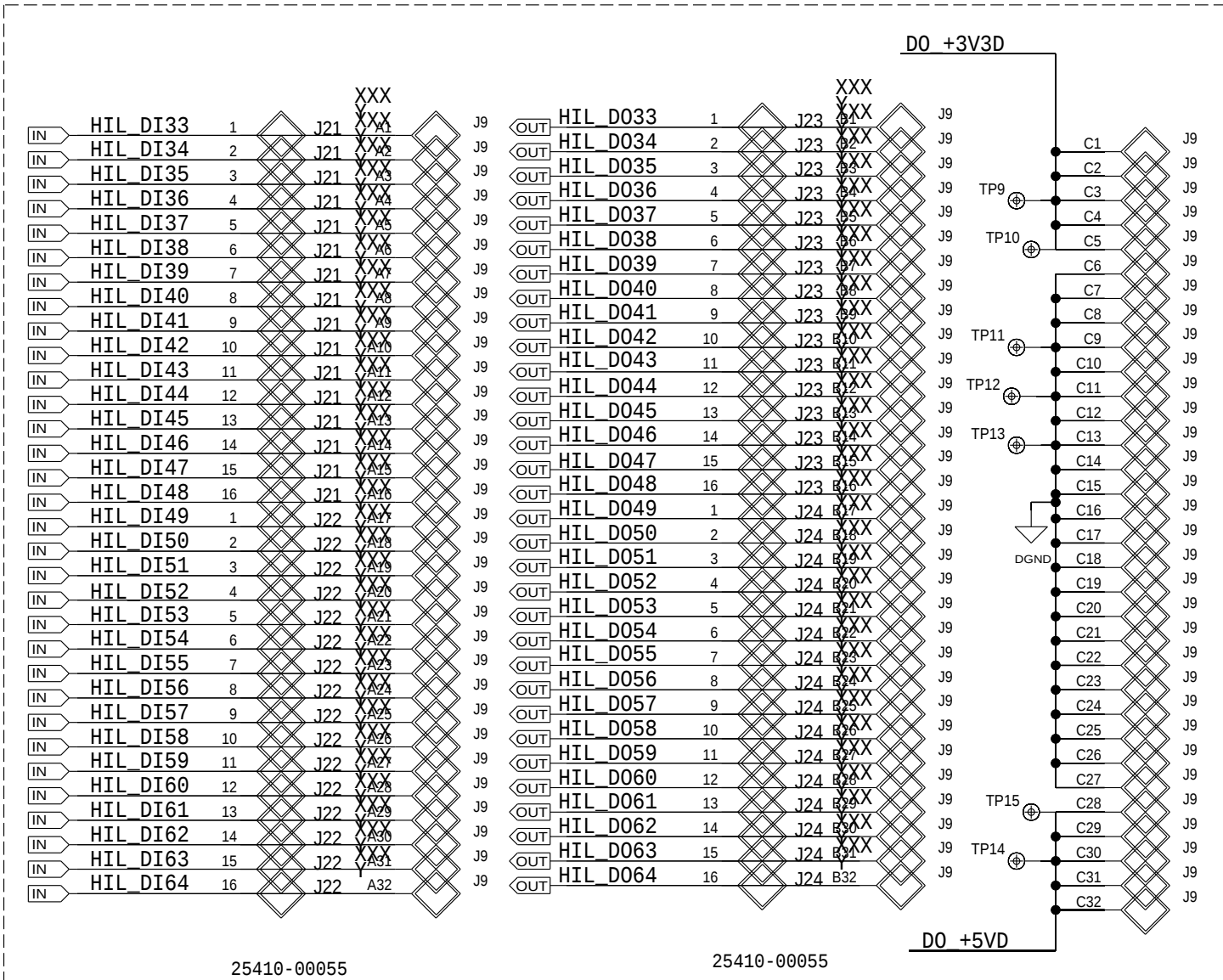
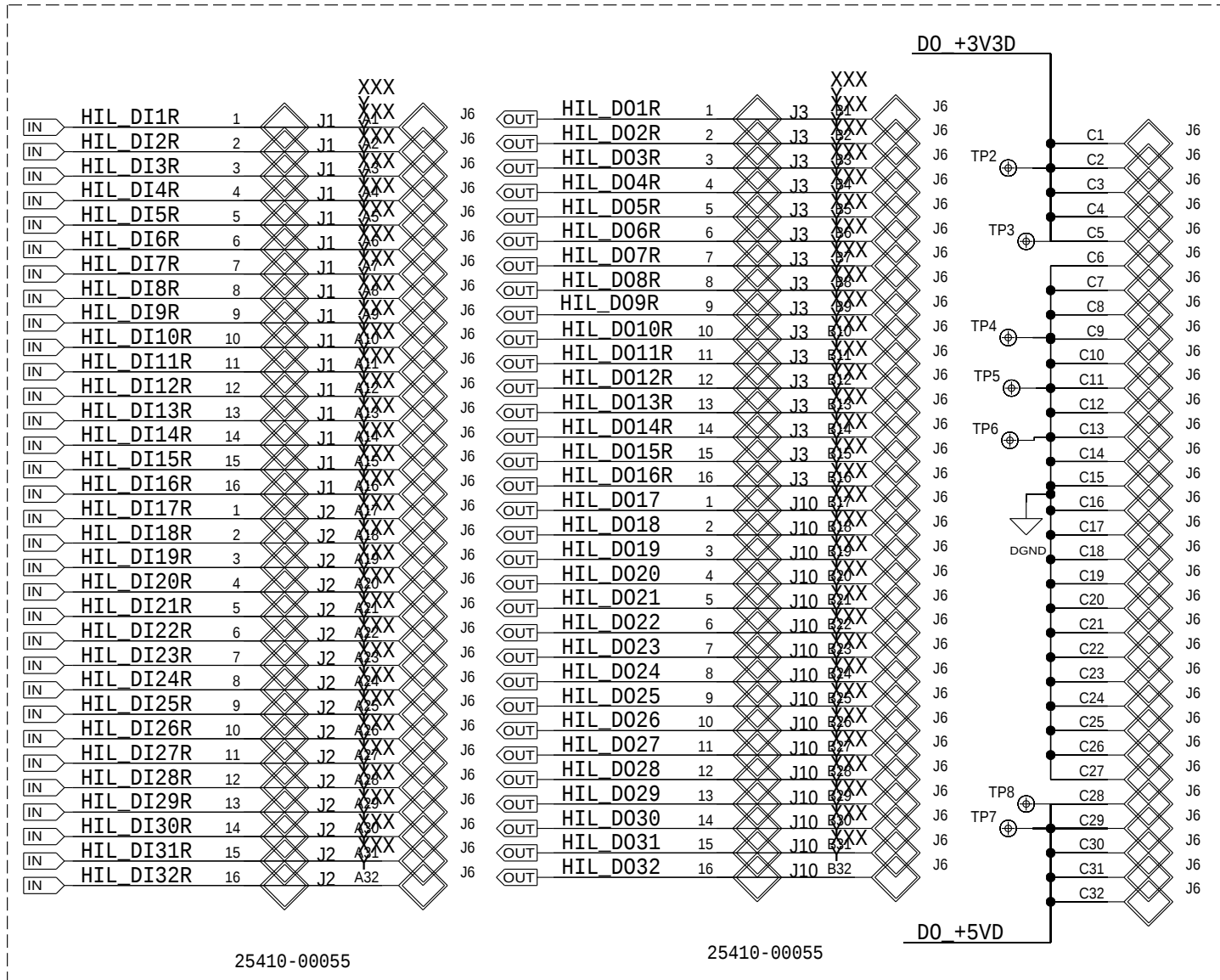
A25-A32 AND B25-32 ARE IN TO TYPHOON



TYPHOON HIL DIGITAL IO

A PINS ARE IN TO TYPHOON

B PINS ARE OUT FROM TYPHOON



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NUMBER :	BLOCK NAME = top	REVISION	SHEET : 2/20
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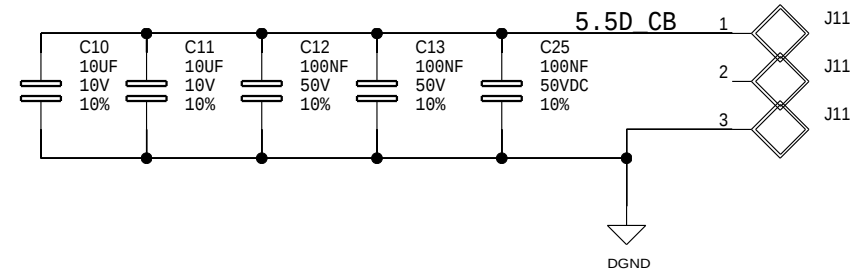
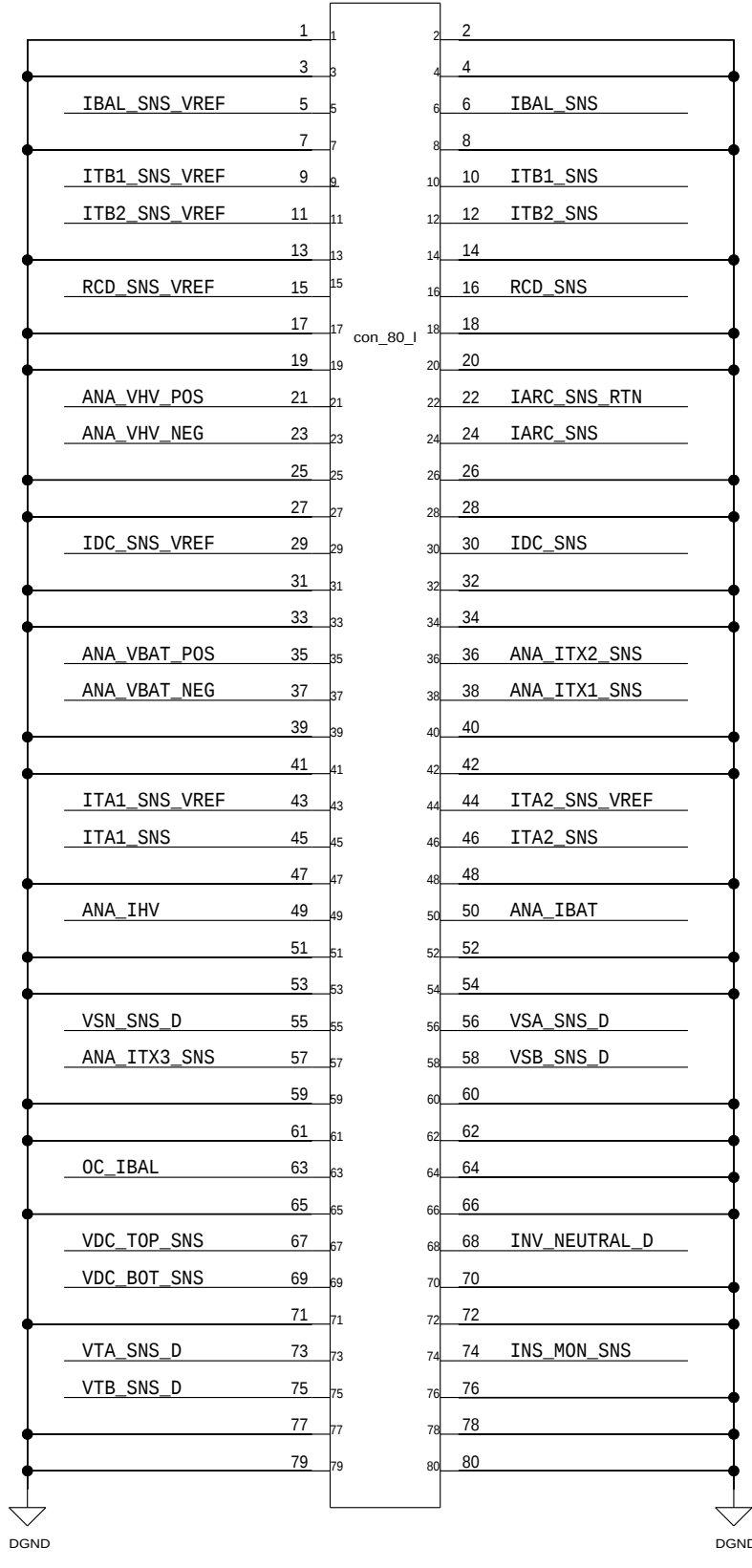
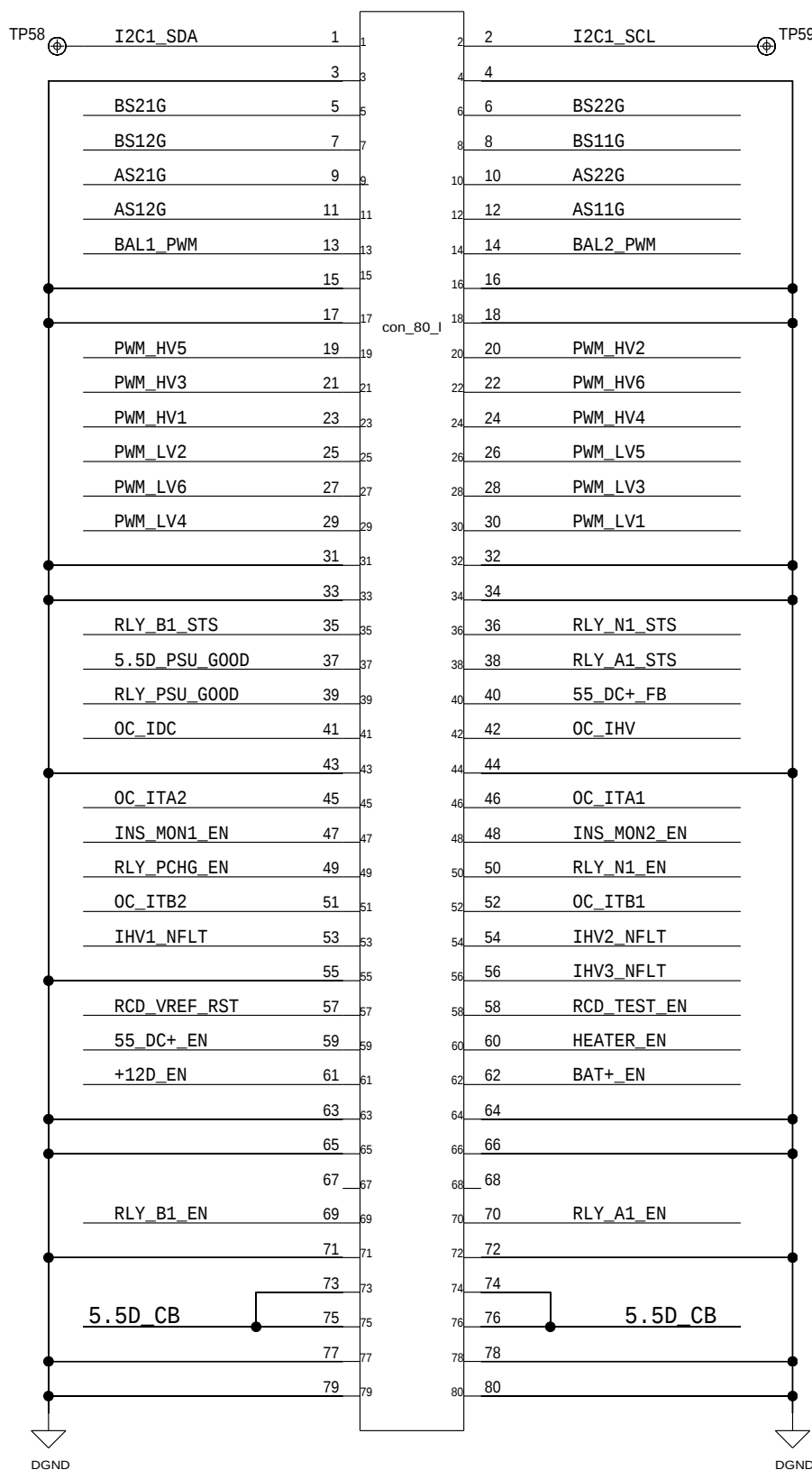
STATE :

TERRA CONTROL BOARD CONNECTORS

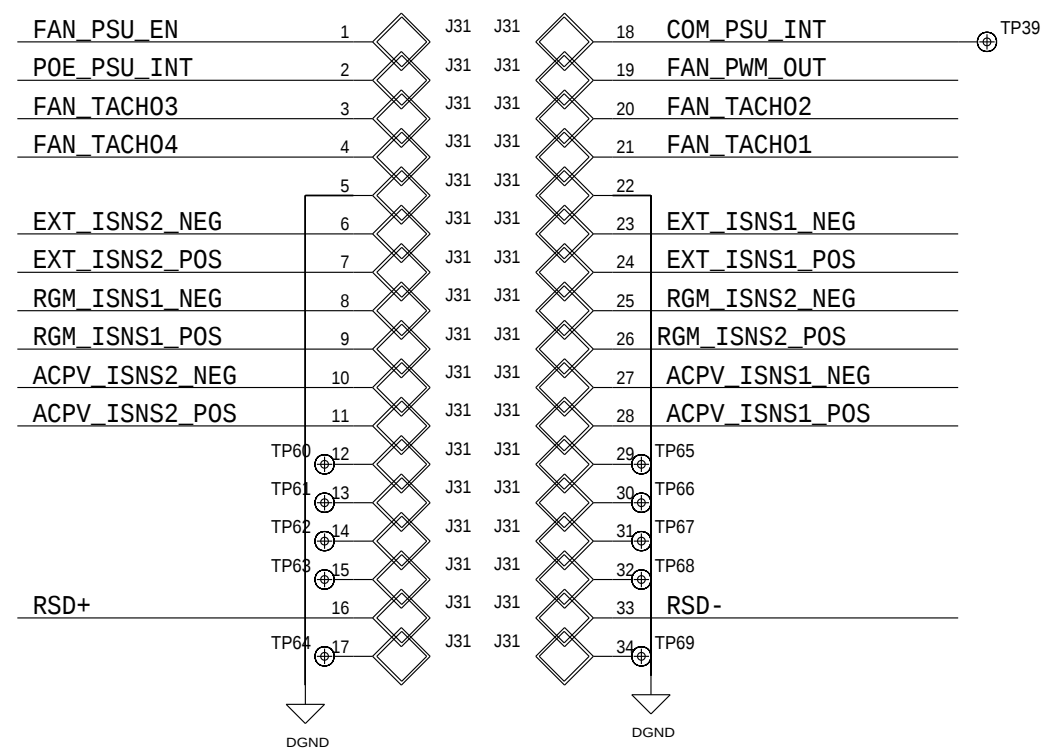
CONNECT TO J602

CONNECT TO J603

POWER CONNECTION TO CONTROL BOARD



RIBBON CABLE CONNECTOR TO MINI HIL INTERFACE BOARD



Block Name = top

NUMBER :

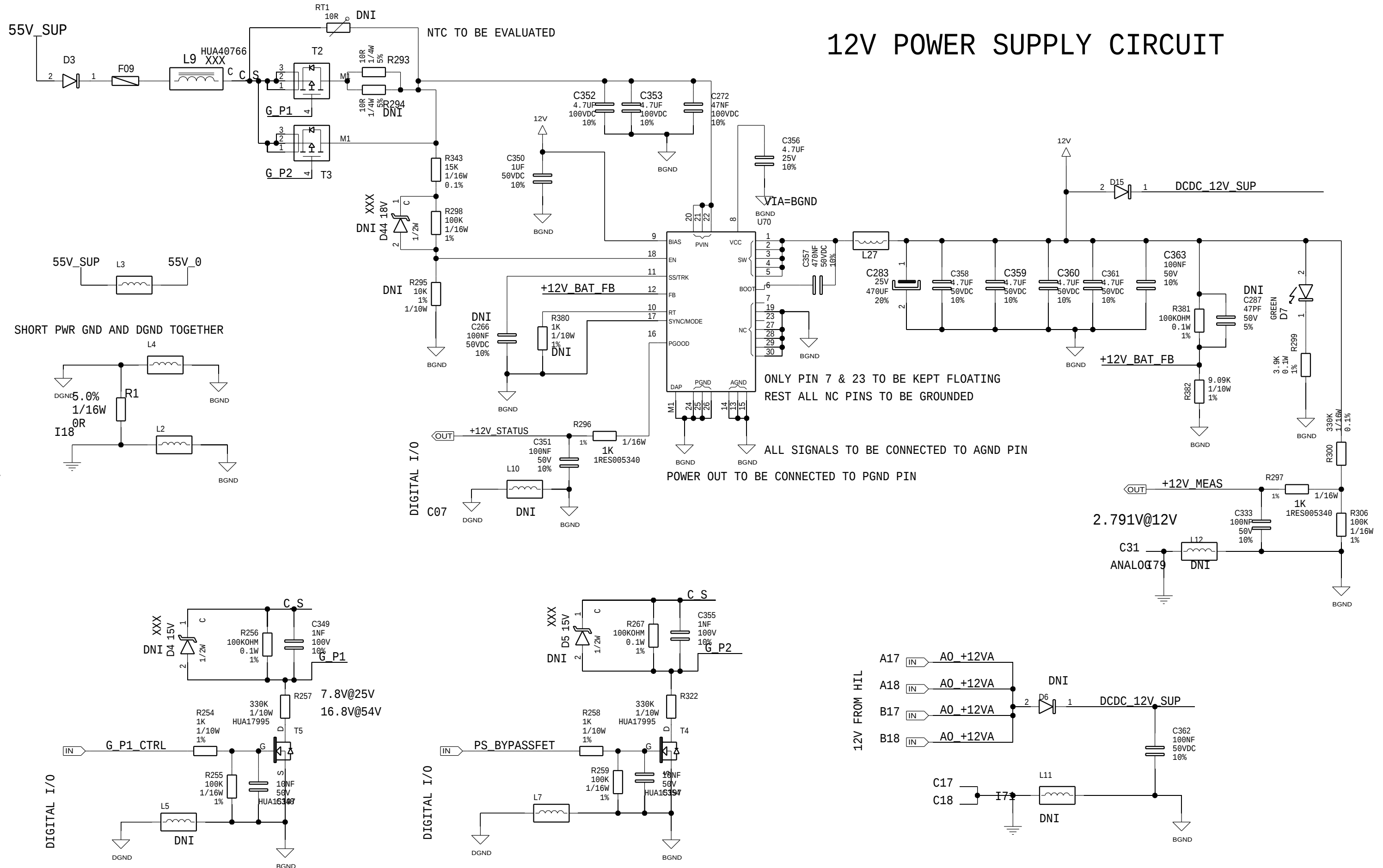
REVISION

SHEET : 3/20

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STATE :

12V POWER SUPPLY CIRCUIT



- PS TURN ON SEQUENCE:
1. G_P1_CTRL TO BE TURNED ON
 2. PS_BYPASSFET TO BE TURNED ON AFTER 1S

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NUMBER :	BLOCK NAME = top	REVISION	SHEET :
.	.	.	4/20

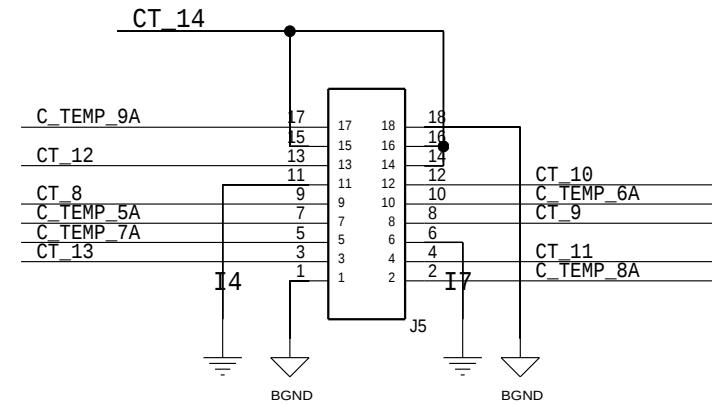
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STATE :

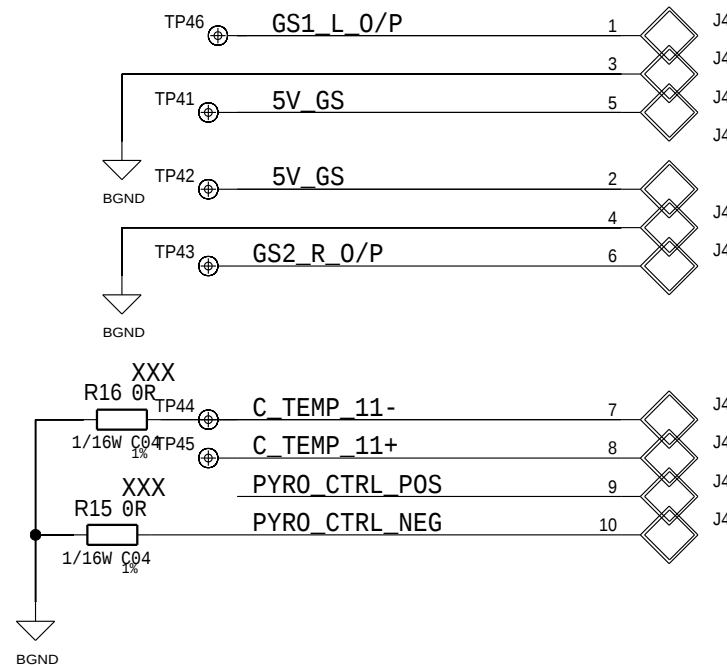
BMS CONNECTIONS

CONNECT TO J04

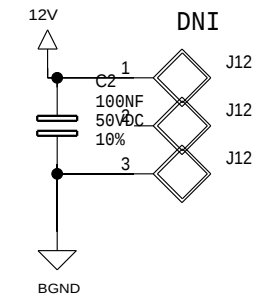
VBAT AND CT_14 SHORTED TOGETHER ON CTRL BRD



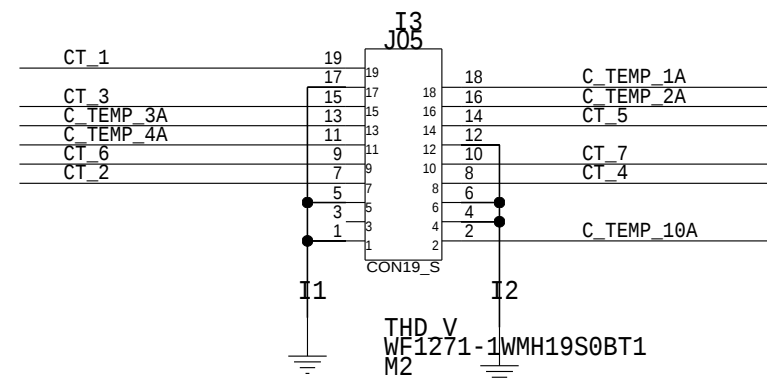
CONNECT TO J06



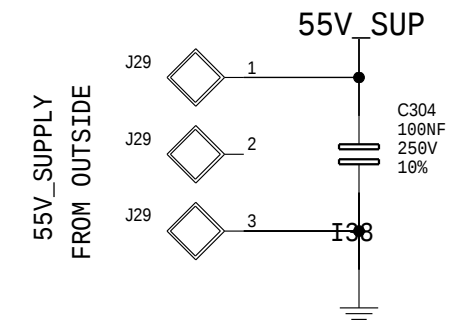
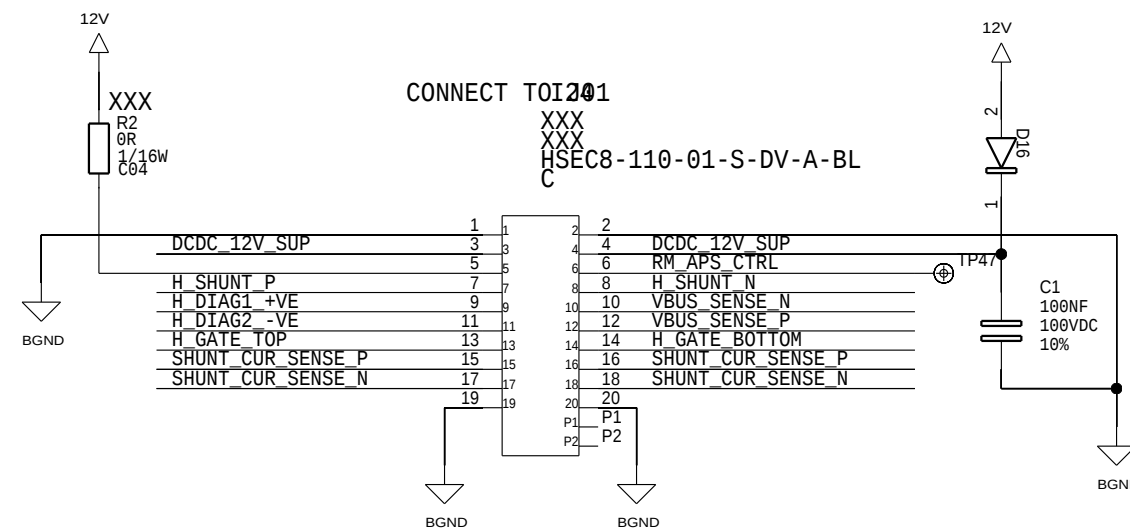
POWER CONNECTION TO BMS



CONNECT TO J05



CONNECT TO J01

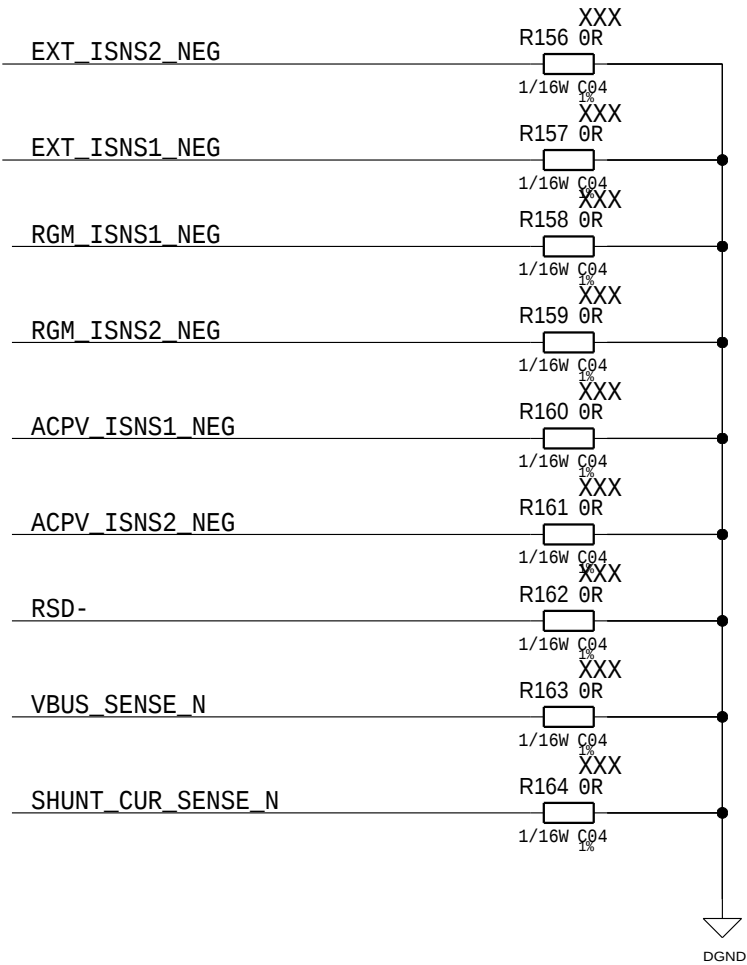
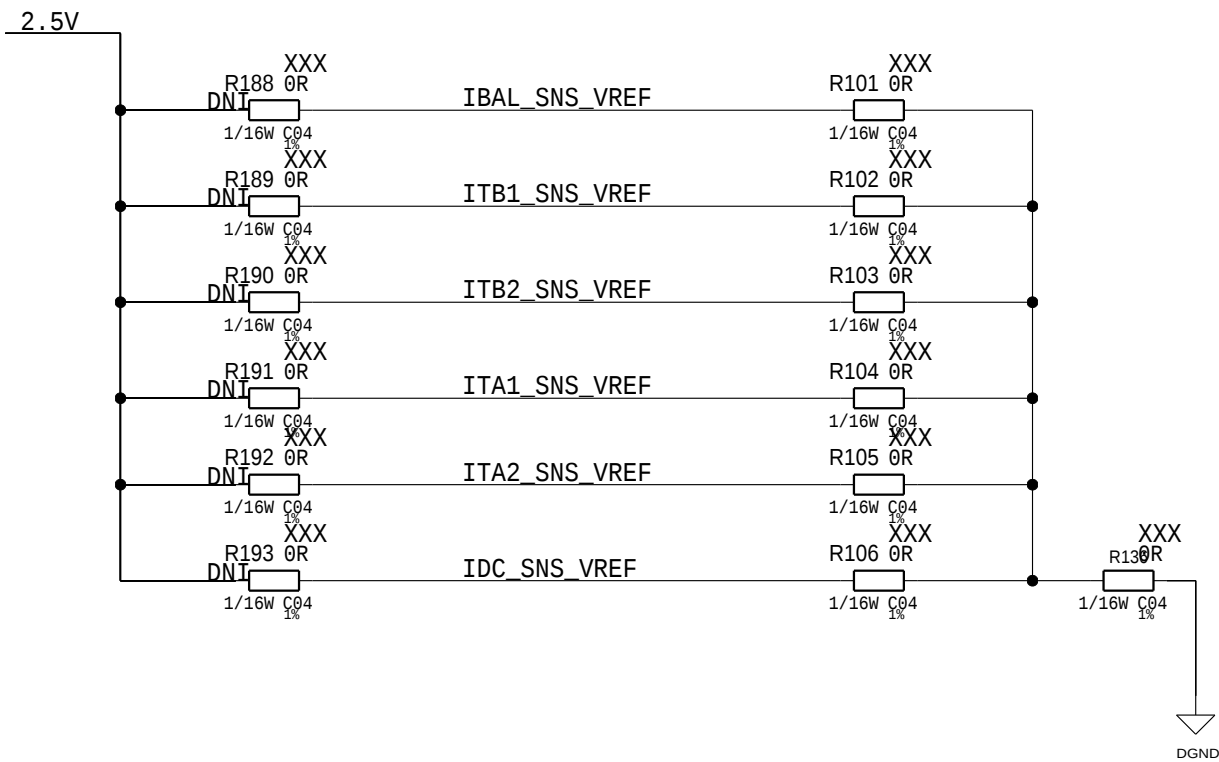


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STATIC SIGNALS

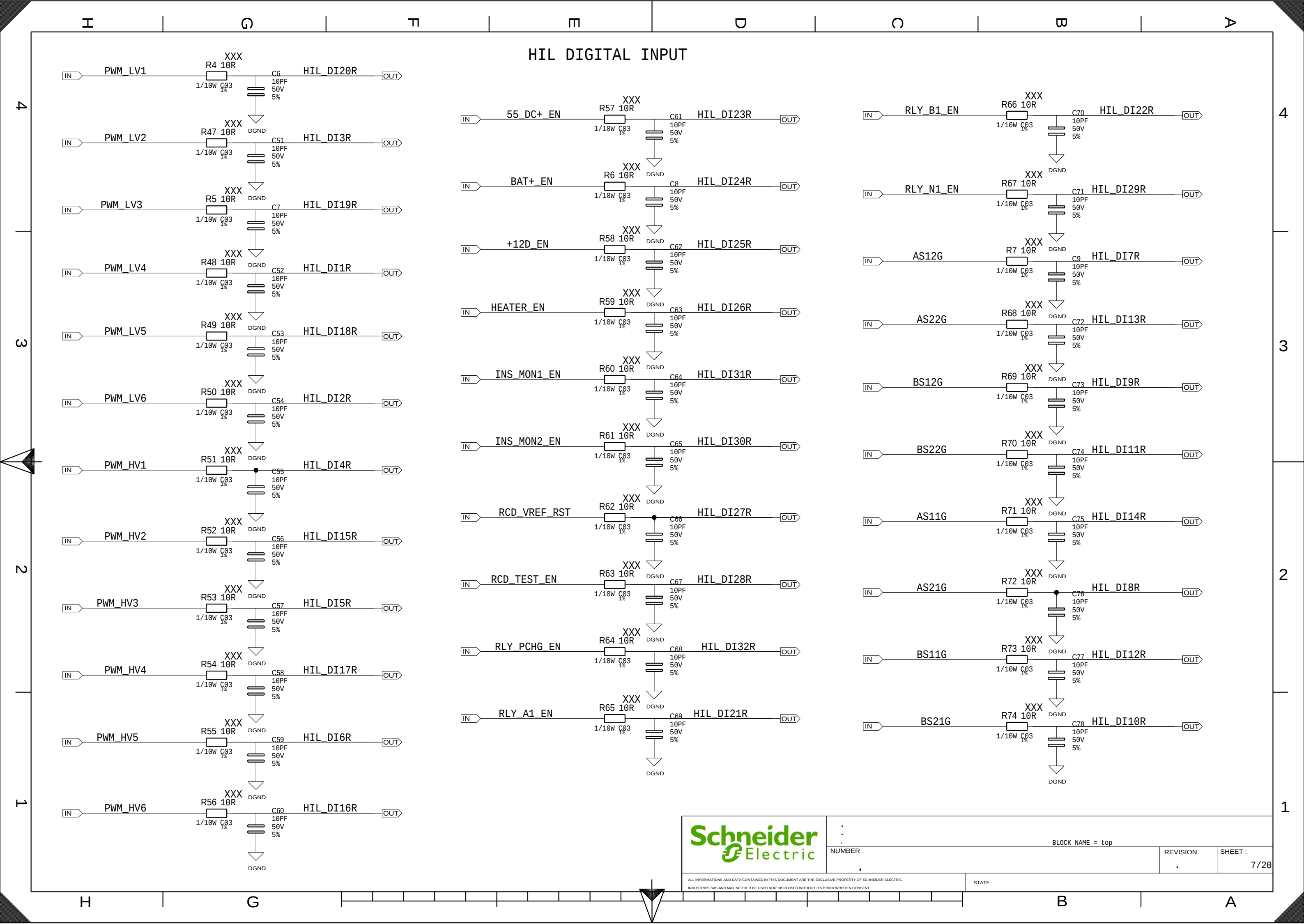
VREF SIGNALS PULLED TO GND



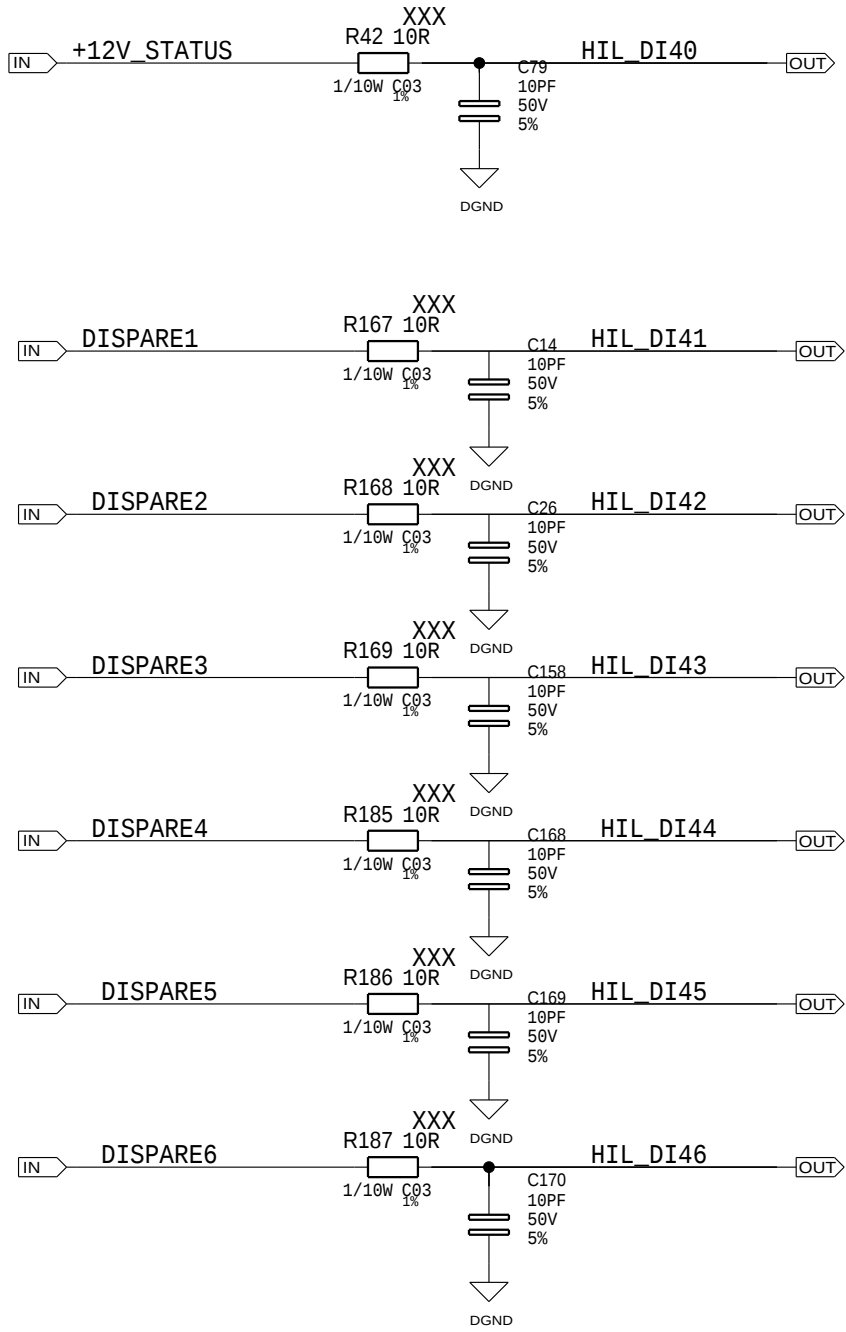
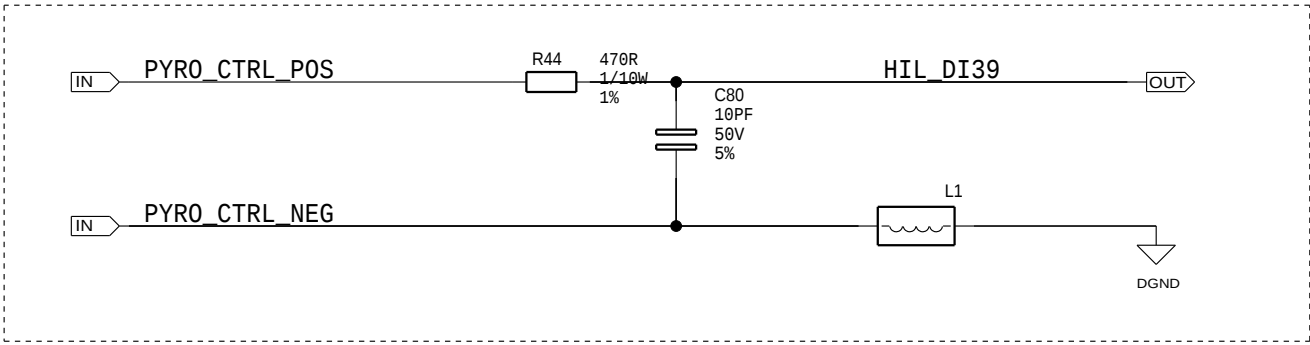
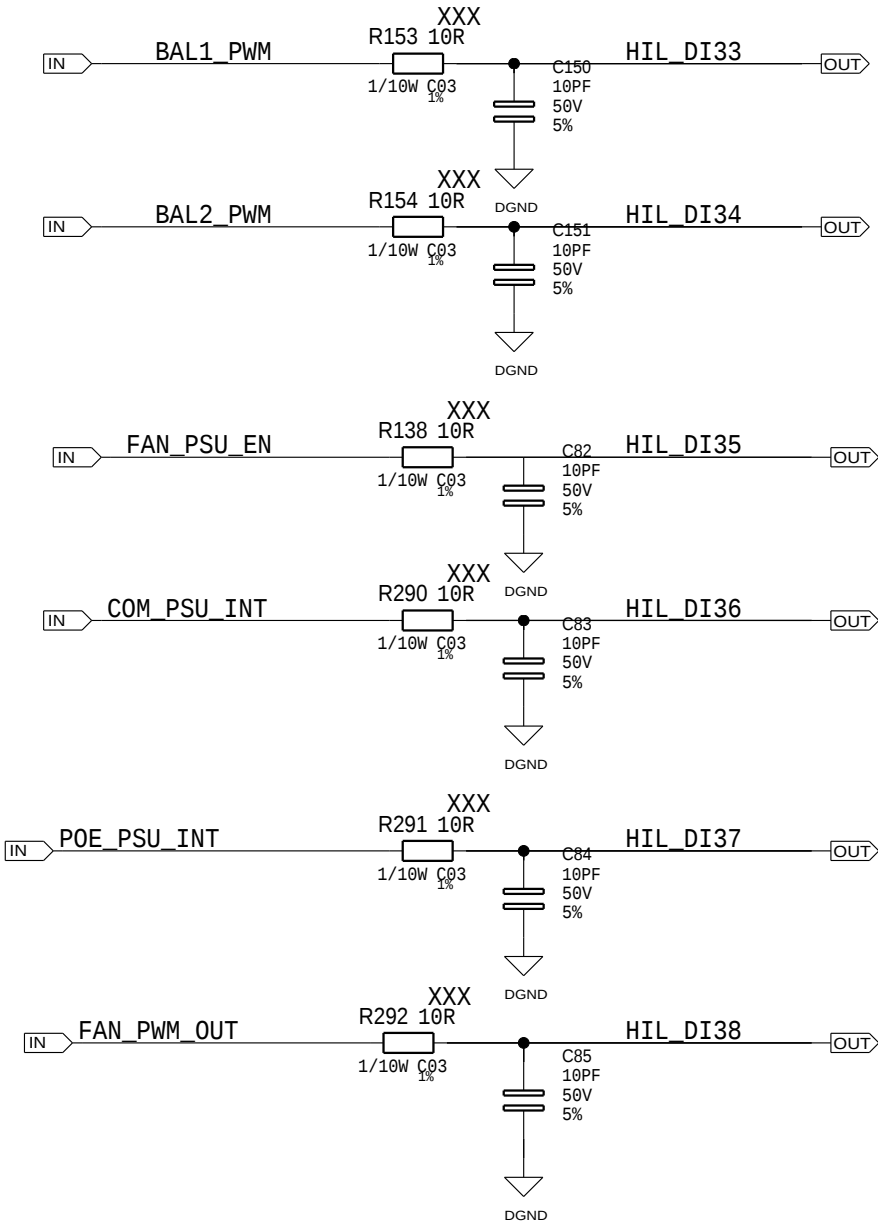
1A	1A	AISPARE1
1B	1B	AISPARE2
2A	2A	AISPARE3
2B	2B	AISPARE4
3A	3A	AISPARE5
3B	3B	AISPARE6
4A	4A	DISPARE1
4B	4B	DISPARE2
5A	5A	DISPARE3
5B	5B	DISPARE4
6A	6A	DISPARE5
6B	6B	DISPARE6
7A	7A	AOSPARE1
7B	7B	AOSPARE2
8A	8A	DOSPARE1
8B	8B	DOSPARE2
9A	9A	DOSPARE3
9B	9B	DOSPARE4
10A	10A	DOSPARE5
10B	10B	DOSPARE6
11A	11A	AOSPARE3
11B	11B	AOSPARE4

12A	12A	TP54
12B	12B	TP49
13A	13A	TP55
13B	13B	TP50
14A	14A	TP56
14B	14B	TP51
15A	15A	TP57
15B	15B	TP52
16A	16A	TP53
16B	16B	

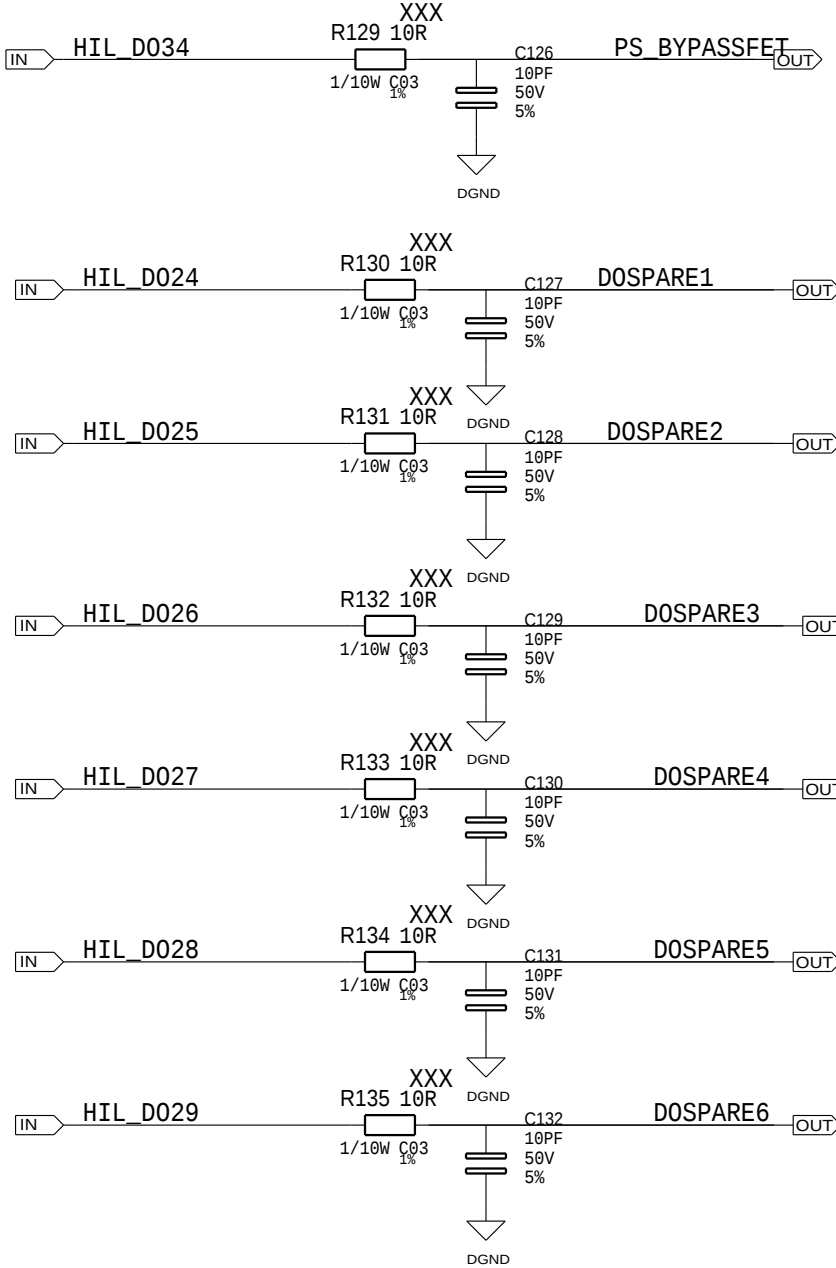
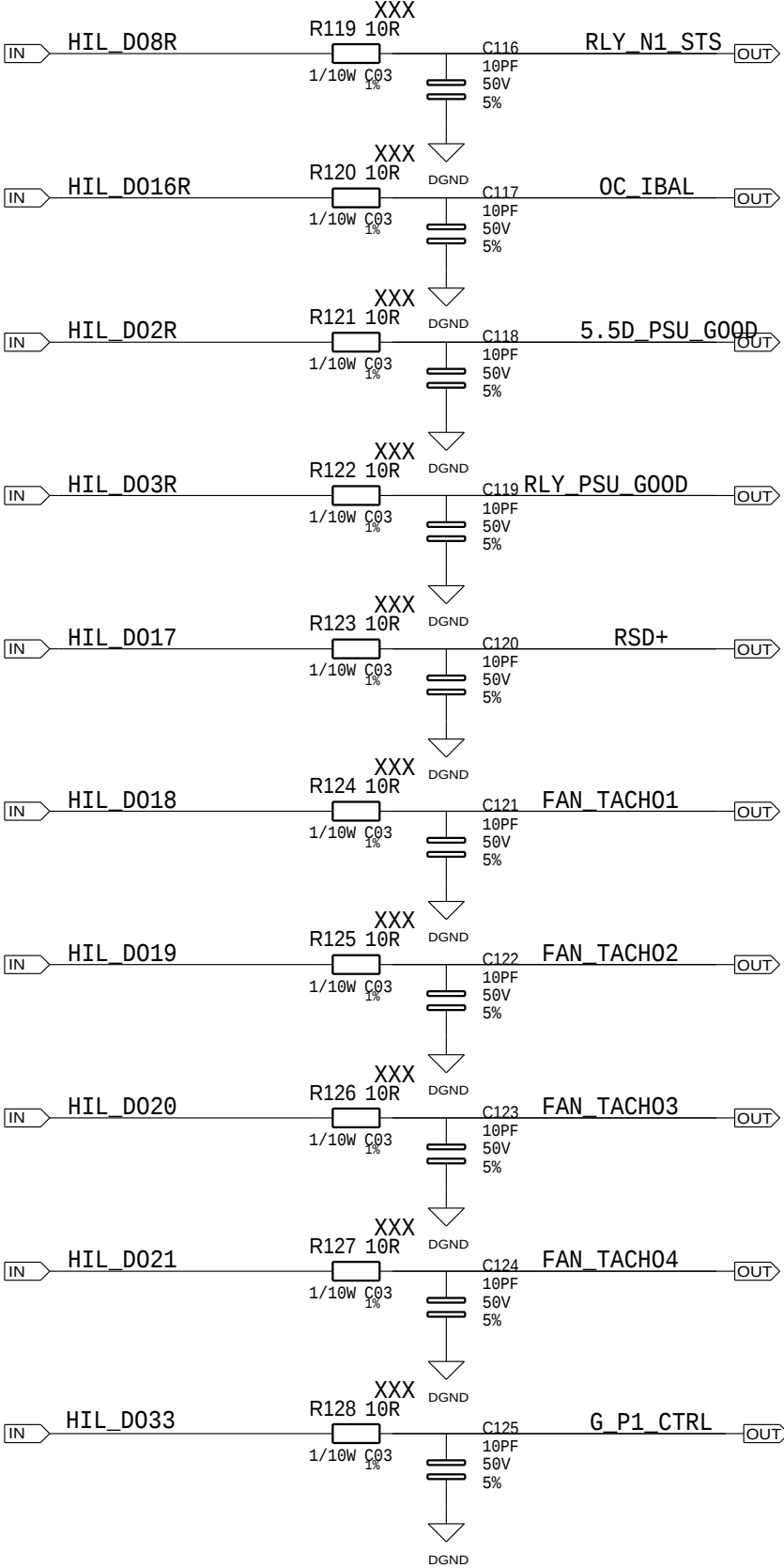
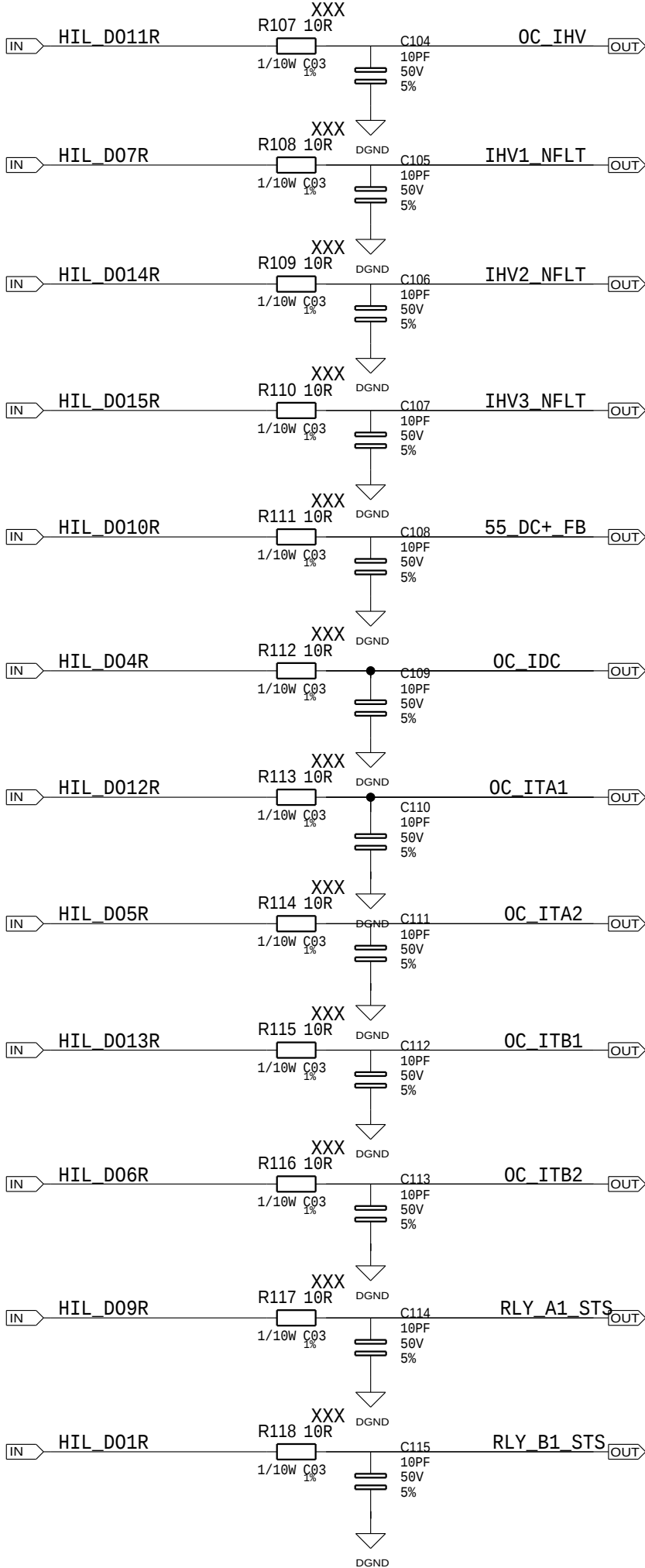
XXX
TBL009V-254-16GY-2GY
X



HIL DIGITAL INPUT



HIL DIGITAL OUTPUT

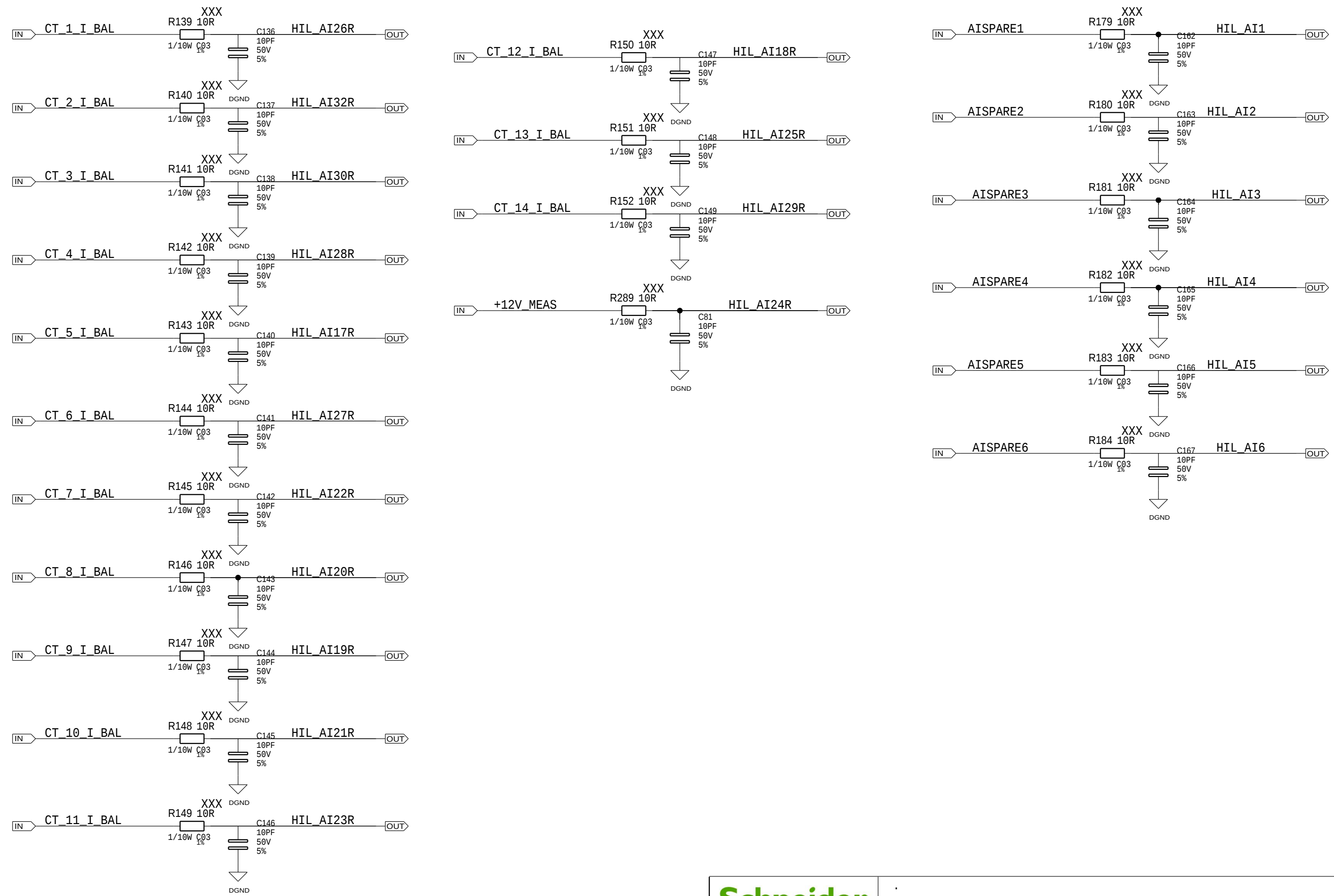


: DIGITAL IO SIGNALS

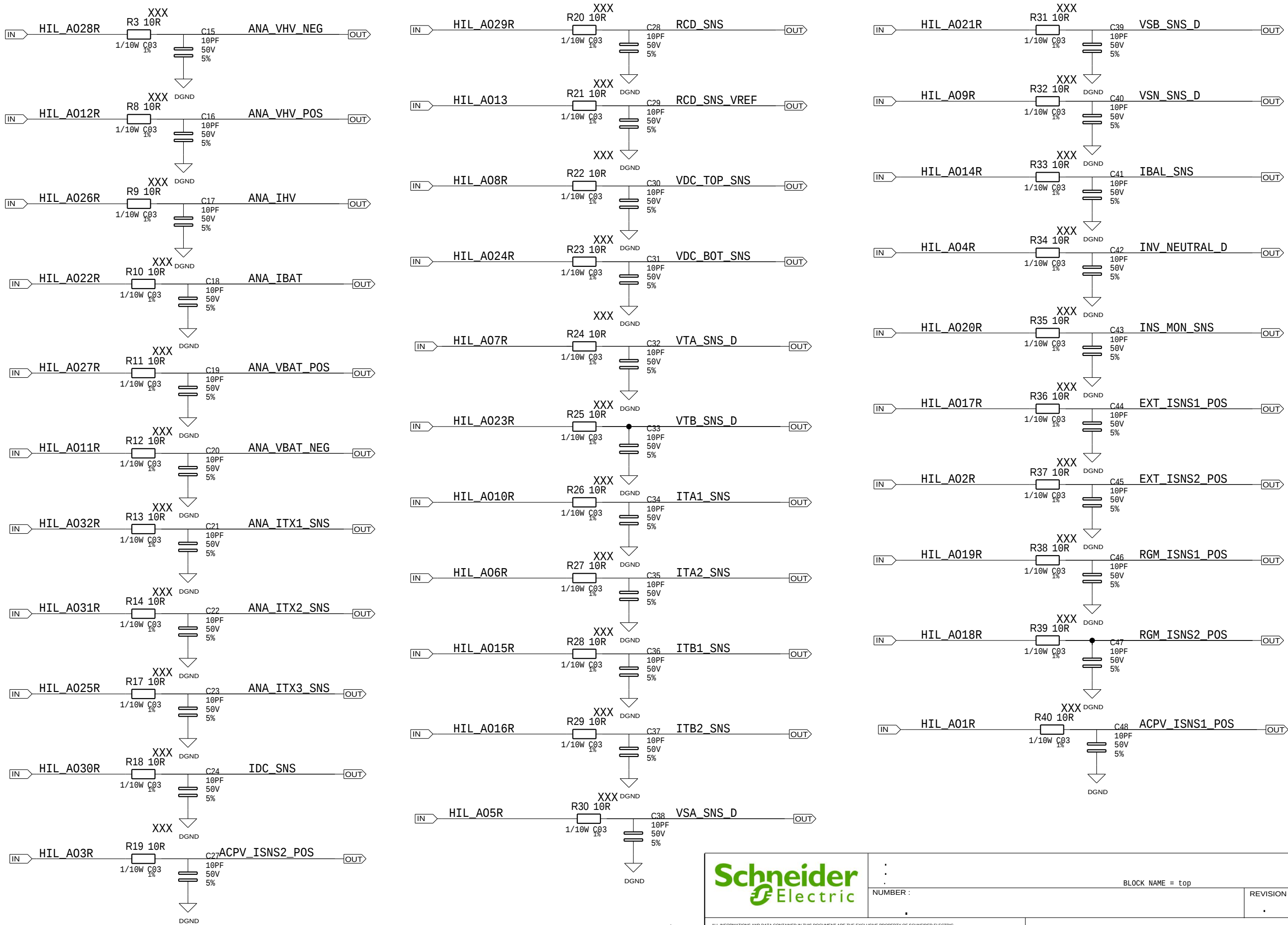
NUMBER :	BLOCK NAME = top	REVISION	SHEET :
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HIL ANALOG INPUT



HIL ANALOG OUTPUT

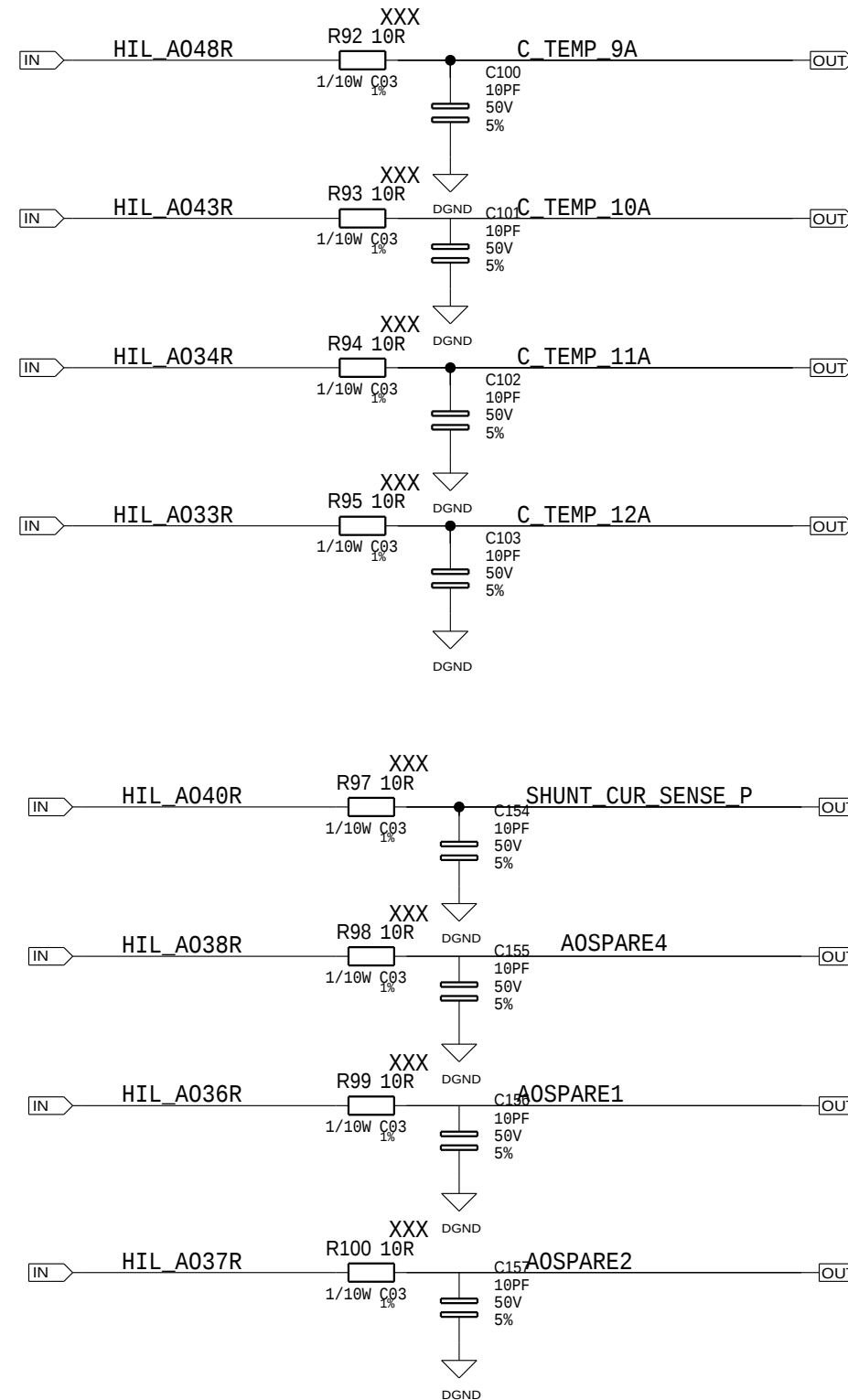
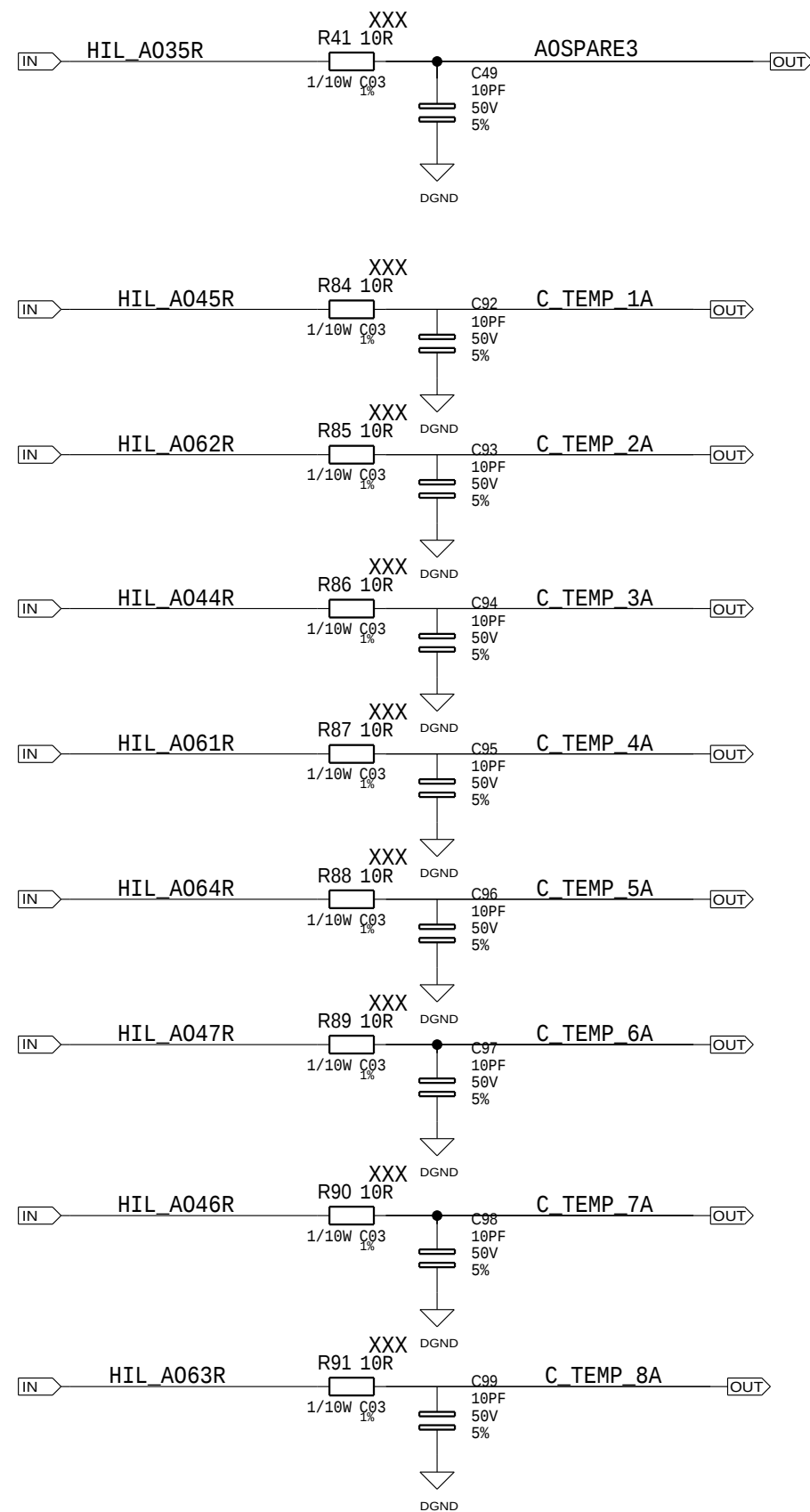


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NUMBER :	BLOCK NAME = top	REVISION	SHEET :
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STATE :

HIL ANALOG OUTPUT

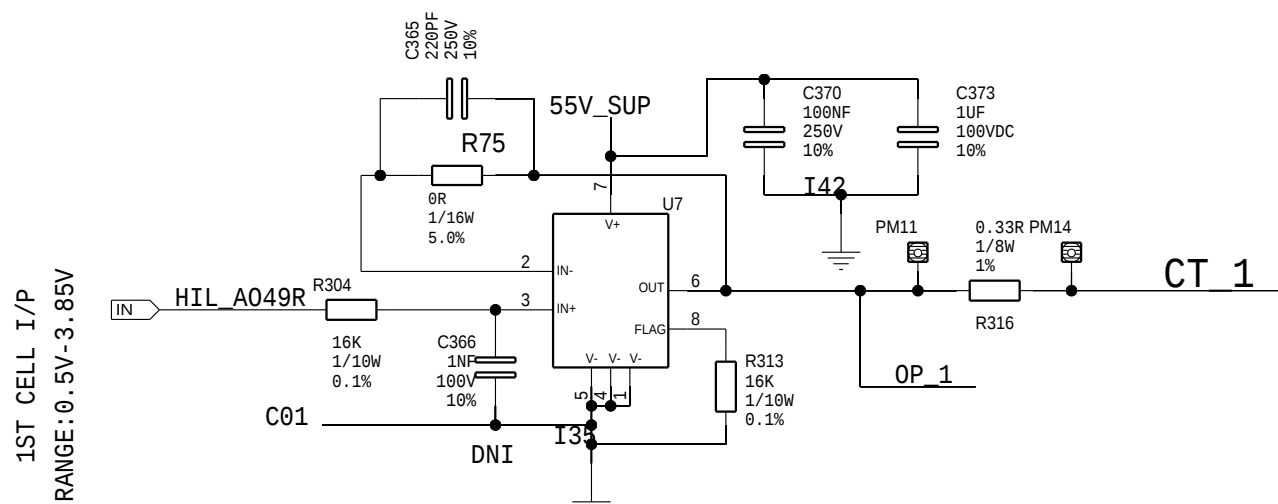


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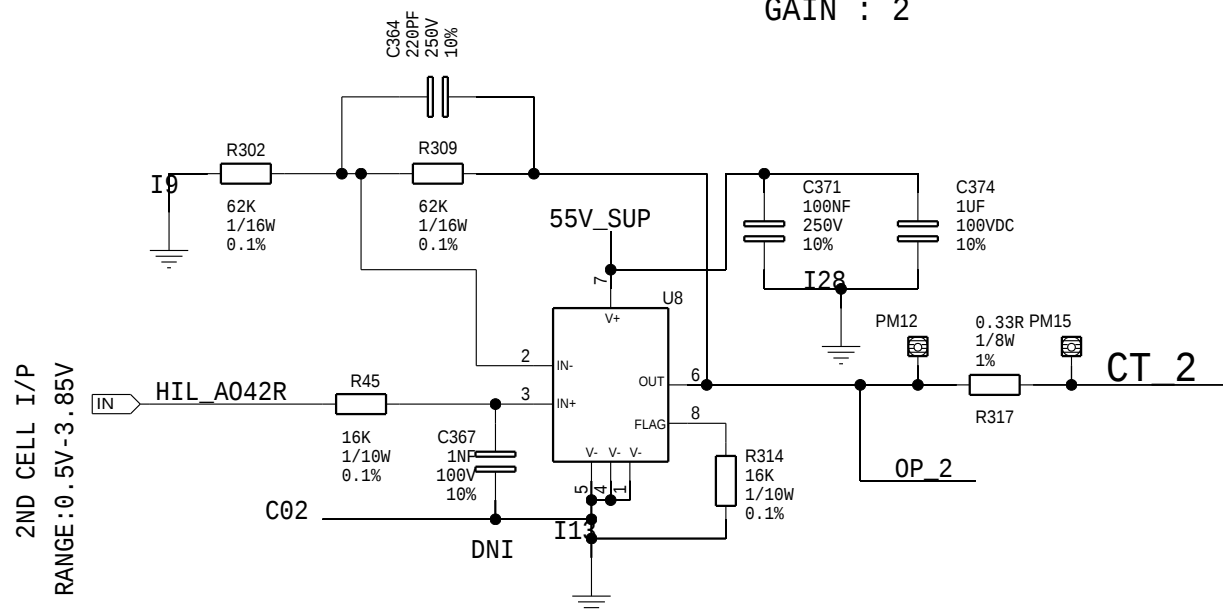
NUMBER :	BLOCK NAME = top	REVISION	SHEET : 12/20
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STATE :

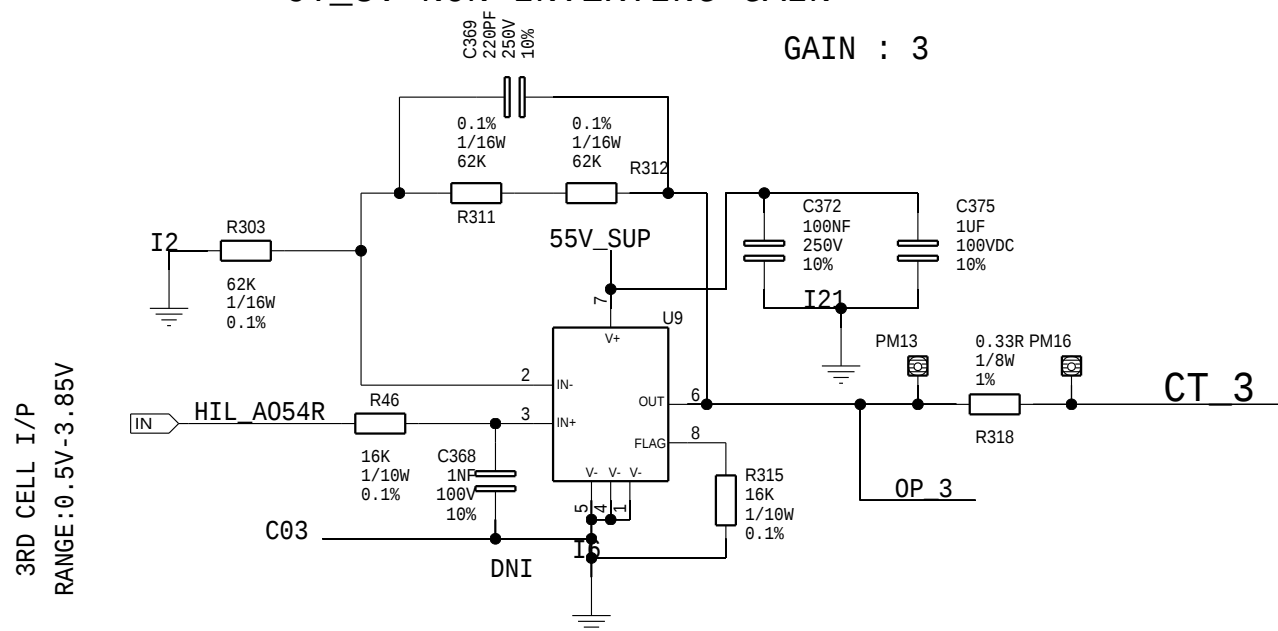
CT_1: BUFFER
GAIN : 1



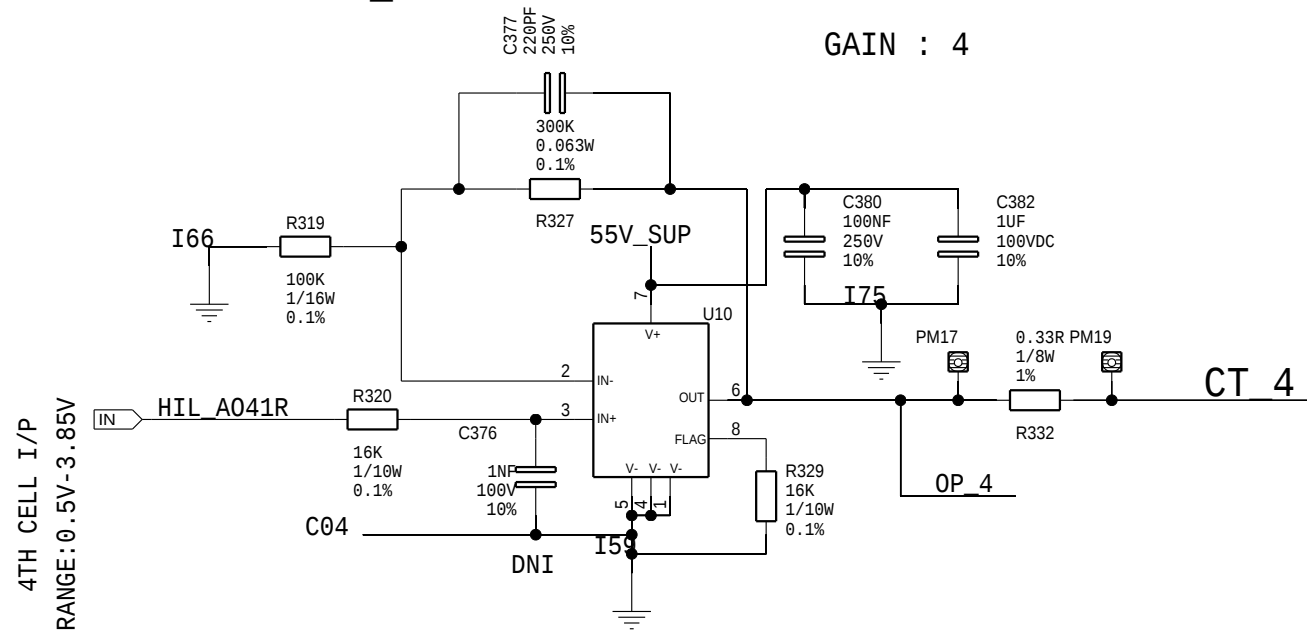
CT_2: NON-INVERTING GAIN
GAIN : 2



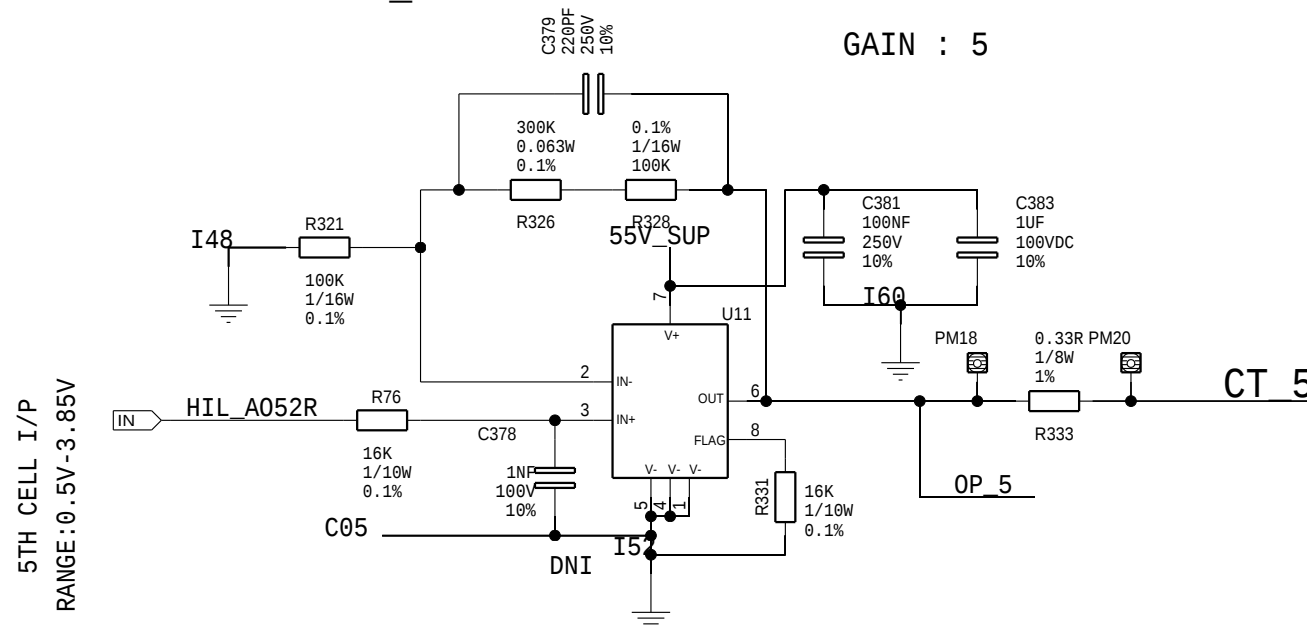
CT_3: NON-INVERTING GAIN
GAIN : 3



CT_4: NON-INVERTING GAIN
GAIN : 4



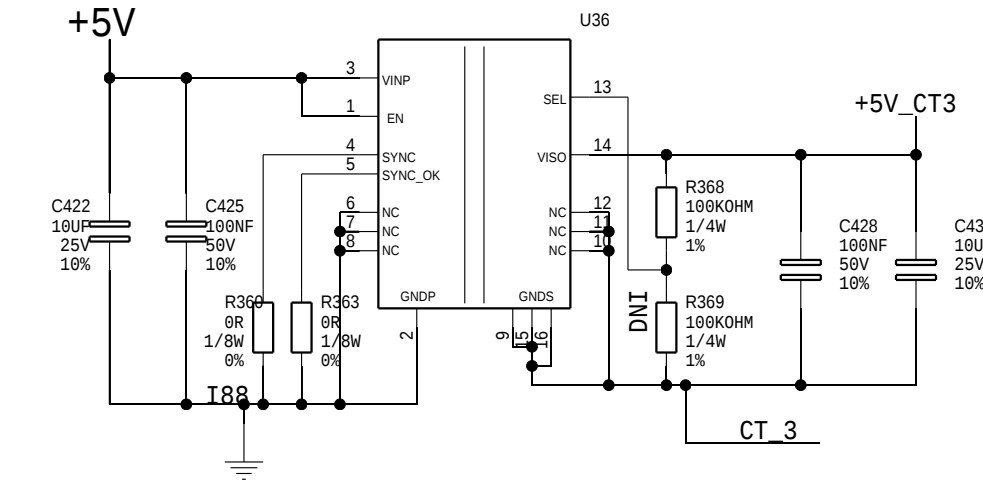
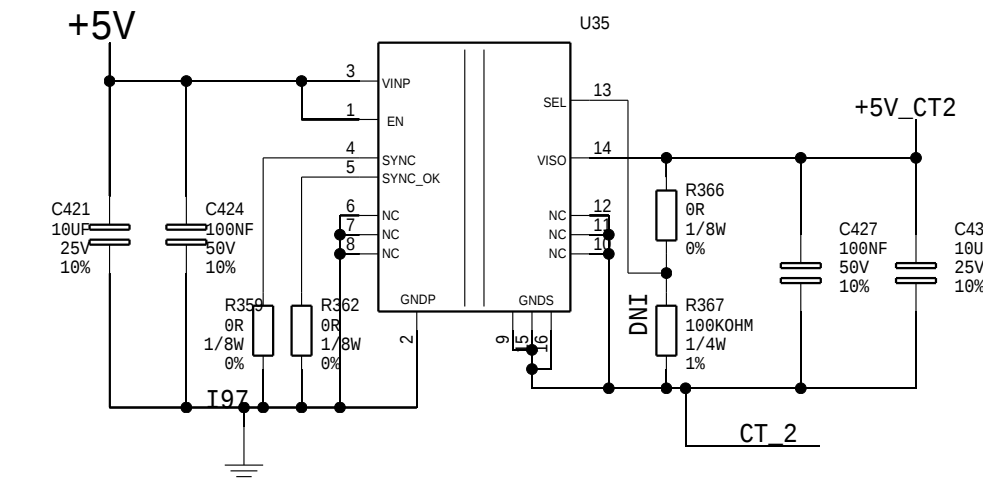
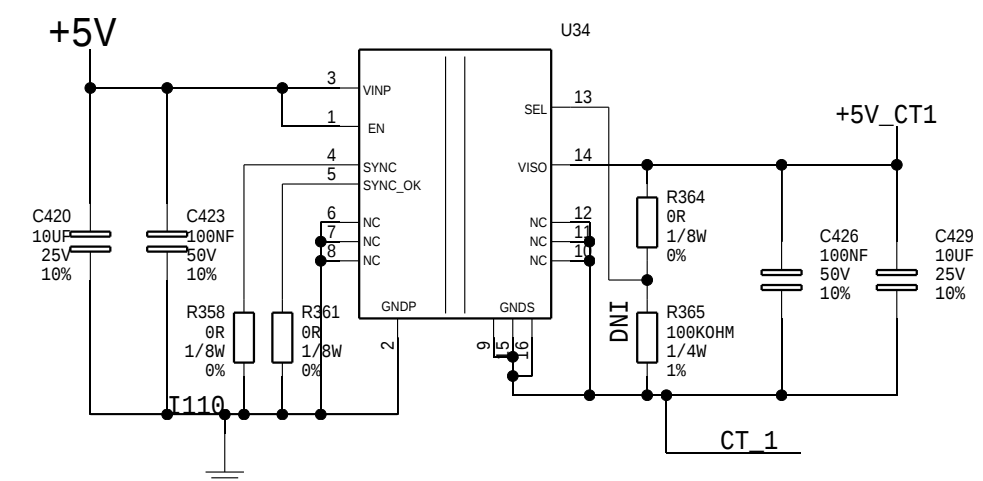
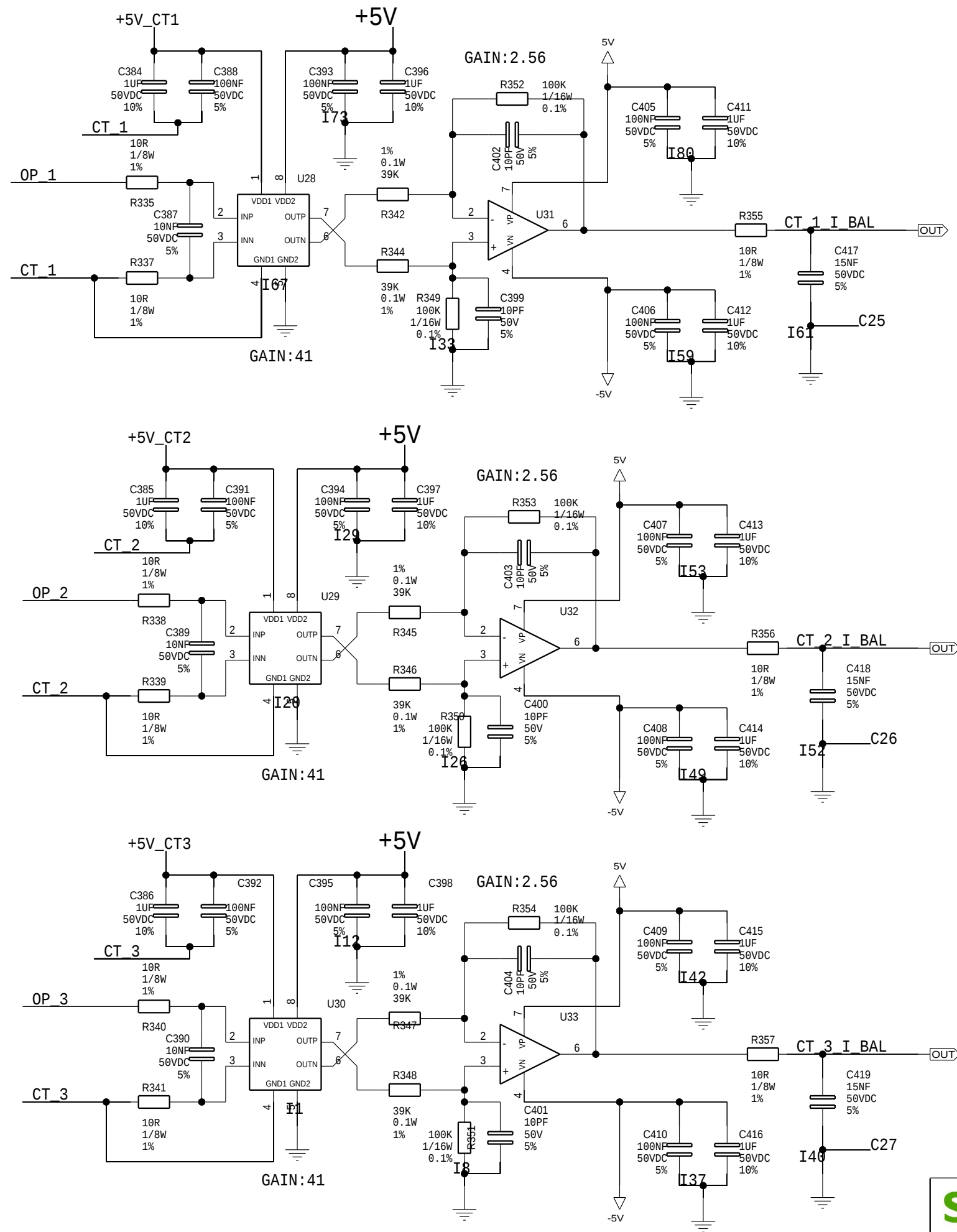
CT_5: NON-INVERTING GAIN
GAIN : 5



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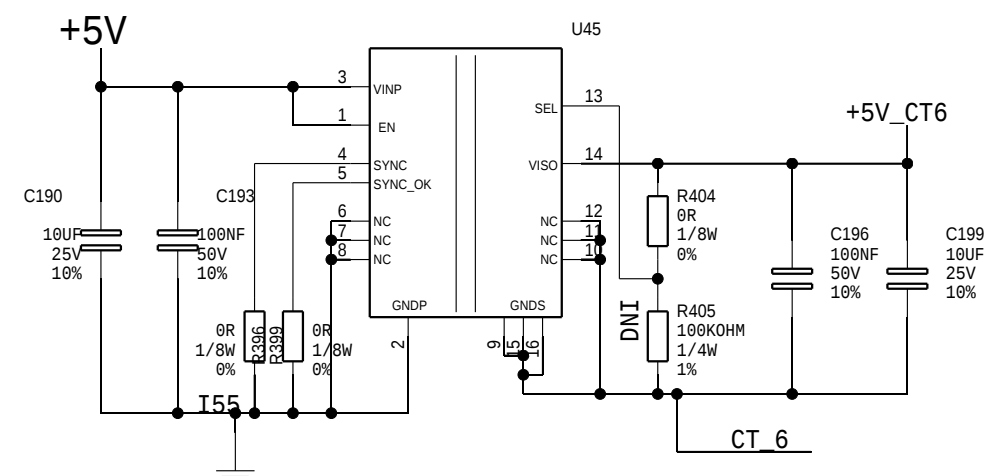
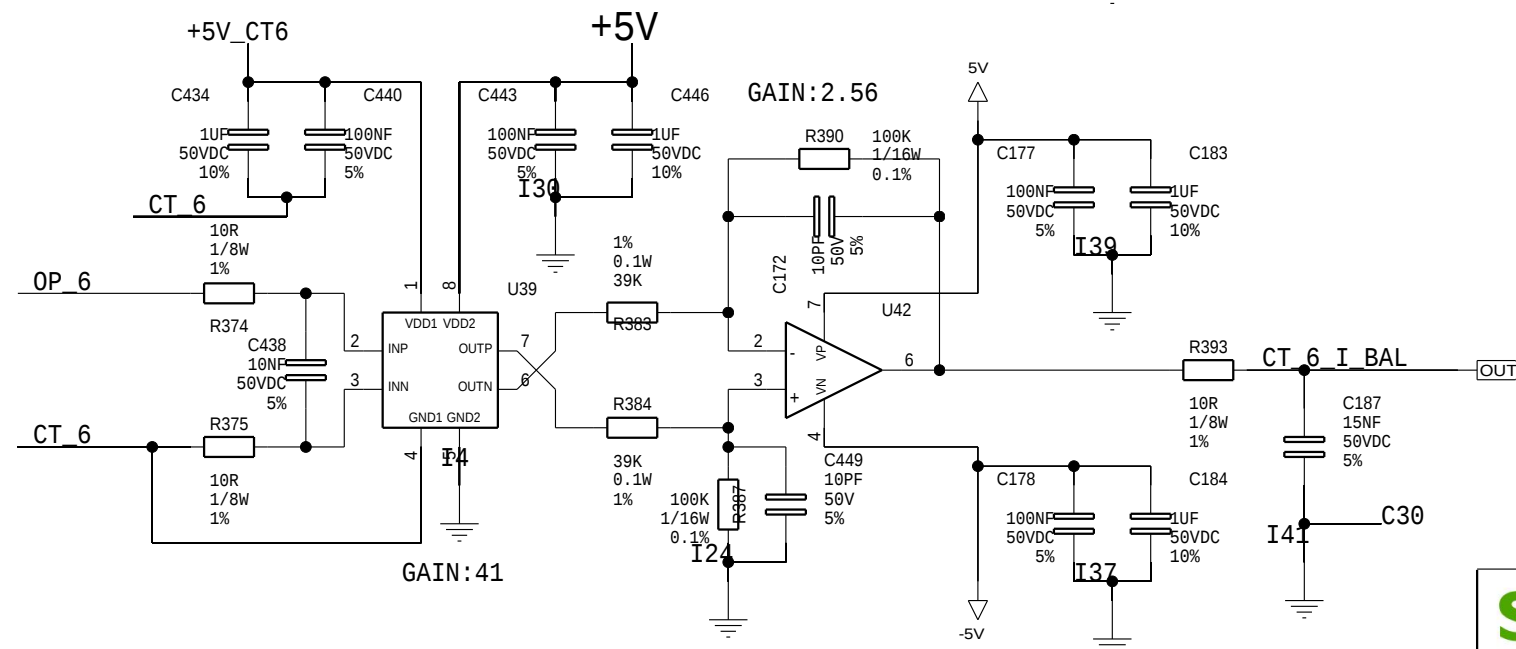
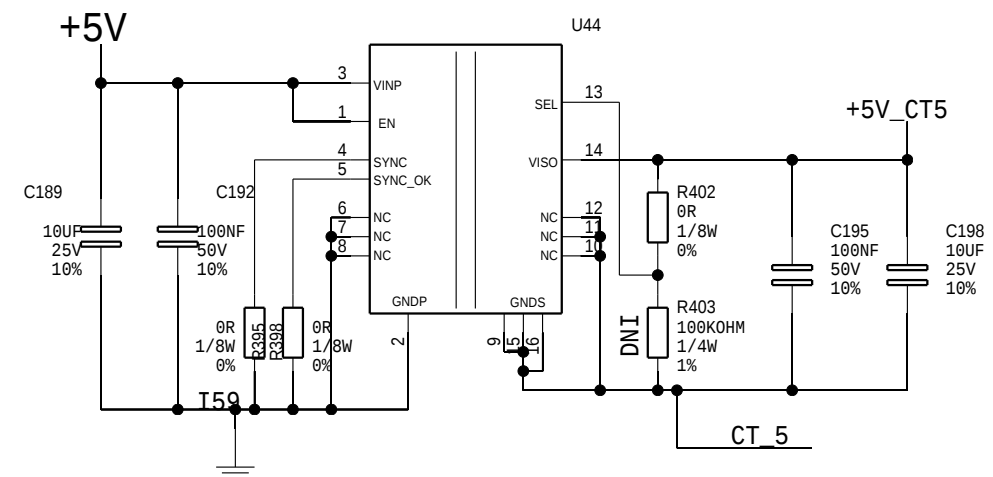
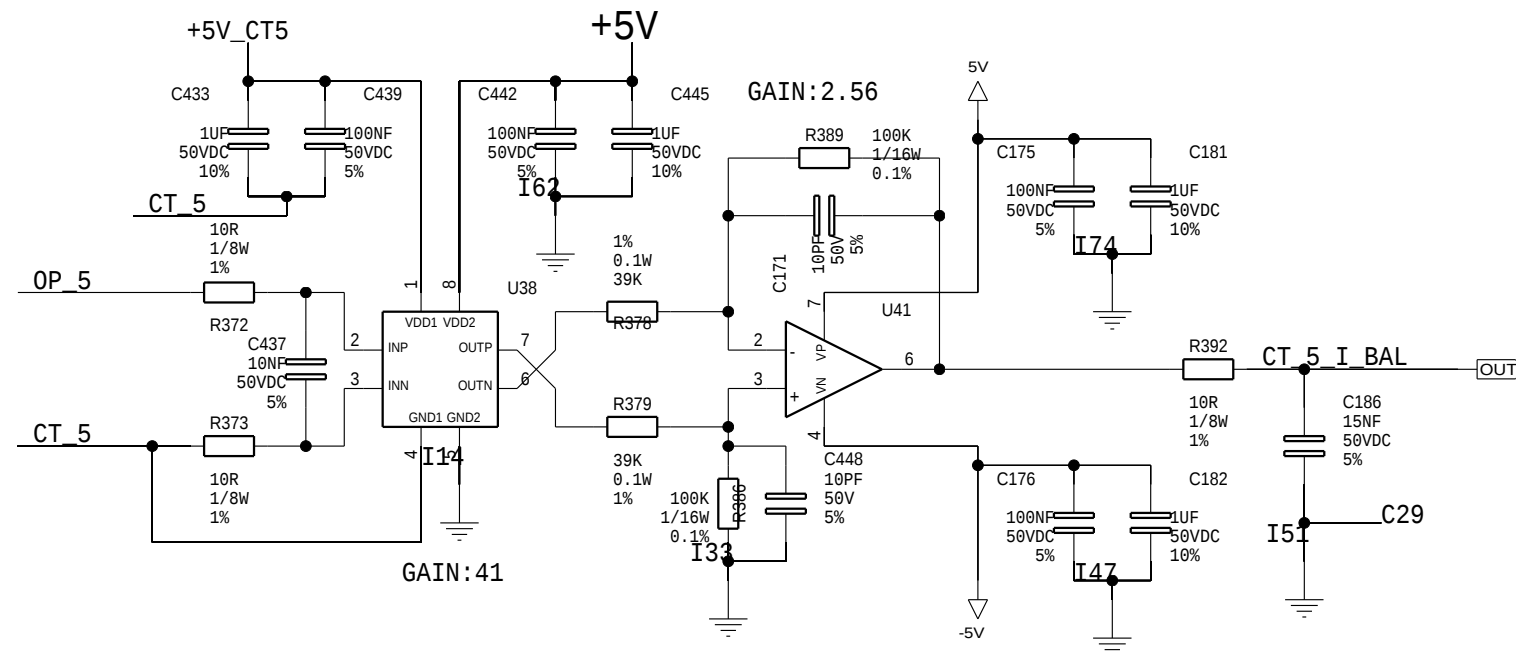
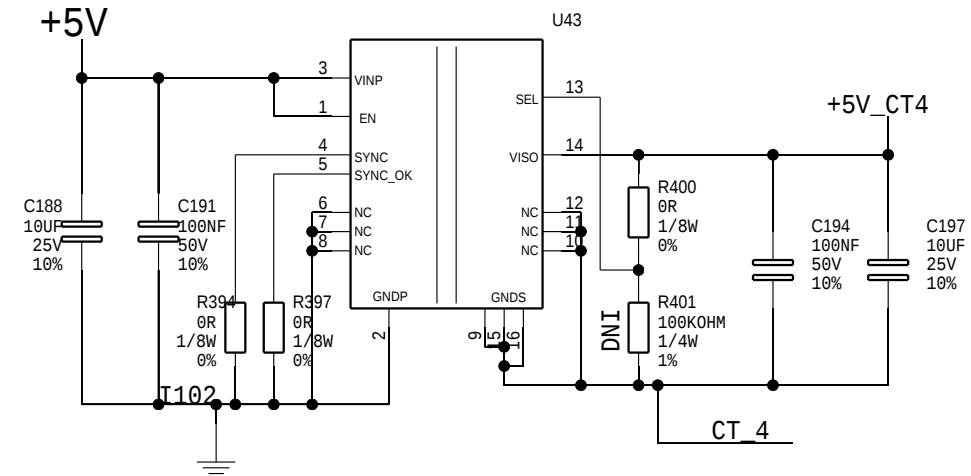
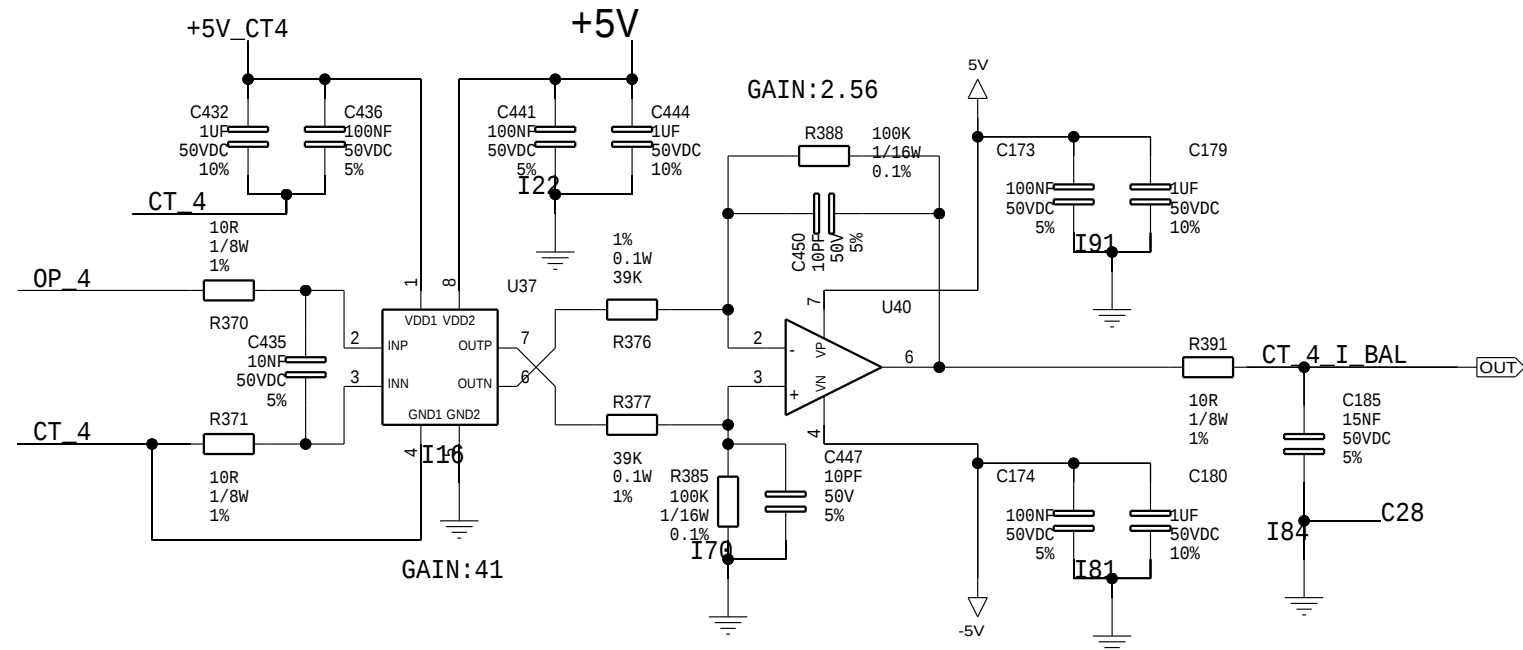
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STATE :



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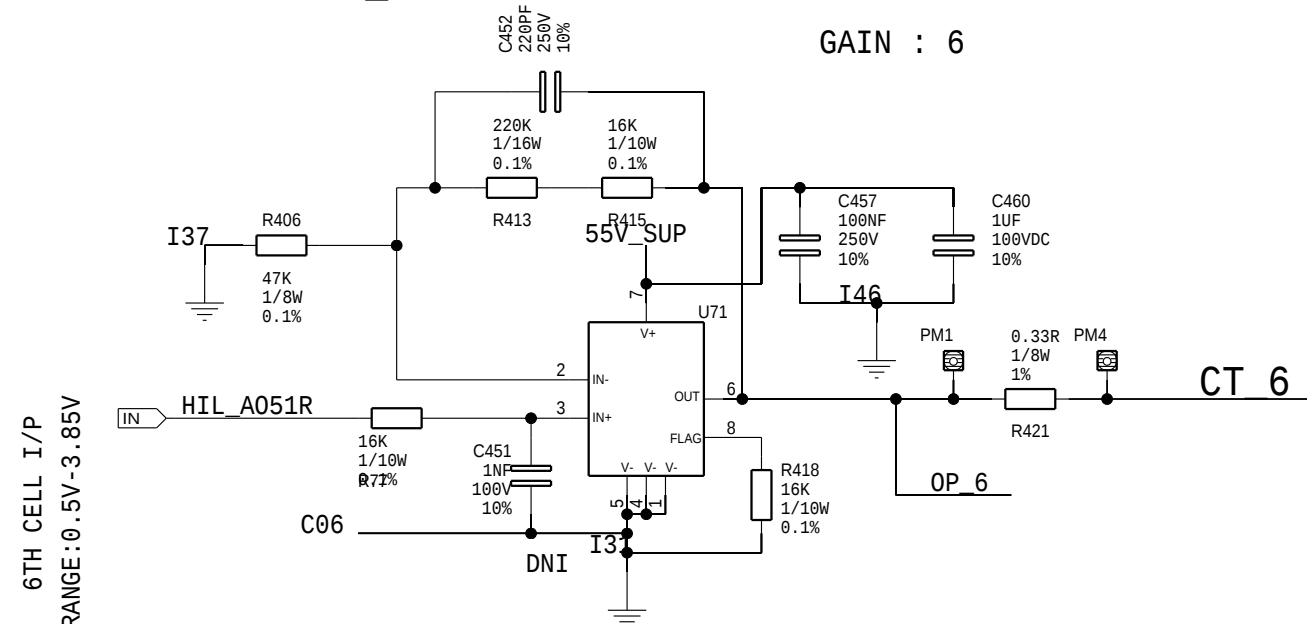
NUMBER :	BLOCK NAME = top	REVISION	SHEET :
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STATE :

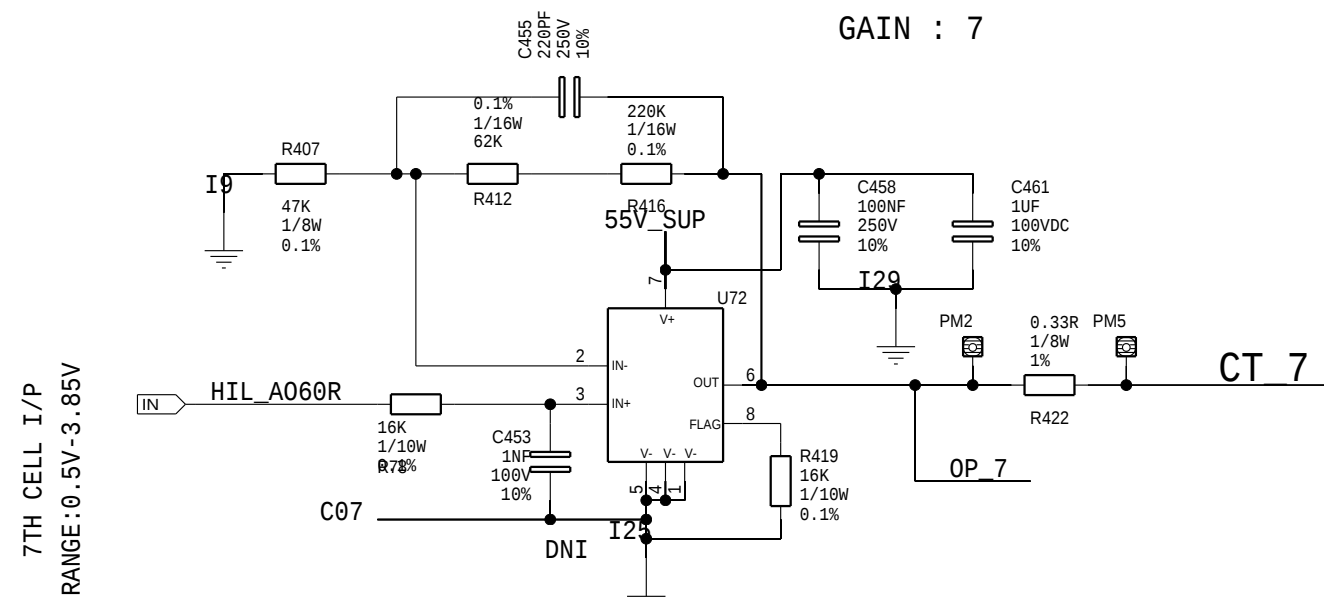
CT_6: NON-INVERTING GAIN

GAIN : 6



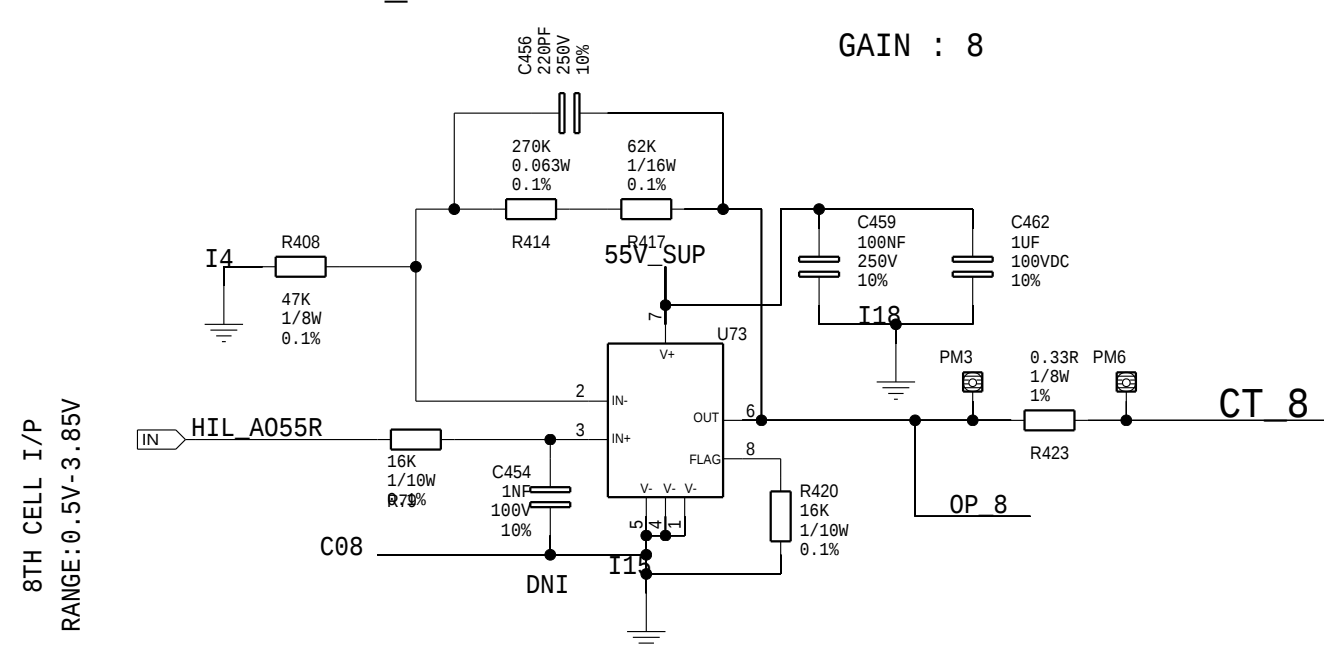
CT_7: NON-INVERTING GAIN

GAIN : 7



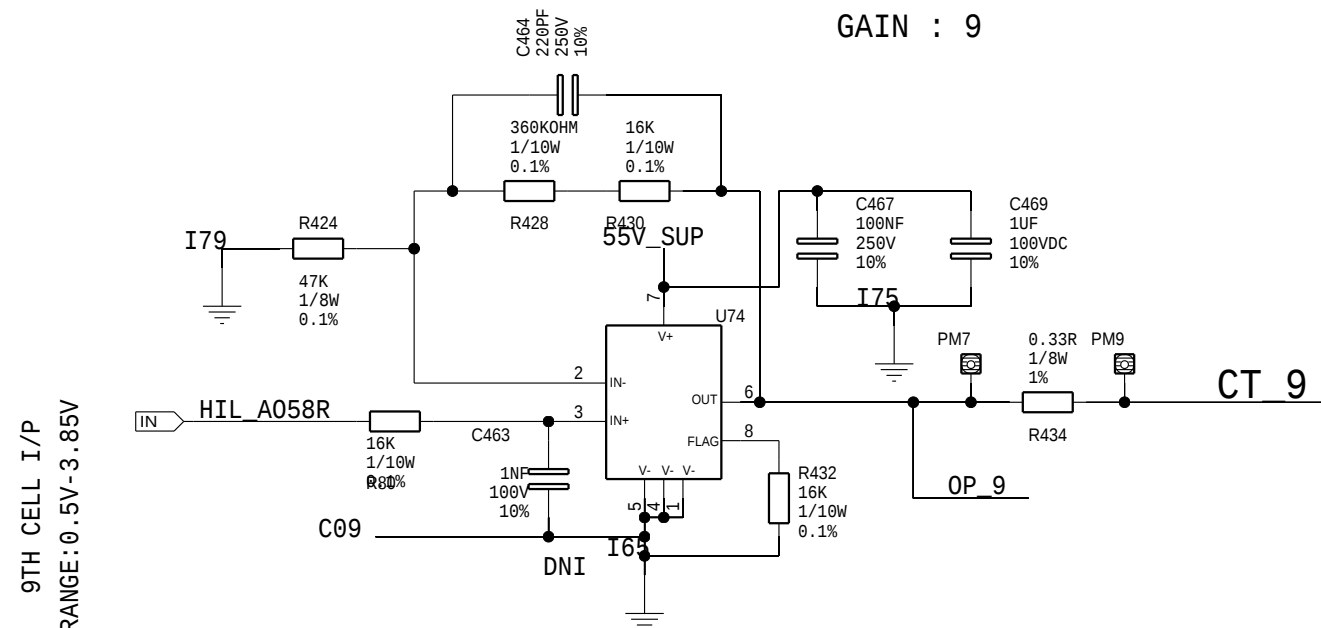
CT_8: NON-INVERTING GAIN

GAIN : 8



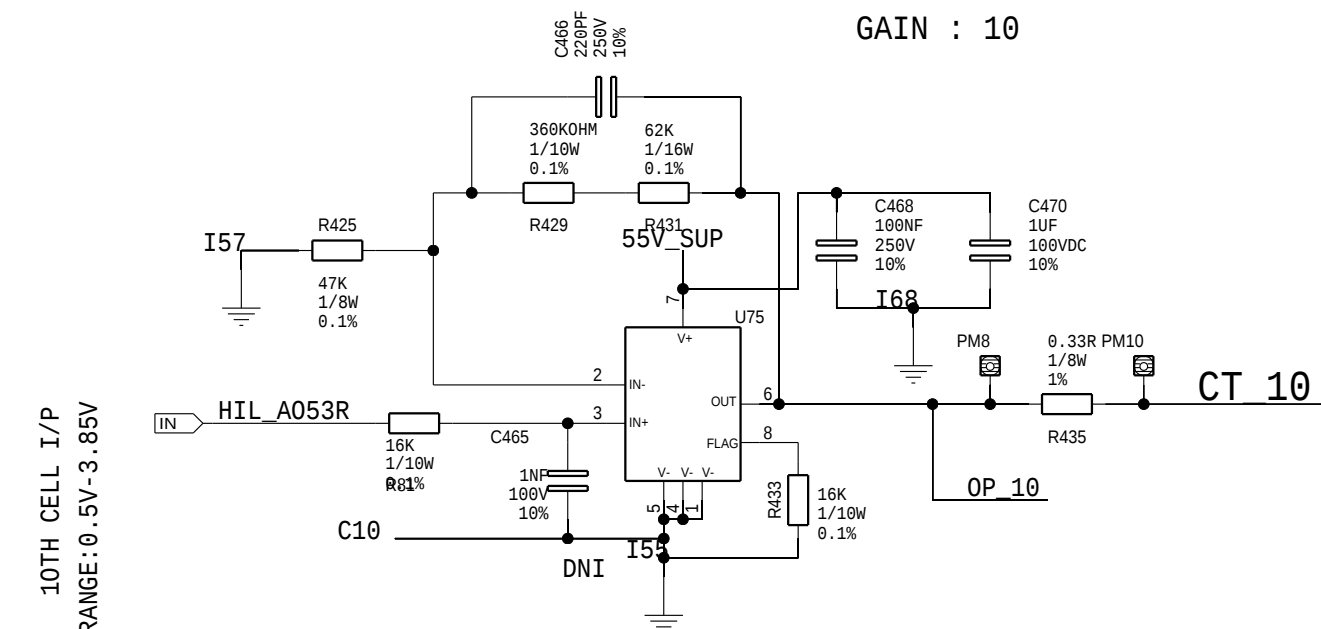
CT_9: NON-INVERTING GAIN

GAIN : 9



CT_10: NON-INVERTING GAIN

GAIN : 10

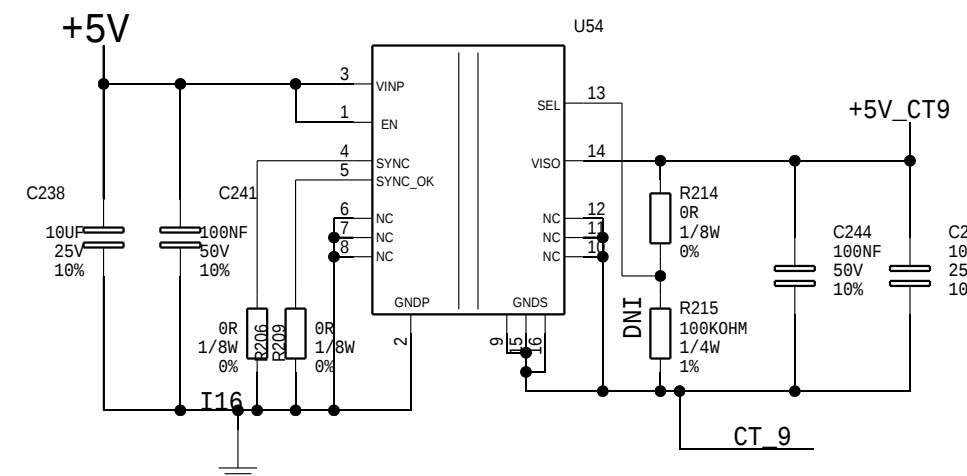
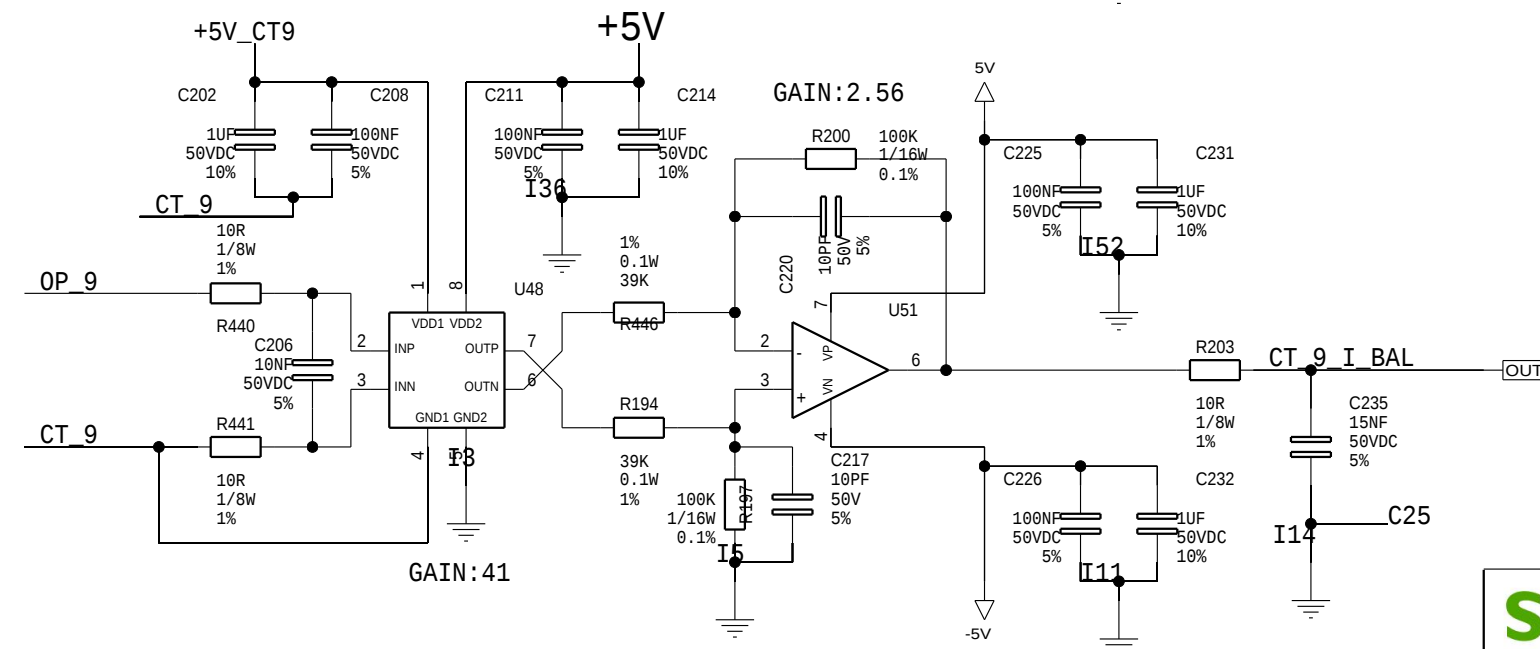
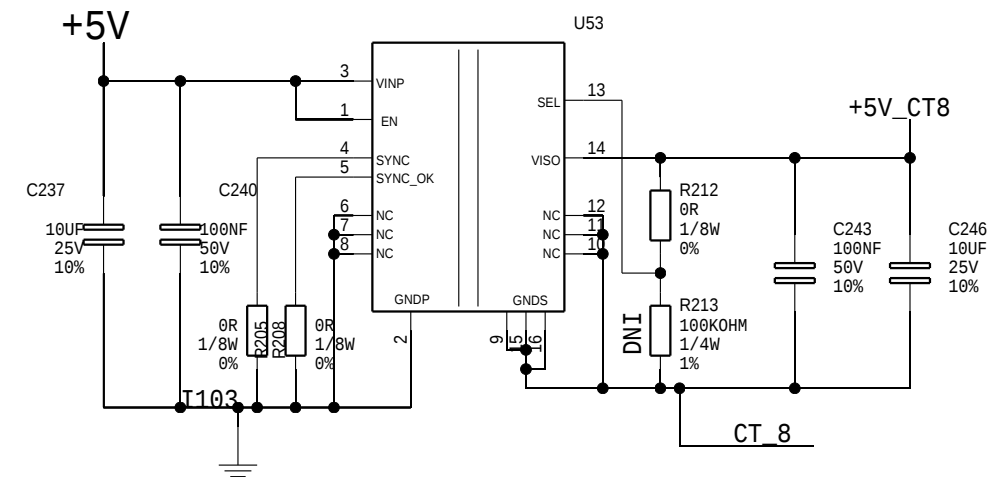
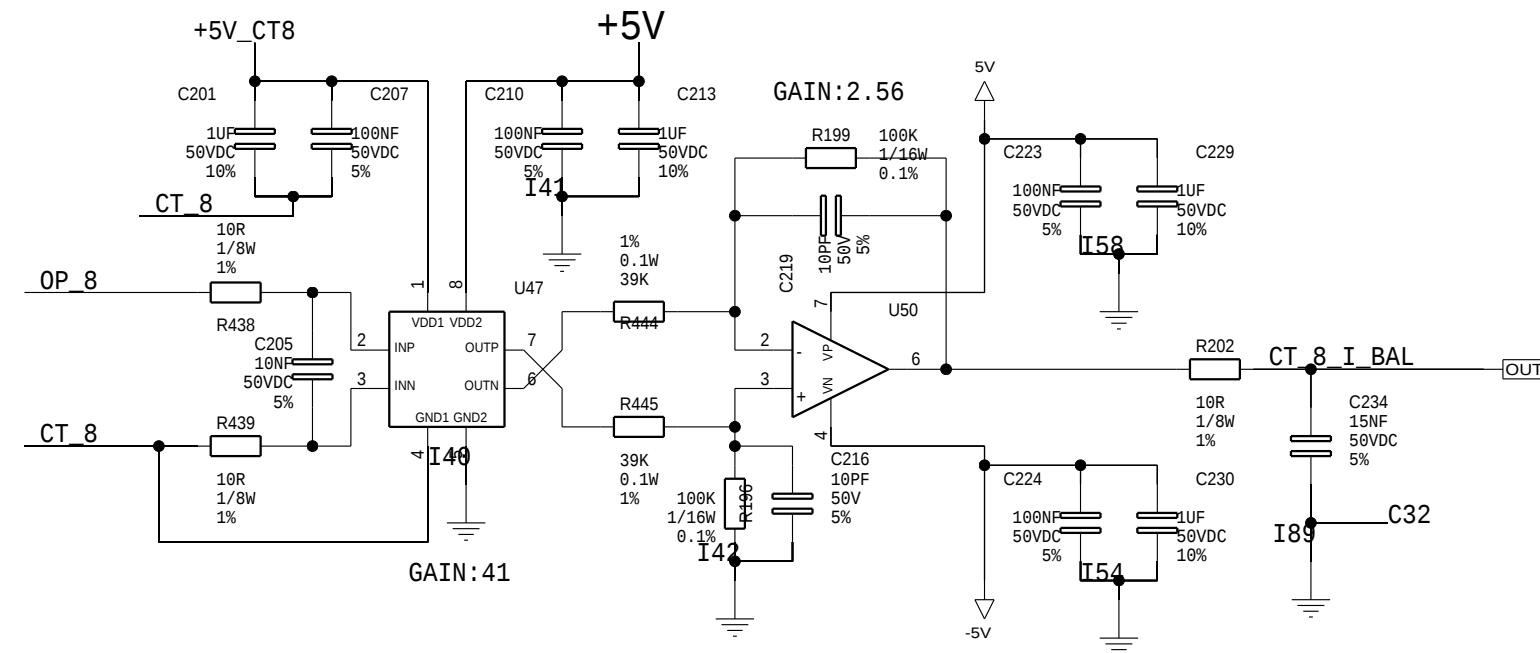
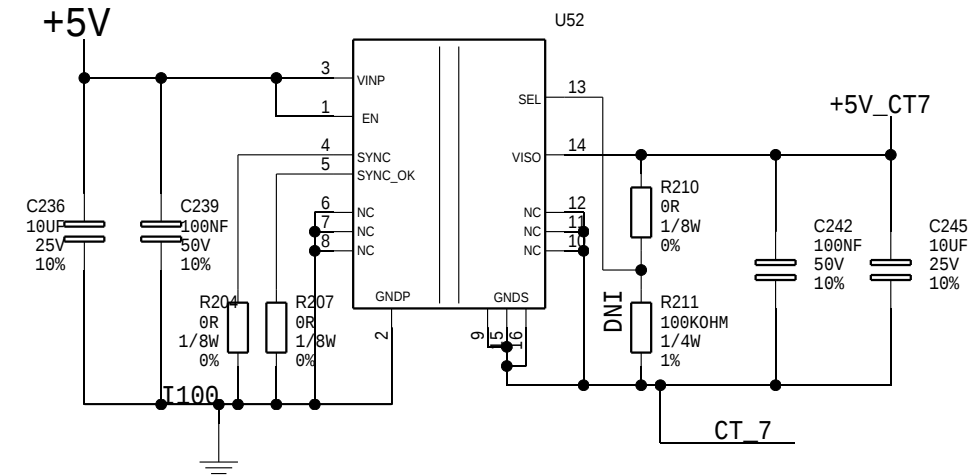
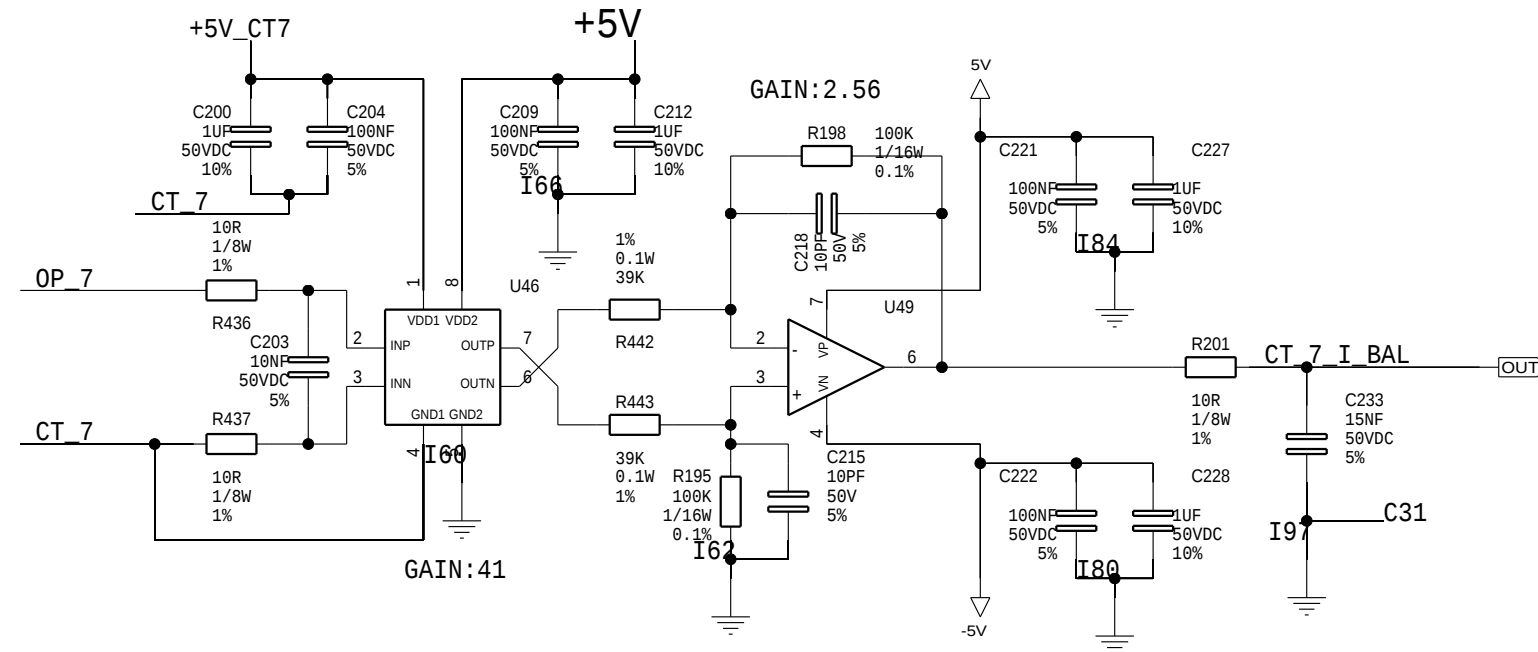


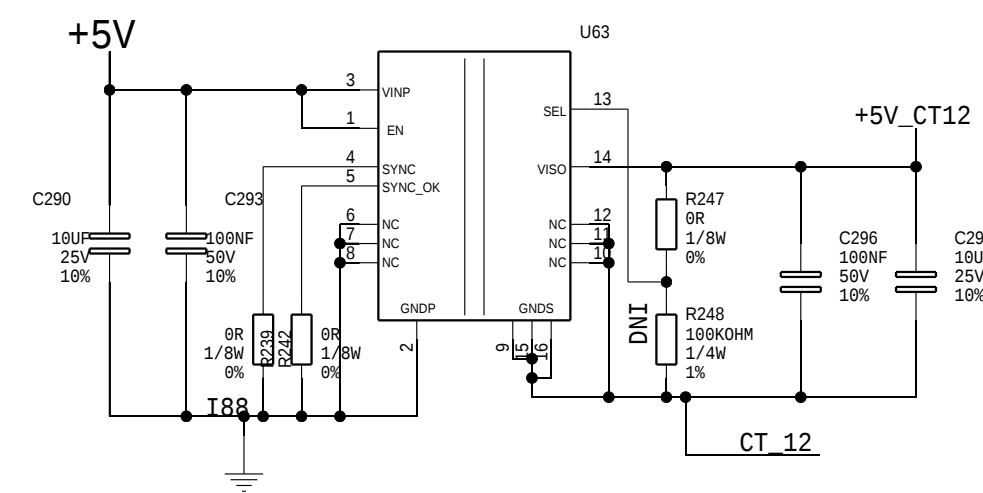
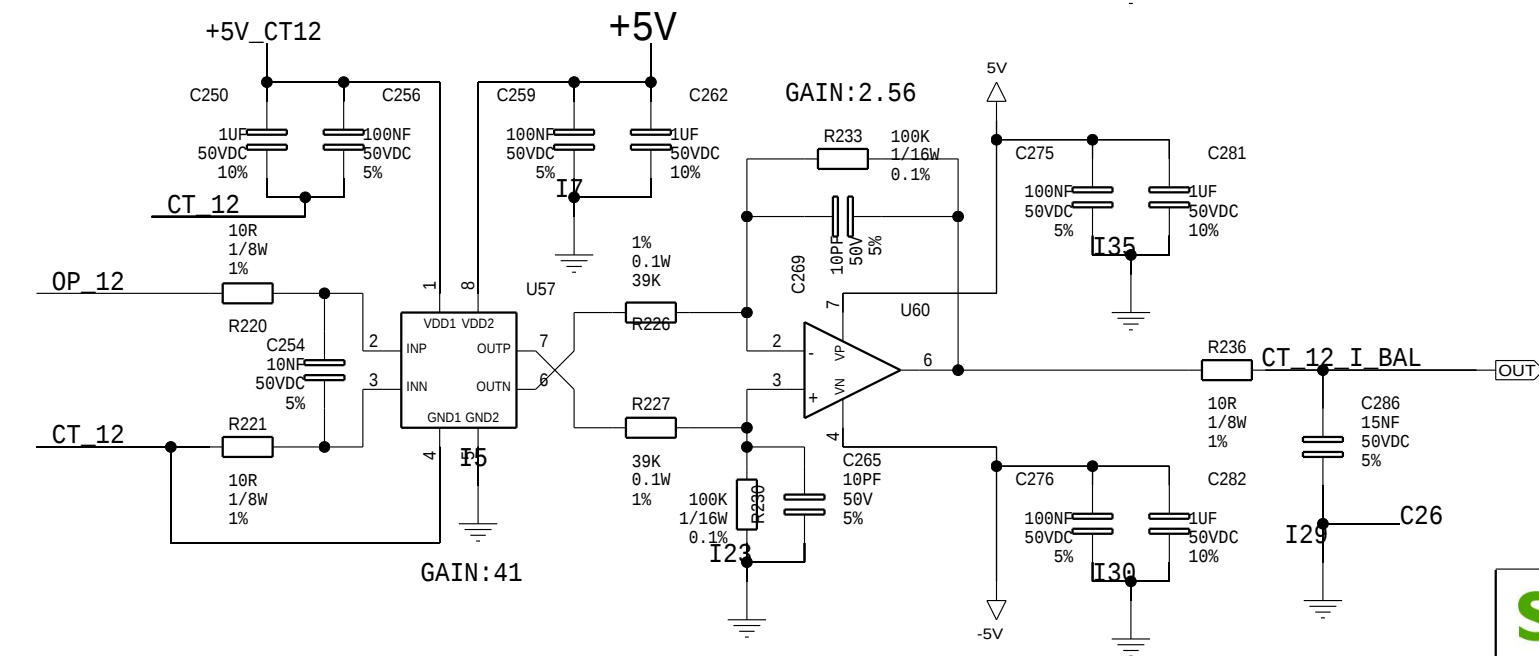
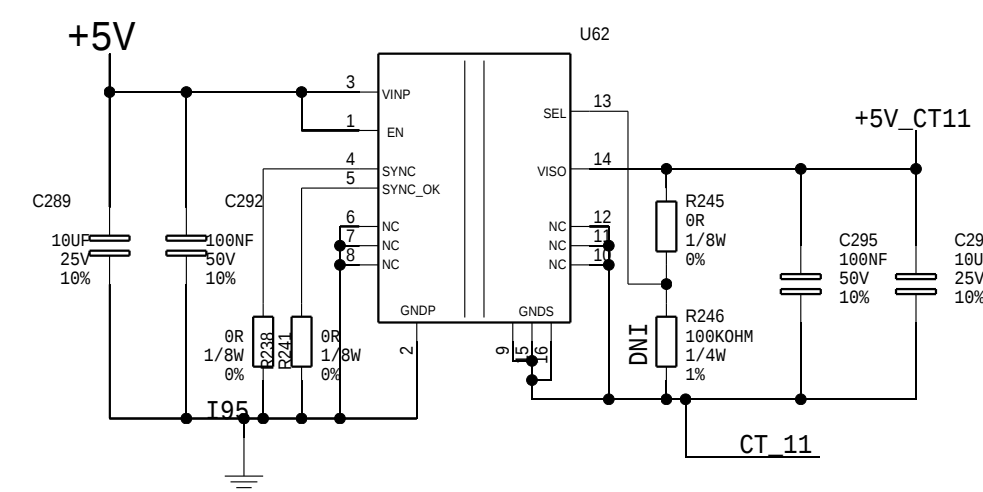
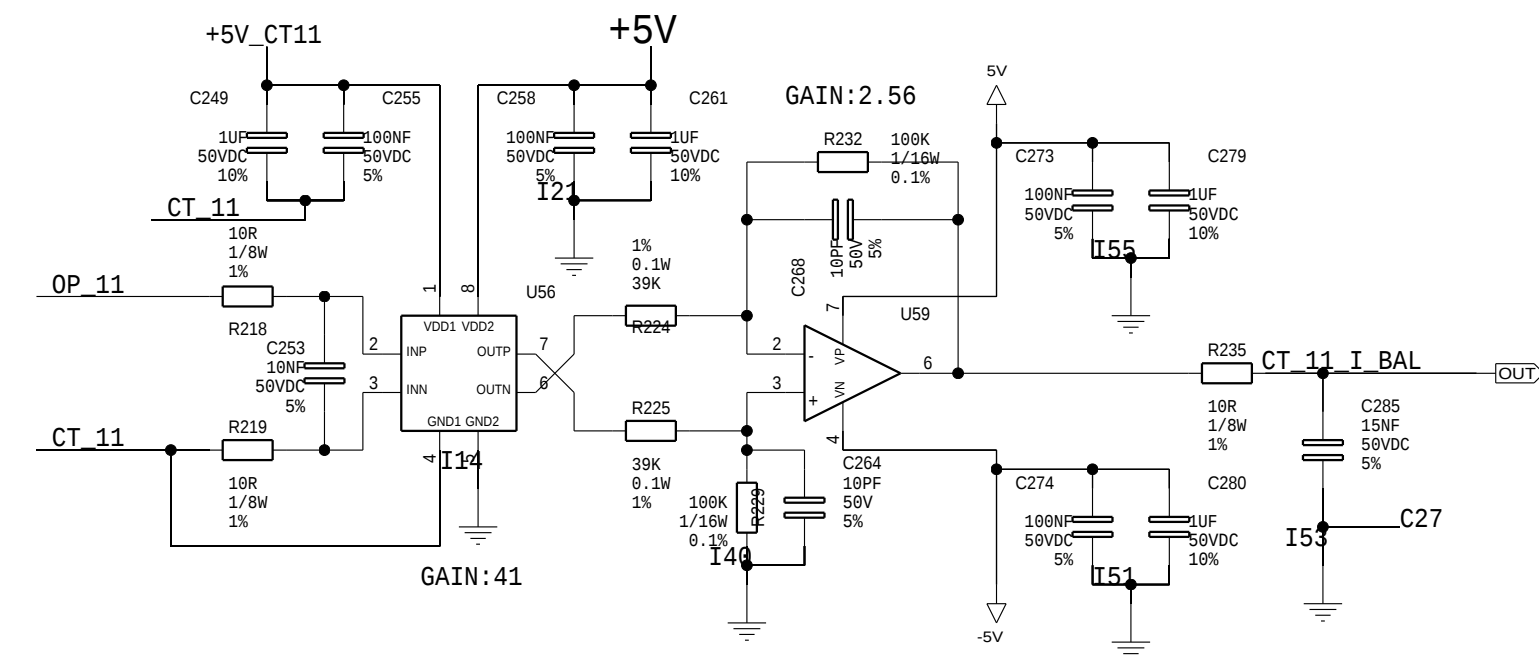
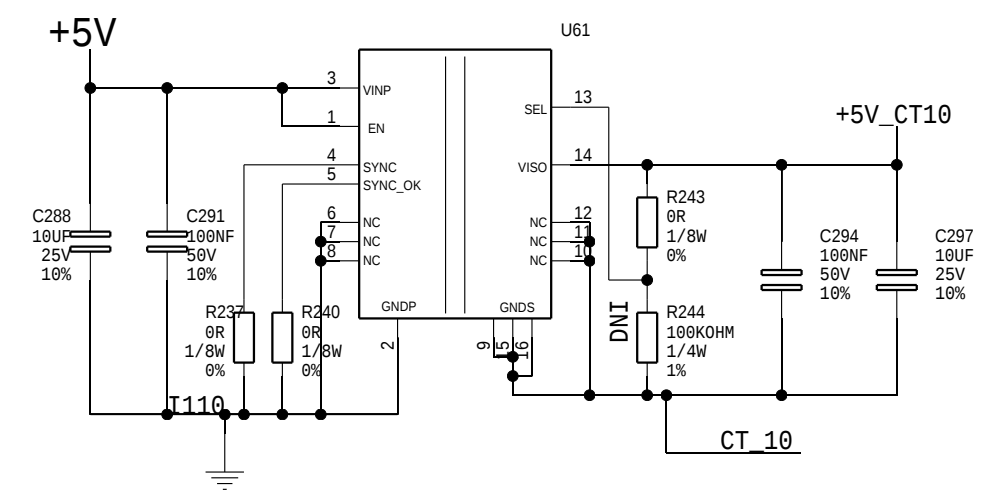
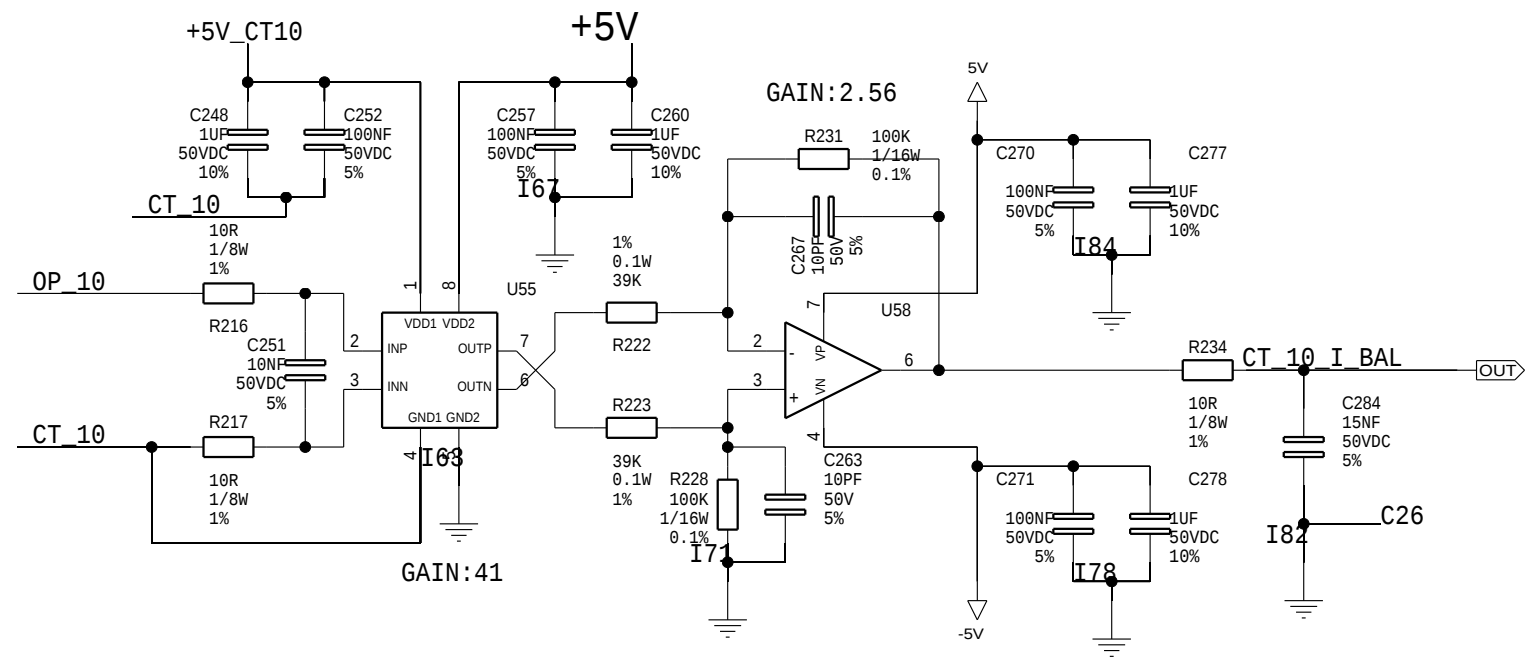
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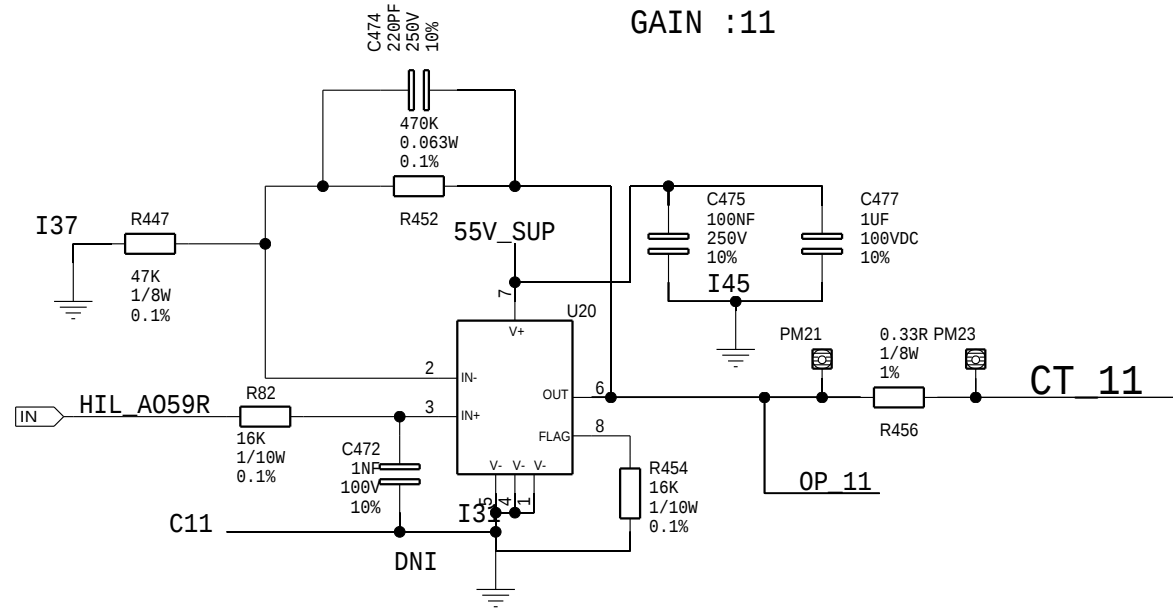
BLOCK NAME = top		REVISION		SHEET :	
NUMBER :		.		18/20	
STATE :					

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CT_11: NON-INVERTING GAIN

GAIN :11

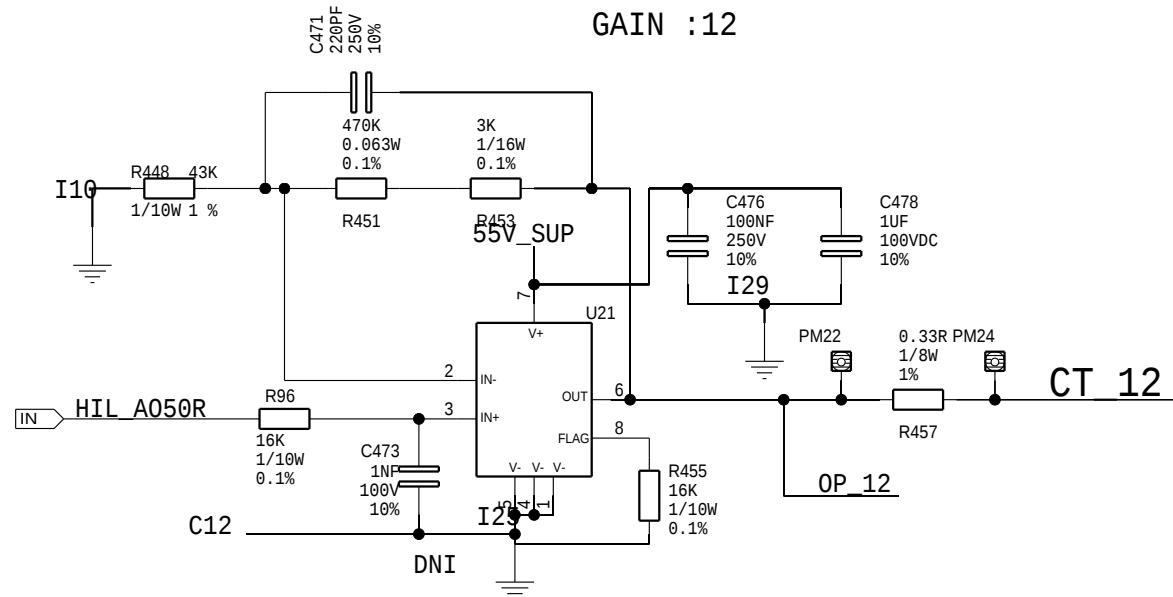
11TH CELL I/P
RANGE:0.5V-3.85V



CT_12: NON-INVERTING GAIN

GAIN :12

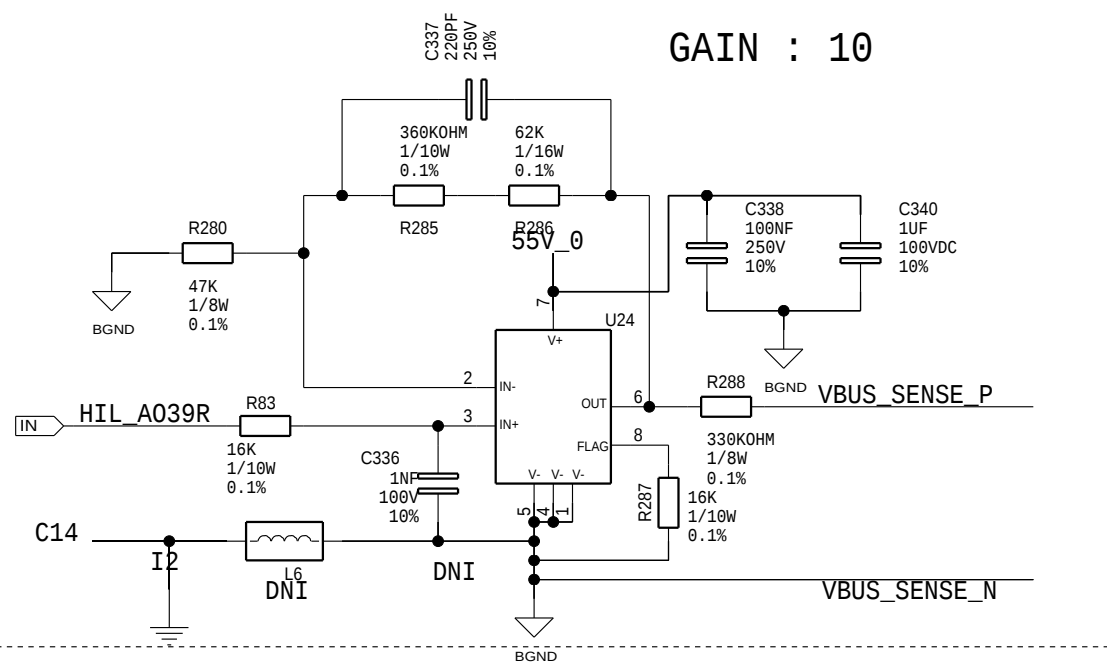
12TH CELL I/P
RANGE:0.5V-3.85V



BUS VOLTAGE: NON-INVERTING GAIN

GAIN : 10

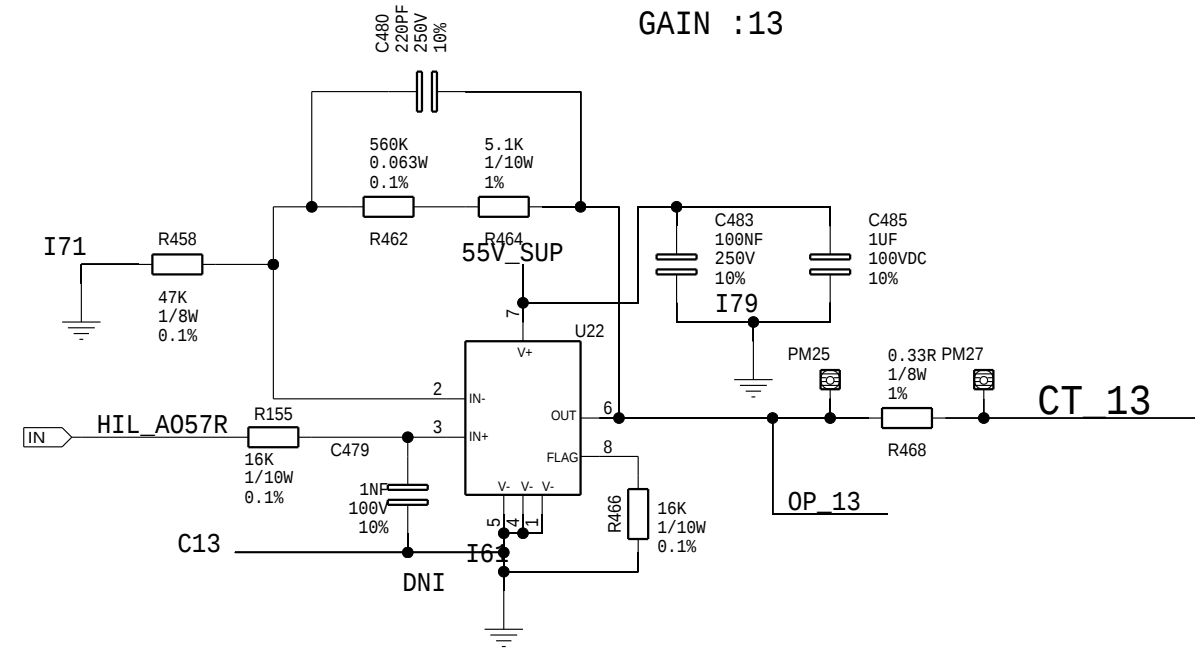
VBUS SENSE I/P
RANGE:0.5V-6V



CT_13: NON-INVERTING GAIN

GAIN :13

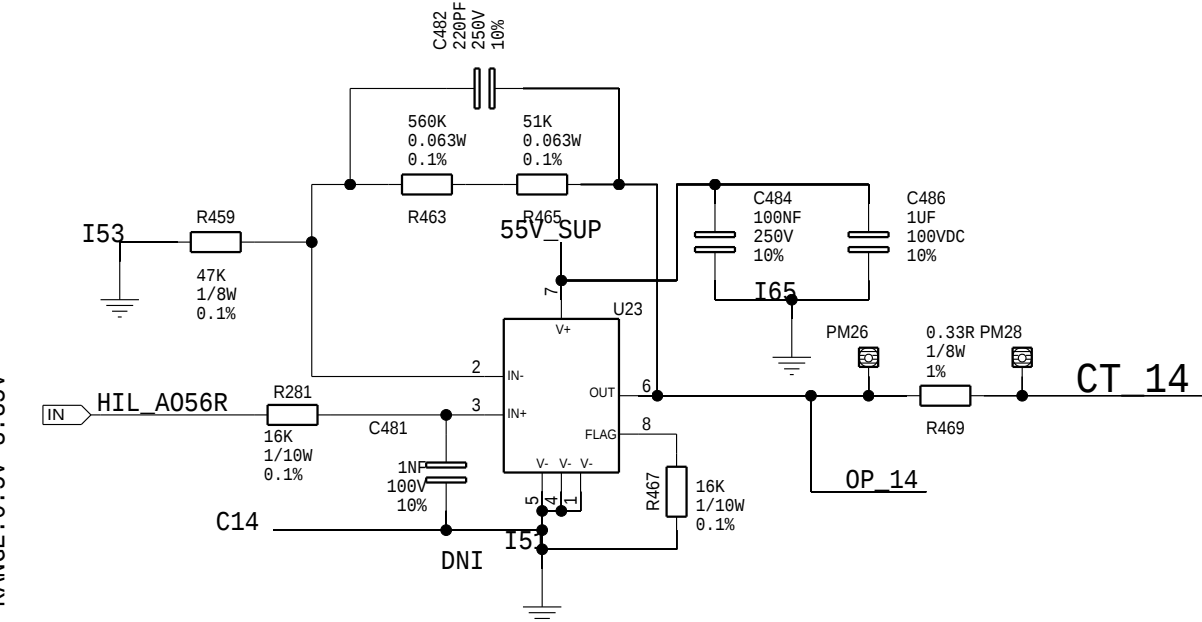
13TH CELL I/P
RANGE:0.5V-3.85V



CT_14: NON-INVERTING GAIN

GAIN :14

14TH CELL I/P
RANGE:0.5V-3.85V



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