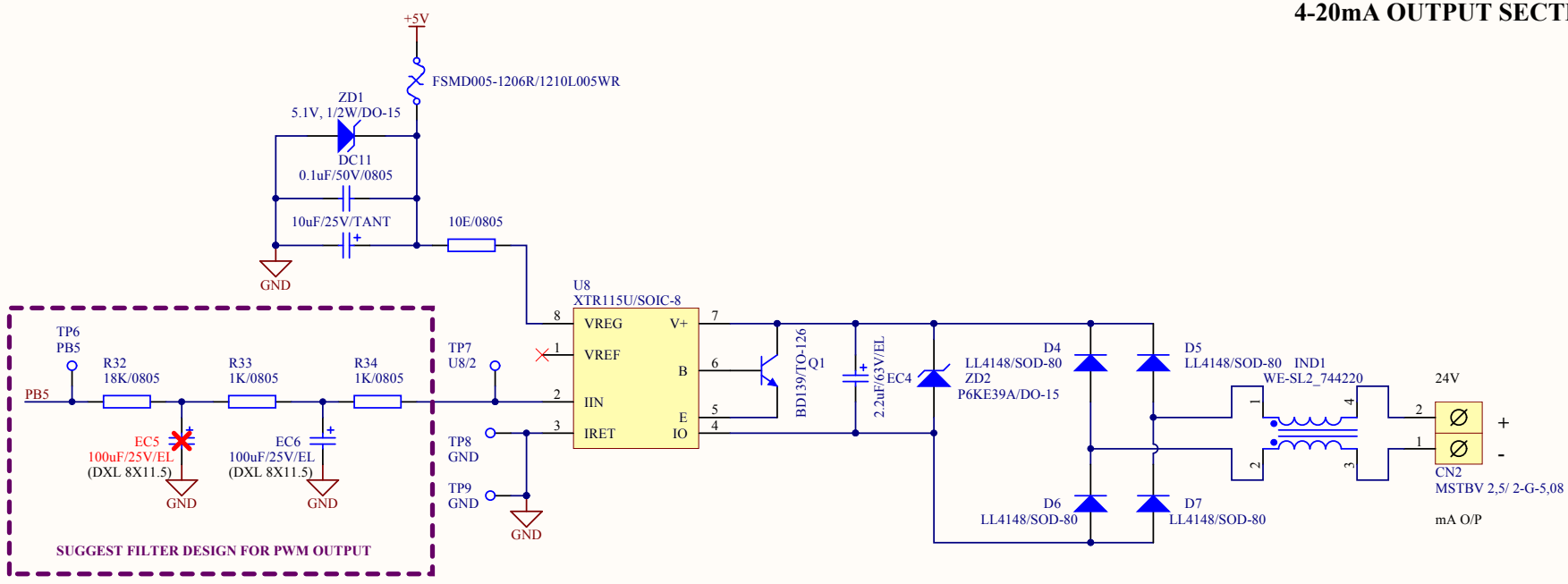
4-20mA OUTPUT SECTION



LOOP O/P mA	VOLTAGE AT TP6/PB5 (±1%) V
4	0.8
8	1.6
12	2.4
16	3.2
20	4.0

NOTES

NOTE:

1. ALL SMD RESISTORS OF 0805 PACKAGE ARE ±1%, 100PPM/°C, 1/8W OR UNLESS OTHERWISE SPECIFIED.
2. ALL SMD CAPACITORS OF 0805 PACKAGE ARE ±10%, X7R, MULTILAYER CERAMIC CHIP CAPACITOR (MLCC) OR UNLESS OTHERWISE SPECIFIED.
3. ALL LEADED MFR RESISTORS ARE ±1%, 100PPM/°C, 1/4W OR UNLESS OTHERWISE SPECIFIED.
4. THE ROWS WILL BE ADDED AS PER THE COMPONENTS WILL BE USED & SEQUENCE WILL BE MAINTAINED ACCORDINGLY AS SUGGESTED BY R&D DEPT.
5. THE MAKE & MATERIAL SPECIFIED IN THE BOM IS RECOMMENDED BY THE R&D DEPT.
6. THE OTHER MAKE OF ABOVE SPECIFIED MATERIAL CAN BE USED BY CHECKING THE SPECIFICATION IN THE RESPECTIVE MATERIAL DATASHEET OR CONSULT WITH R&D DEPT.

ASSEMBLE OPEN

DOCUMENT NAME:			AEPL
PRODUCT NAME:			MODEL NO:
CIRCUIT DIAGRAM OF: 4-20mA OUTPUT SECTION			
MADE BY:	CHECKED BY:	APPROVED BY:	
DATE:	DATE:	DATE:	
FILE PATH:			
DEPT:	FORMAT NO:	REV:	DATE :	PAGE: