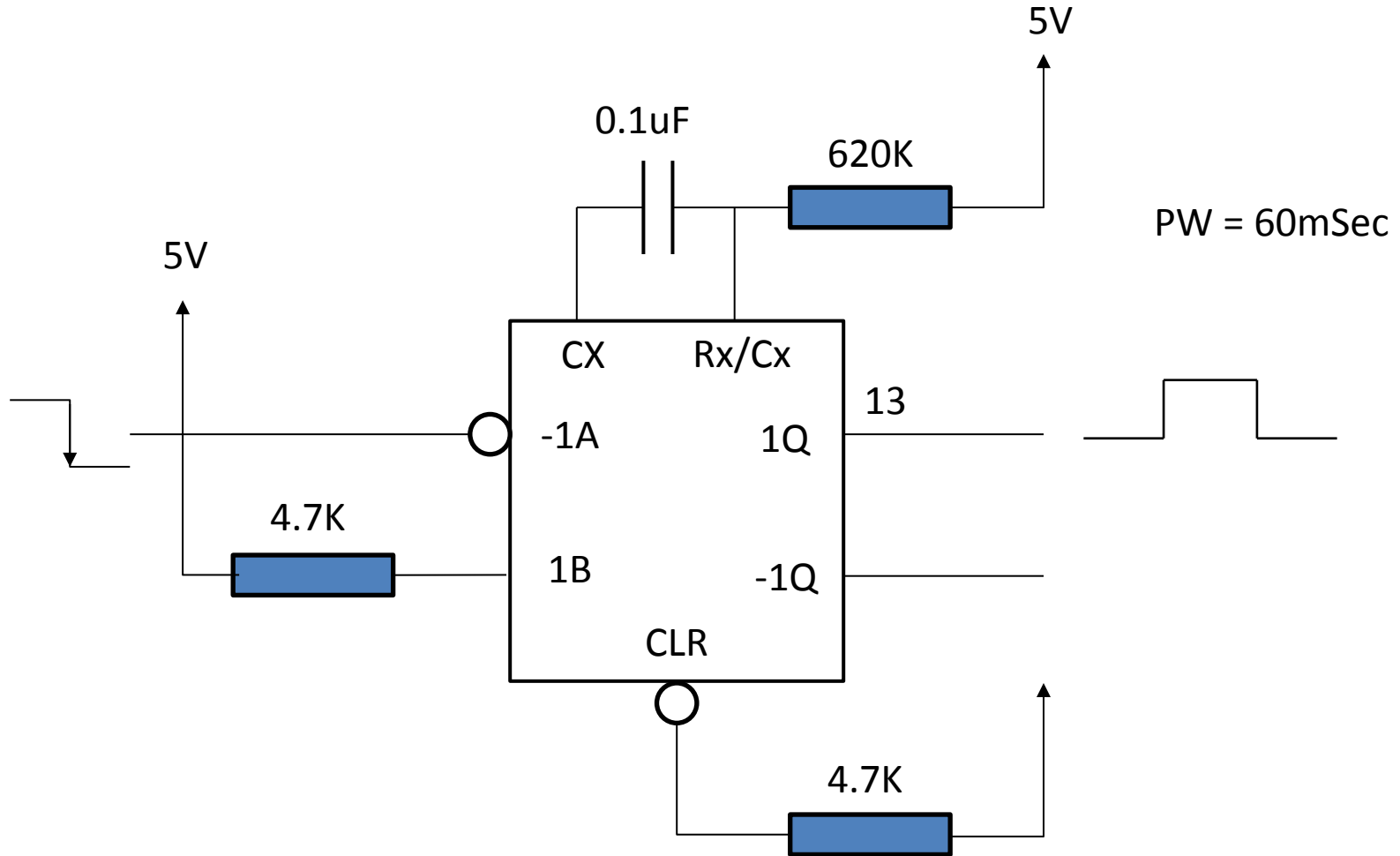
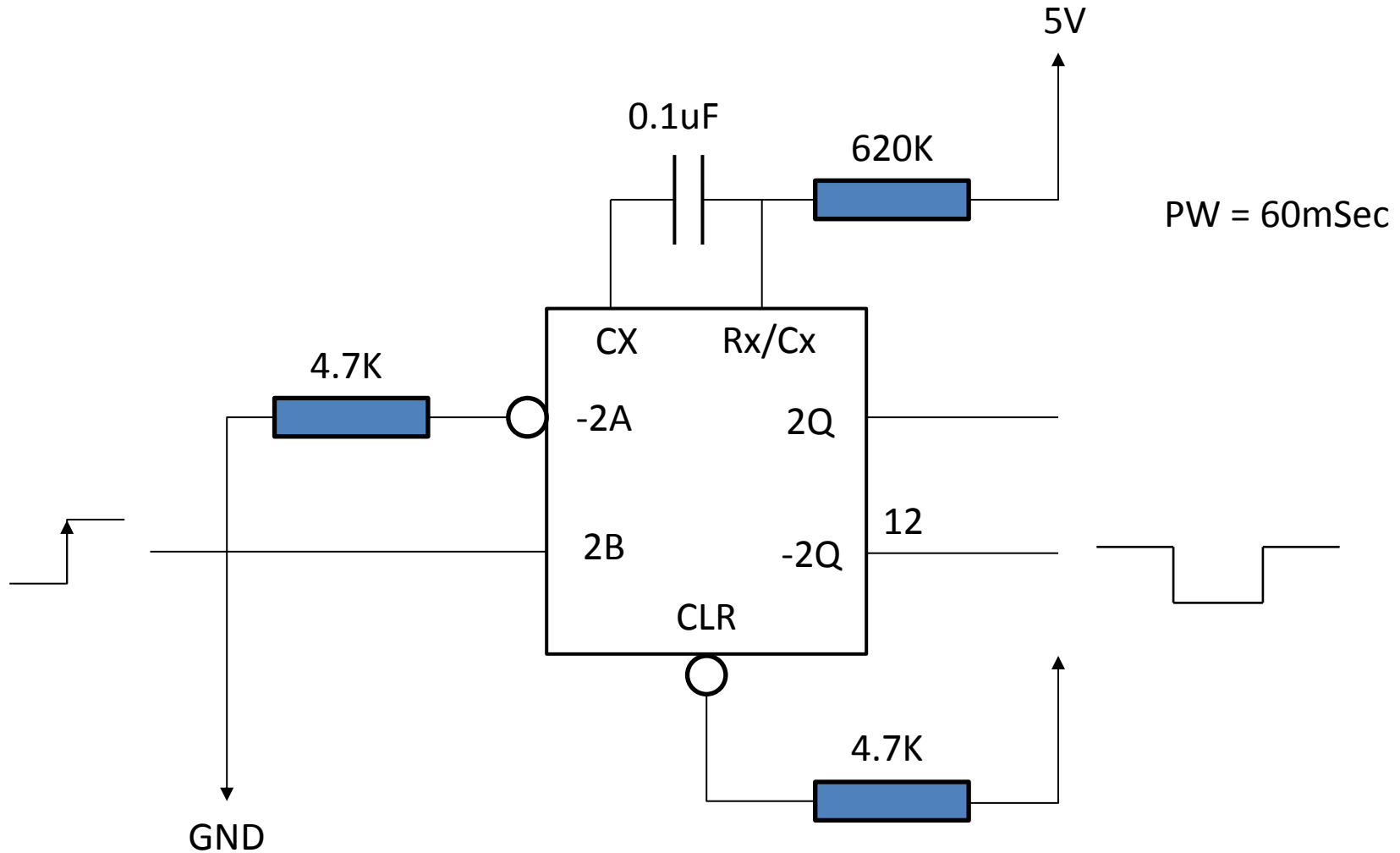


SN74LV123A circuit 1ch

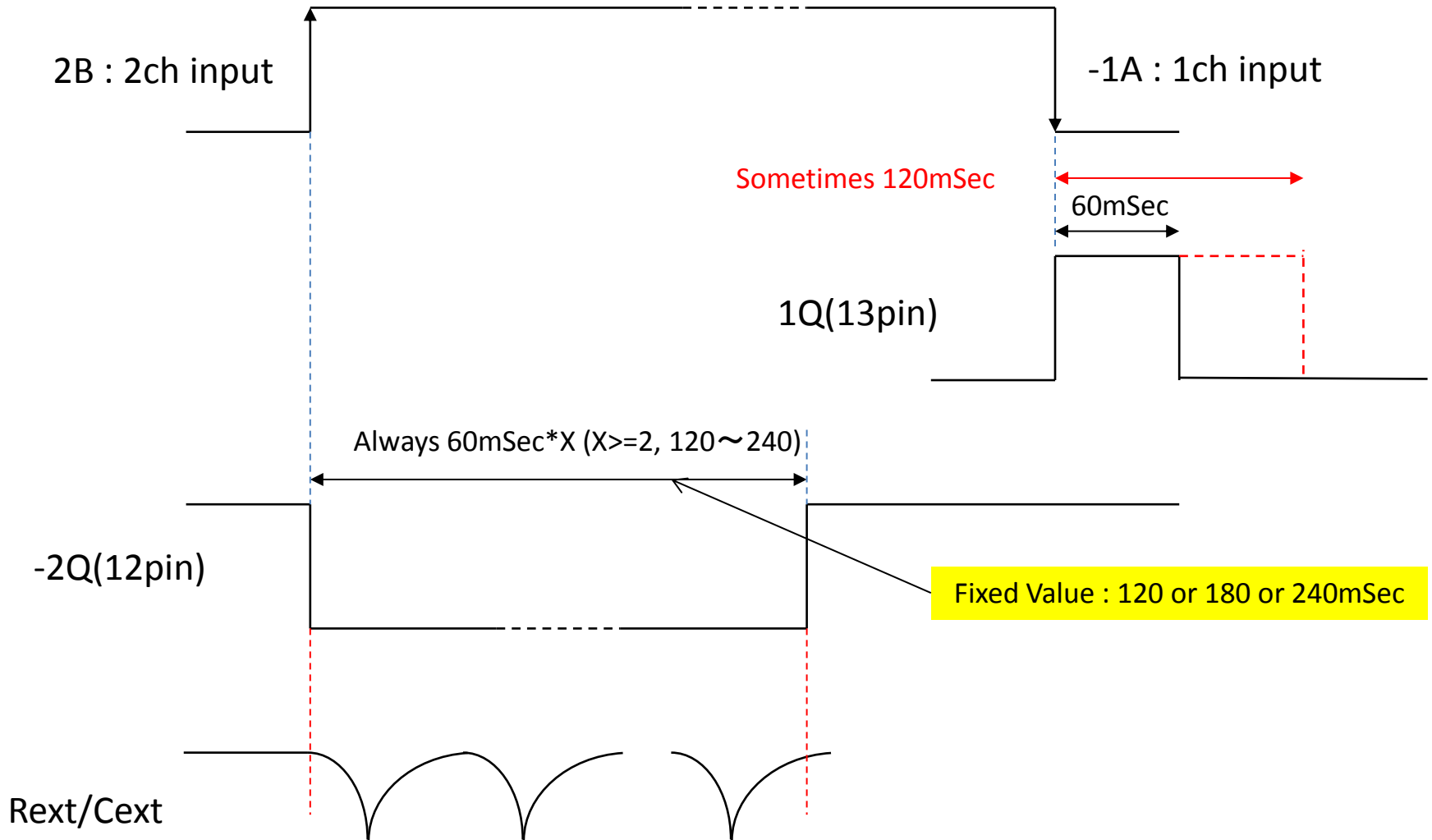


SN74LV123A circuit 2ch



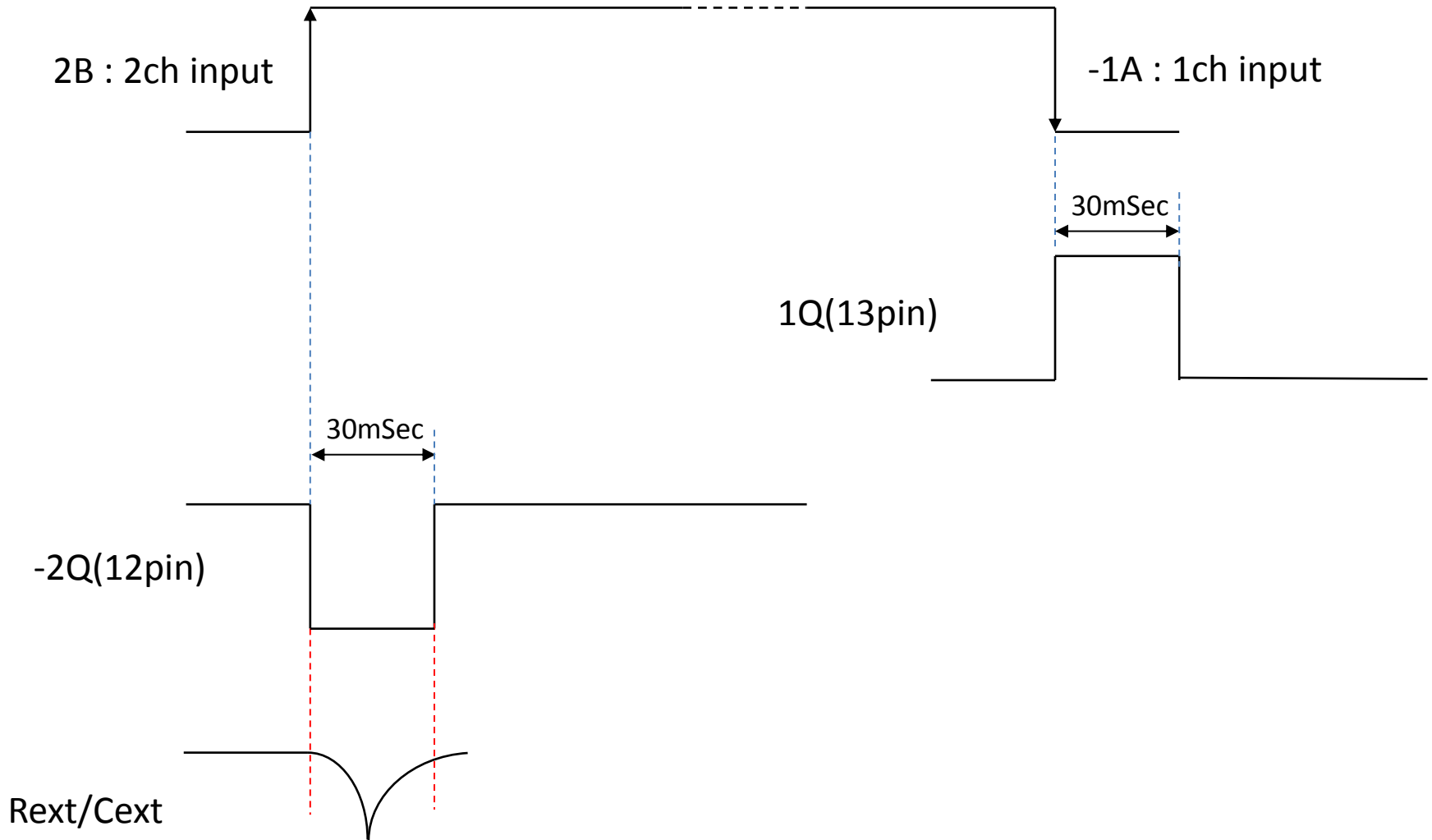
In case of Pulse width 60mSec ($R_t = 620K$, $C_t = 0.1\mu\text{Sec}$)

Irregular behavior happened

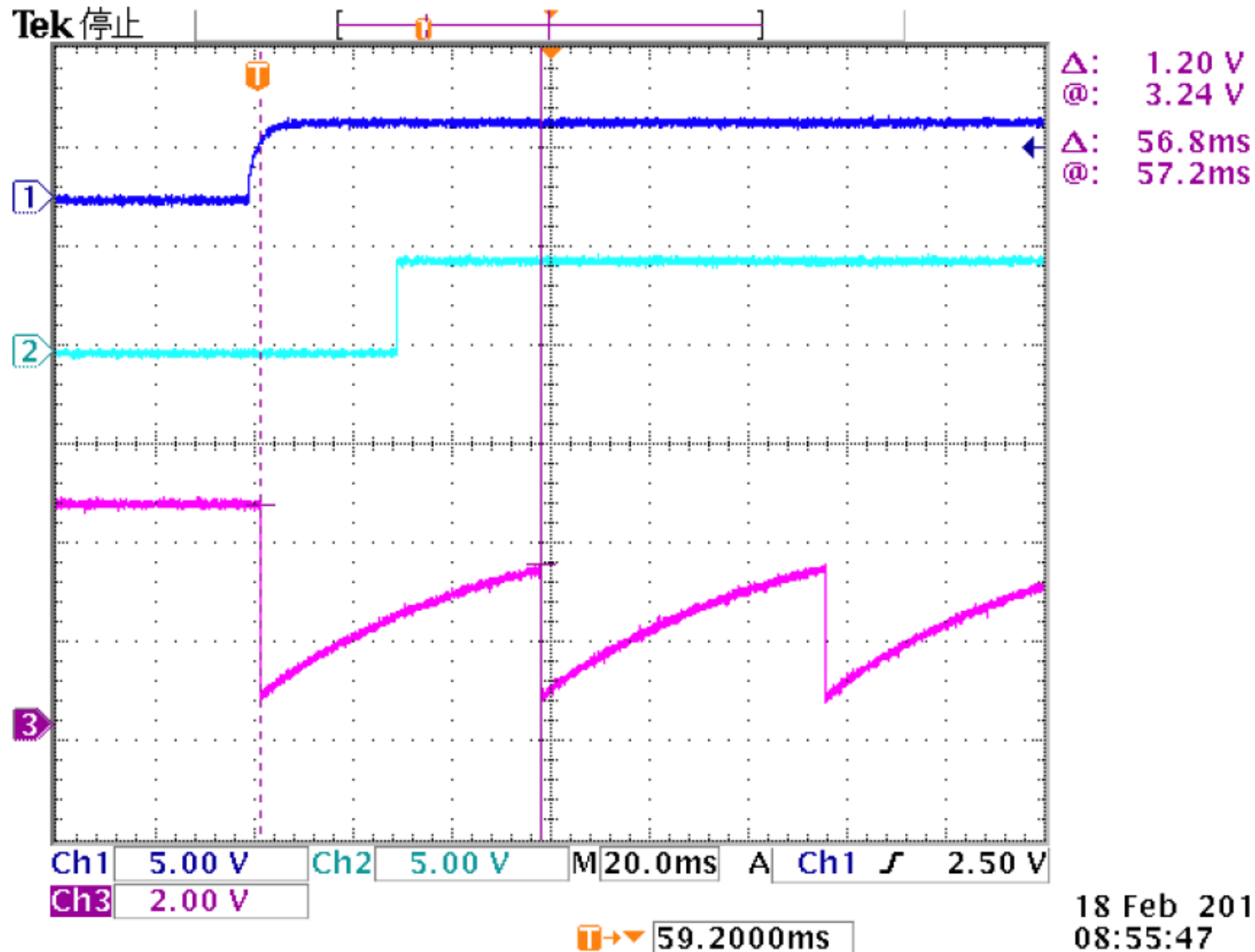


In case of Pulse width 30mSec ($R_t = 310K$, $C_t = 0.1\mu\text{Sec}$)

Normal behavior happened



Tek 停止



18 Feb 2015
08:55:47