

Circuit Diagram

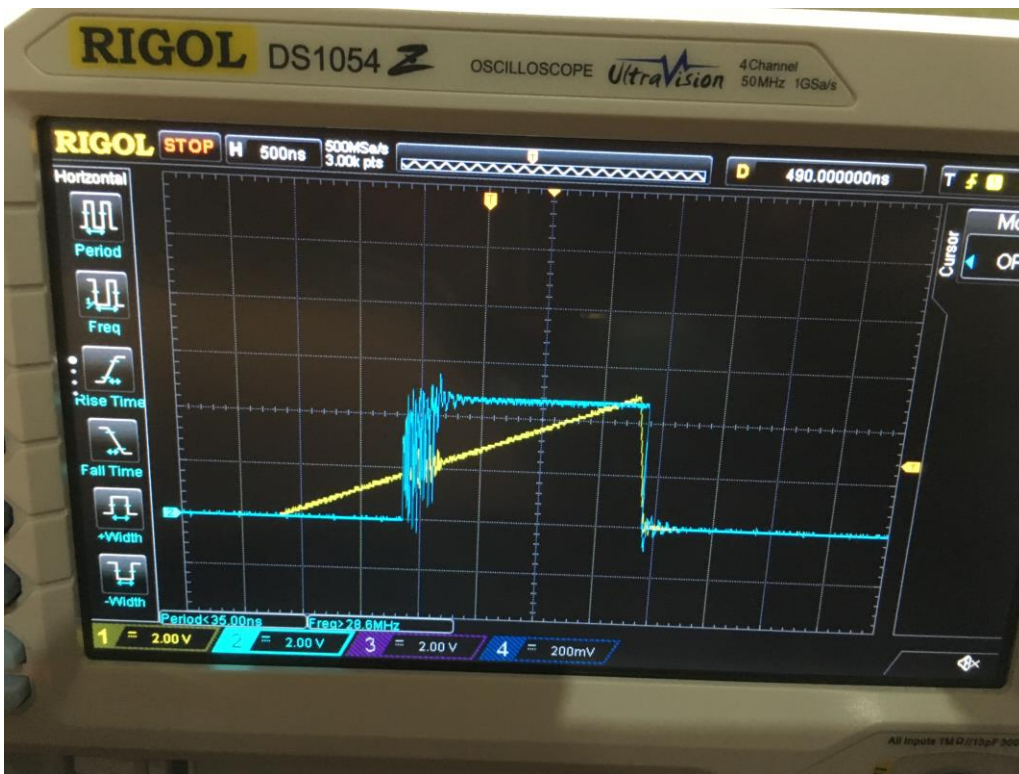


Image 01



Image 02