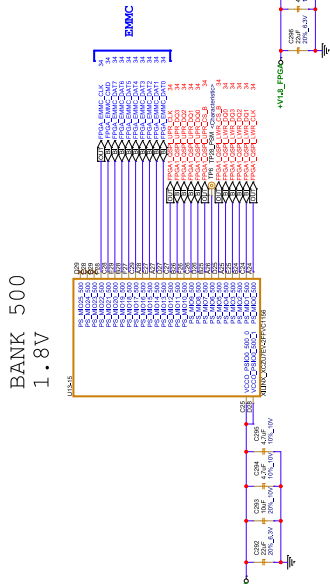


BANK 500
1.8V



BANK 501
1.8V

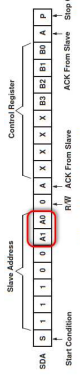
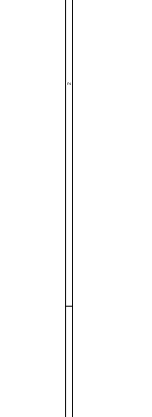
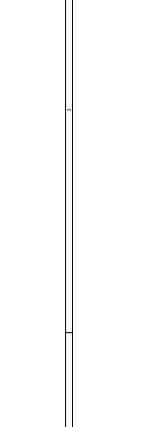
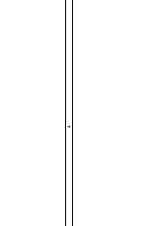
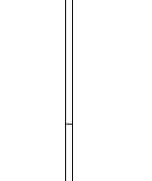
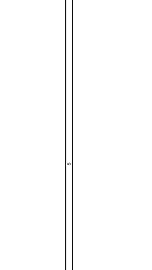
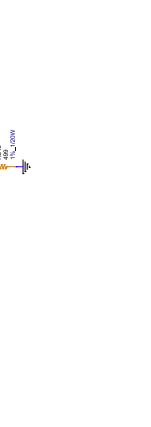
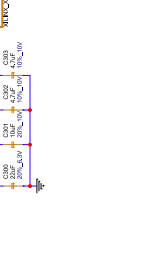
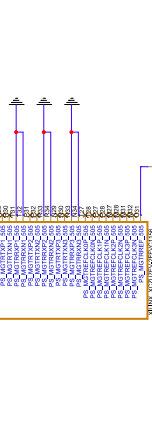
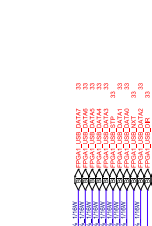
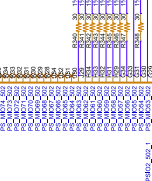
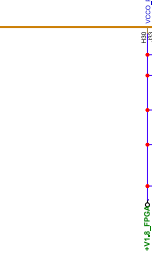
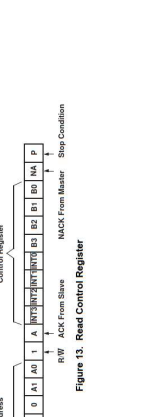
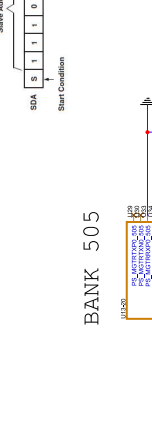
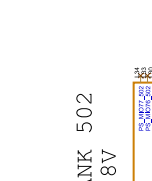
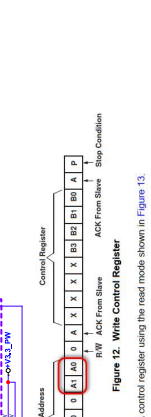
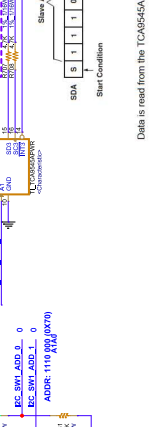
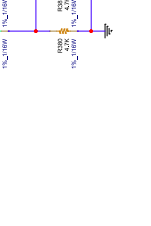
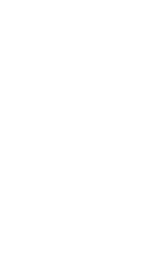
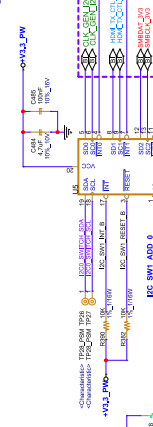
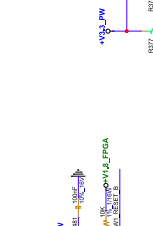
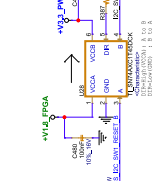
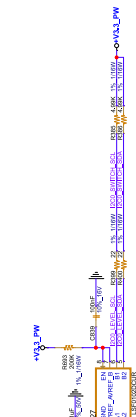
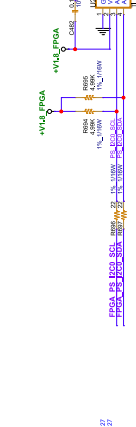
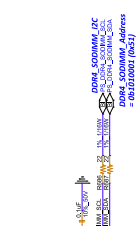
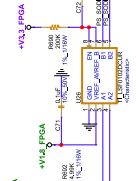
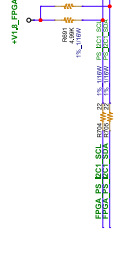
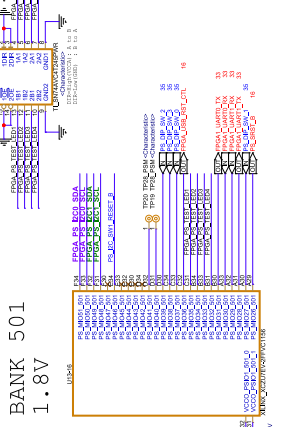


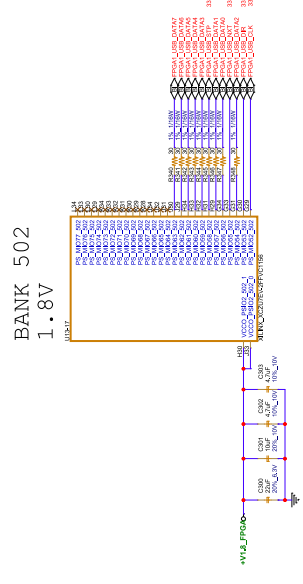
Figure 12. Write Control Register

Data is read from the TC9A654A control register using the read mode shown in Figure 13.



Figure 13. Read Control Register

BANK 502
1.8V



BANK 505

