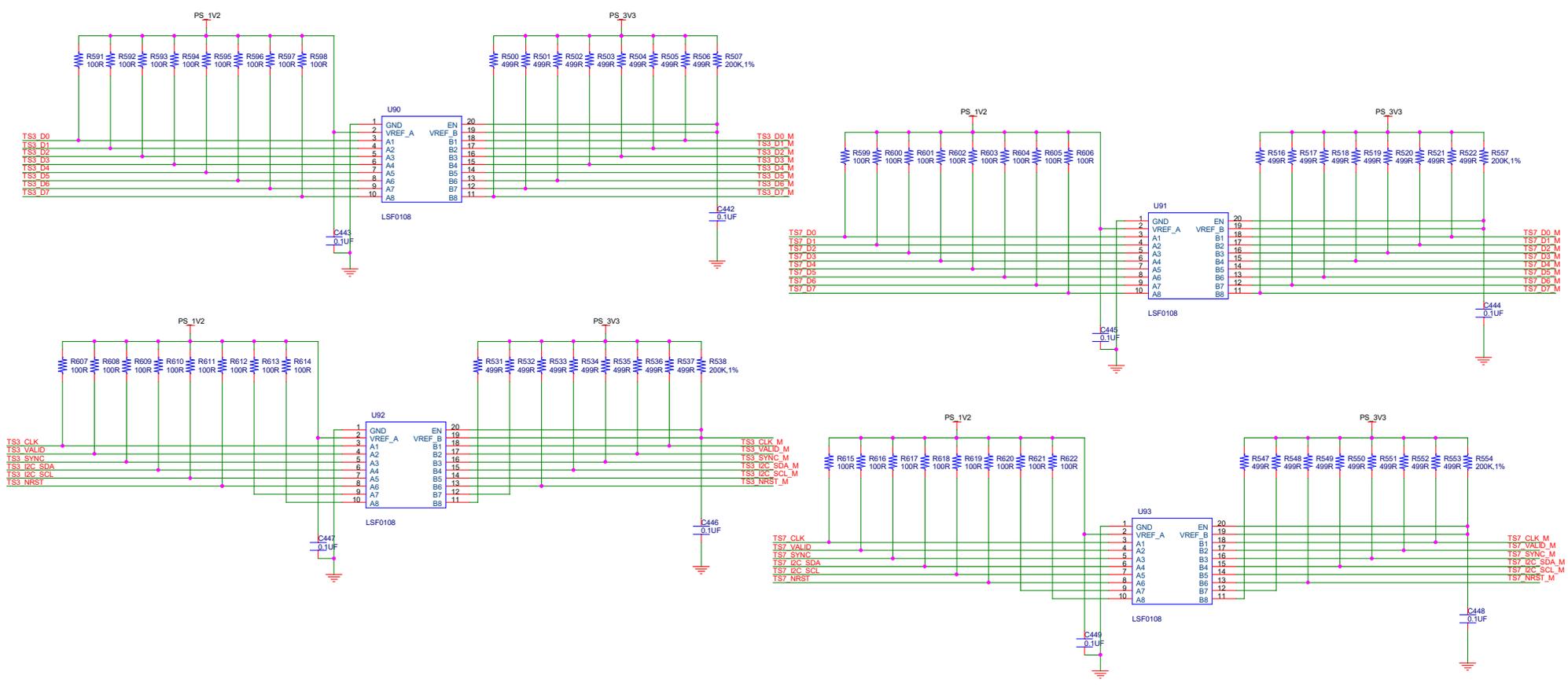


# LEVEL SHIFTER



- [13] TS3\_CLK
- [13] TS3\_VALID
- [13] TS3\_SYNC
- [13] TS3\_D0
- [13] TS3\_D1
- [13] TS3\_D2
- [13] TS3\_D3
- [13] TS3\_D4
- [13] TS3\_D5
- [13] TS3\_D6
- [13] TS3\_D7
- [13] TS3\_I2C\_SDA
- [13] TS3\_I2C\_SCL
- [13] TS3\_NRST

- [12] TS3\_CLK\_M
- [12] TS3\_VALID\_M
- [12] TS3\_SYNC\_M
- [12] TS3\_D0\_M
- [12] TS3\_D1\_M
- [12] TS3\_D2\_M
- [12] TS3\_D3\_M
- [12] TS3\_D4\_M
- [12] TS3\_D5\_M
- [12] TS3\_D6\_M
- [12] TS3\_D7\_M
- [12] TS3\_I2C\_SDA\_M
- [12] TS3\_I2C\_SCL\_M
- [12] TS3\_NRST\_M

- [13] TS7\_CLK
- [13] TS7\_VALID
- [13] TS7\_SYNC
- [13] TS7\_D0
- [13] TS7\_D1
- [13] TS7\_D2
- [13] TS7\_D3
- [13] TS7\_D4
- [13] TS7\_D5
- [13] TS7\_D6
- [13] TS7\_D7
- [13] TS7\_I2C\_SDA
- [13] TS7\_I2C\_SCL
- [13] TS7\_NRST

- [12] TS7\_CLK\_M
- [12] TS7\_VALID\_M
- [12] TS7\_SYNC\_M
- [12] TS7\_D0\_M
- [12] TS7\_D1\_M
- [12] TS7\_D2\_M
- [12] TS7\_D3\_M
- [12] TS7\_D4\_M
- [12] TS7\_D5\_M
- [12] TS7\_D6\_M
- [12] TS7\_D7\_M
- [12] TS7\_I2C\_SDA\_M
- [12] TS7\_I2C\_SCL\_M
- [12] TS7\_NRST\_M