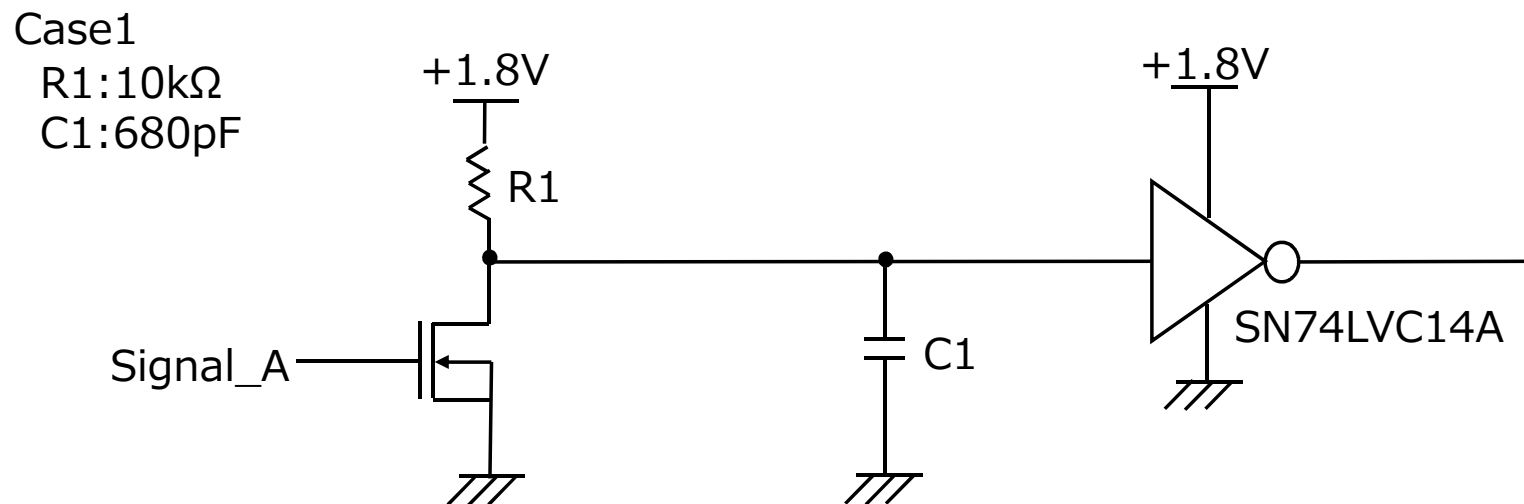


Q1 : Does case1, case2 circuit have problems on the input/output pin of SN74LVC14A?
We have concern, that is the repeat of charge to and discharge from the C1 or C2 capacitor may damage to input/output pin of SN74LVC14A and then break the pin.
Could you tell what we should things for consideration except slope on input/output pin?
Should we consider I_{ik} and I_{ok} spec. of SN74LVC14A?
If some consideration, could you let us know?



Case2

