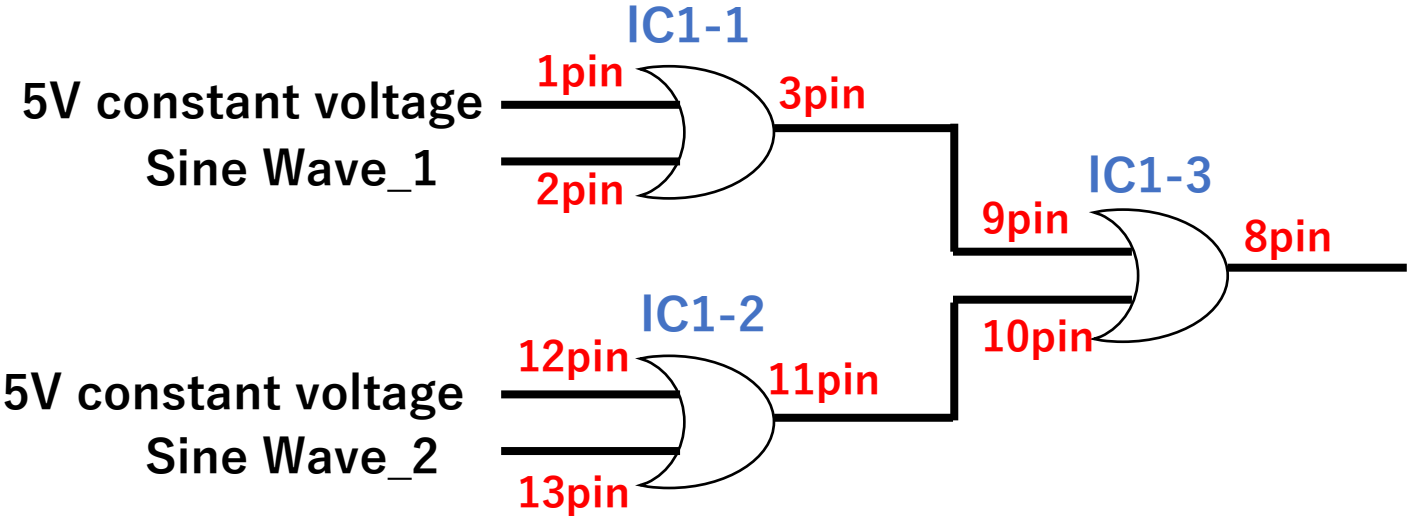


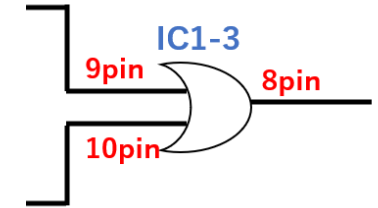
SN74HC86APWR

This is a simplified circuit diagram, but it is designed using the SN74HC86APW.  
IC1-1 to IC1-3 use each pin of one SN74HC86APWR.  
The input is a constant voltage of 5 V and each sine wave.

Circuit



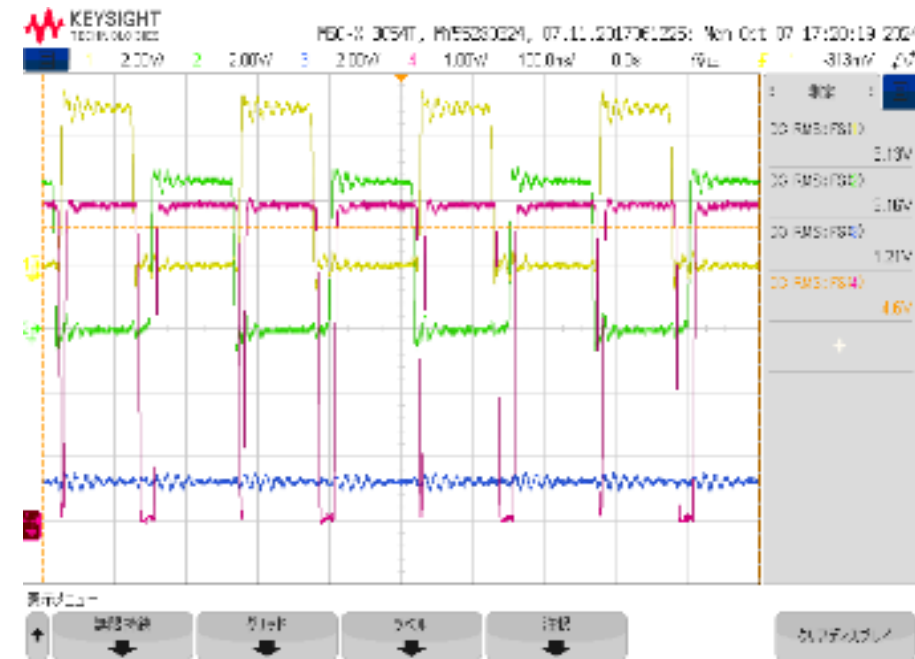
Since one lot, the output (8 pins) of IC1-3 has changed.  
I think that the operation is different due to the difference in the timing of the transition between the high and low of pins 9 and 10.  
The input and output waveforms of IC1-1 and IC1-2 will be shown in the slides that follow.



### Operation up to now

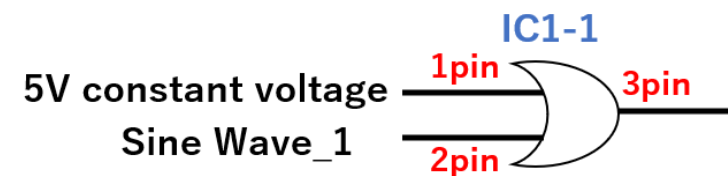


### Operation when a certain lot or later IC is used

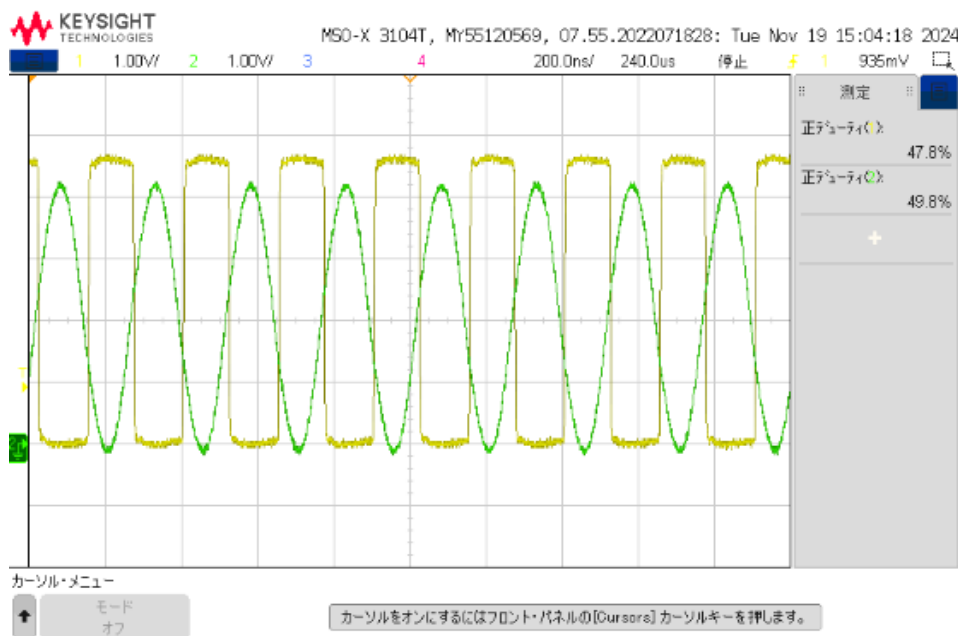


1ch: 10pin(IC1-2's output), 2ch: 9pin(IC1-1's output), 3ch: 8pin

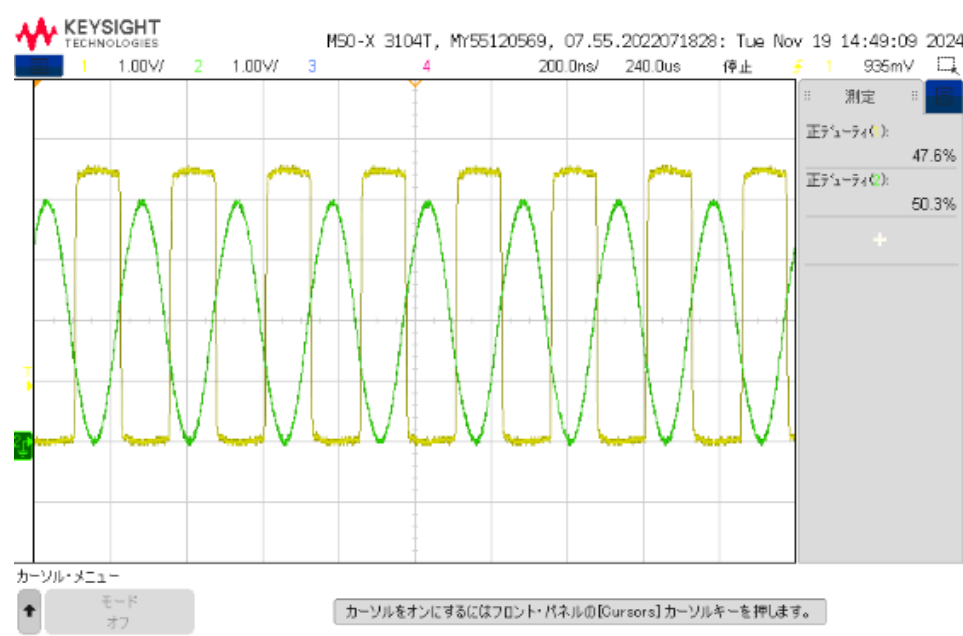
This is the voltage waveform of IC1-1.  
5V constant voltage is not measured.



### Operation up to now

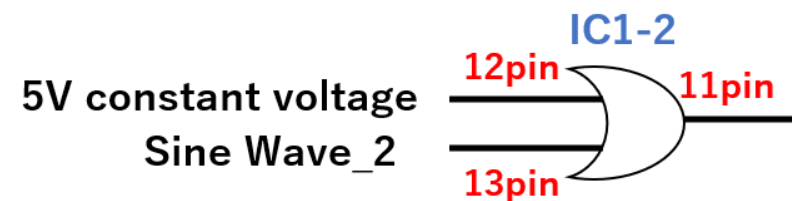


### Operation when a certain lot or later IC is used

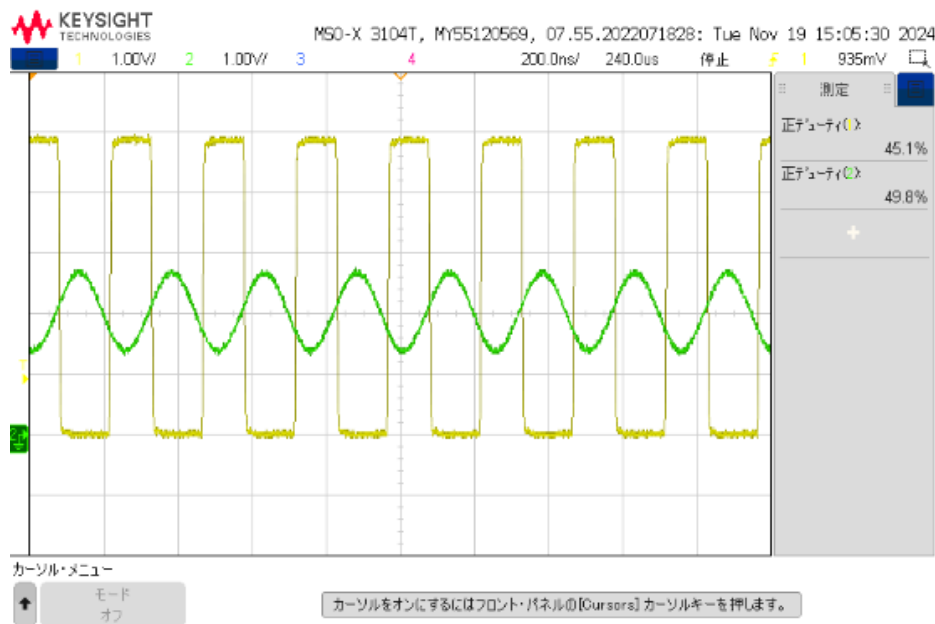


1ch: 2pin, 2ch: 3pin

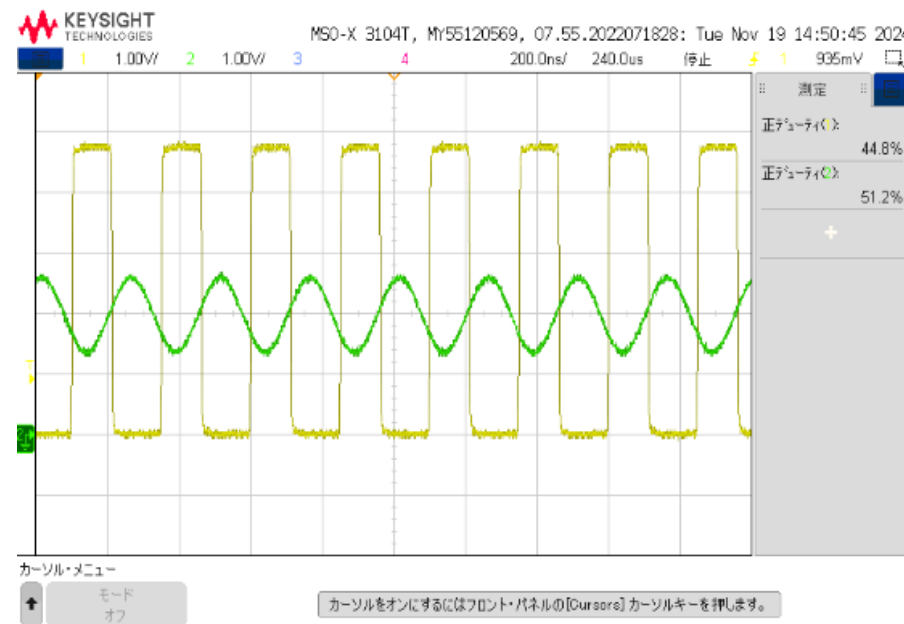
This is the voltage waveform of IC1-2.  
5V constant voltage is not measured.



### Operation up to now



### Operation when a certain lot or later IC is used



1ch: 2pin, 2ch: 3pin