

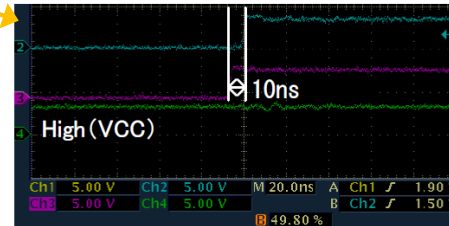
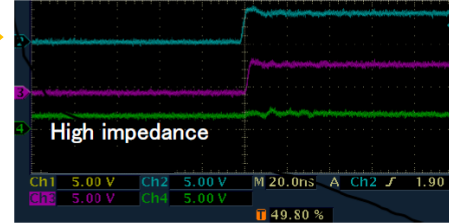
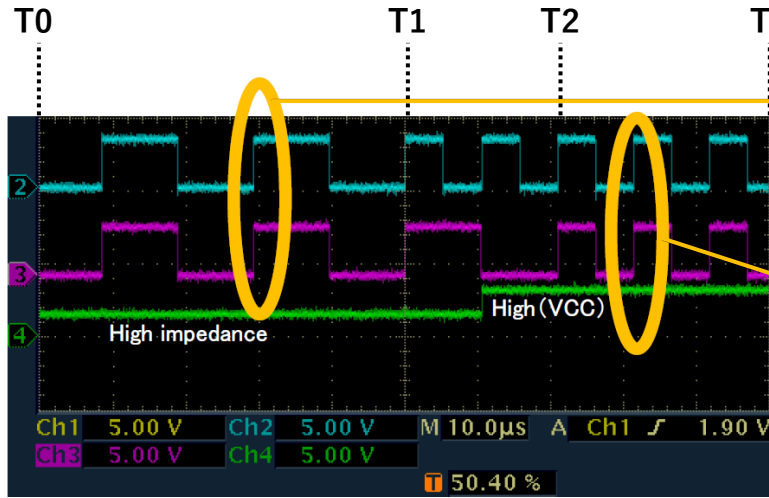
SN74LV4046A

T0 to T1: Same Phase(Frequency A)
 T1 to T2: Different Phase
 T2 to T3: Same Phase(Frequency B)

CH2: SIG_IN

CH3: COMP_IN

CH4: PC2_OUT



COMP_IN rising edge to
 SIG_IN rising edge is about 10ns.

Is this correct?

Expected output

High(VCC)

Low(GND)

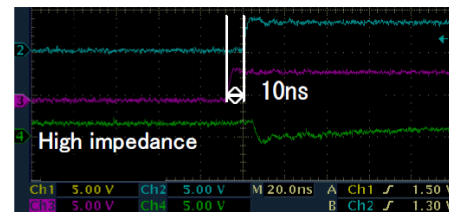
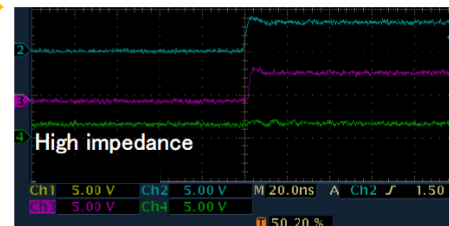
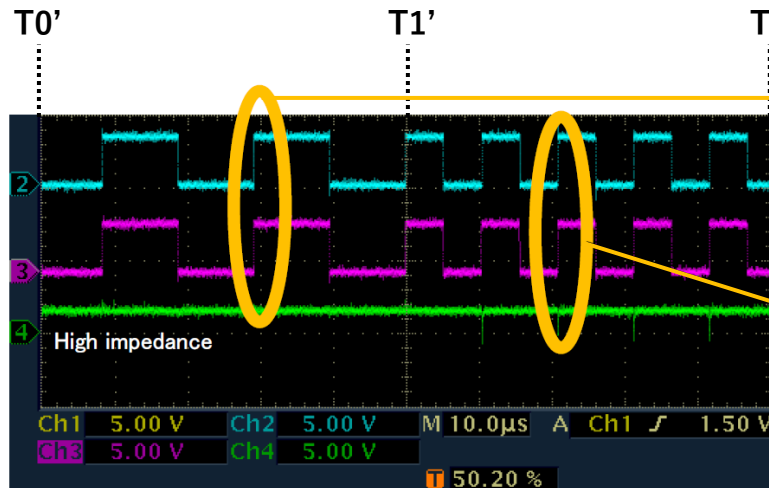
High Impedance

T0' to T1': Same Phase(Frequency A)
 T1' to T2': Same Phase(Frequency B)

CH2: SIG_IN

CH3: COMP_IN

CH4: PC2_OUT



COMP_IN rising edge to
 SIG_IN rising edge is about 10ns.