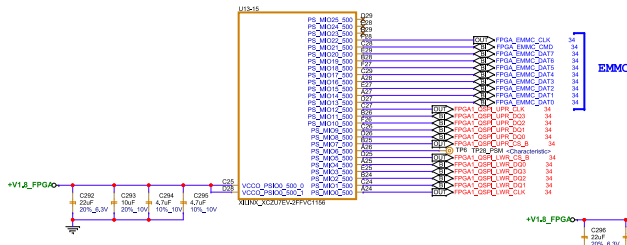
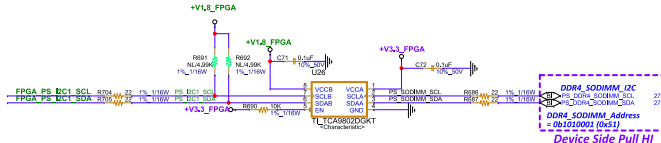
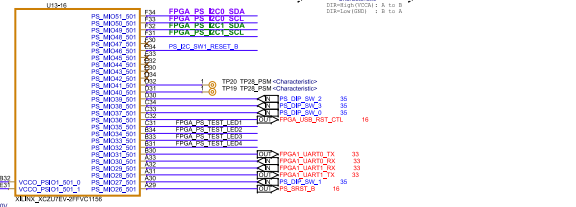


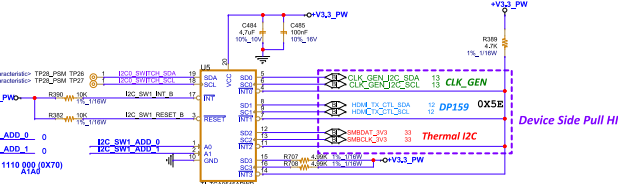
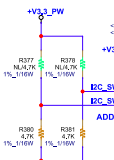
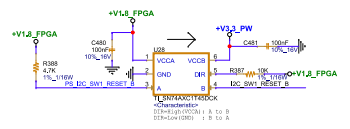
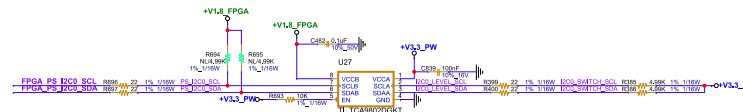
BANK 500
1.8V



BANK 501
1.8V



Device Side Pull HI



Device Side Pull HI

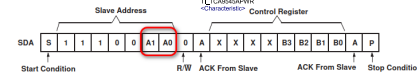


Figure 12. Write Control Register

Data is read from the TCA9545A control register using the read mode shown in Figure 13.

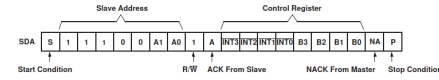
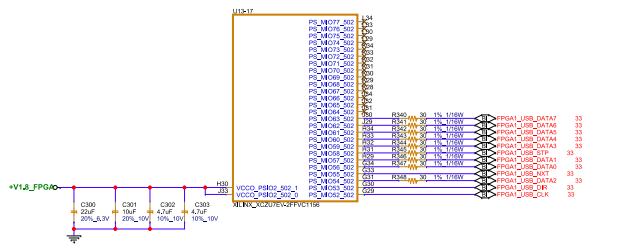
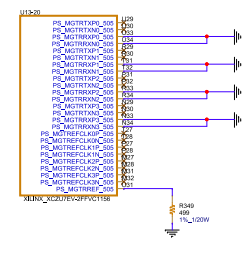


Figure 13. Read Control Register

BANK 502
1.8V



BANK 505

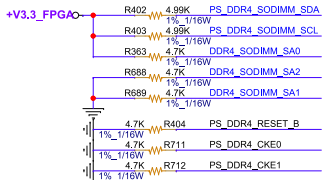
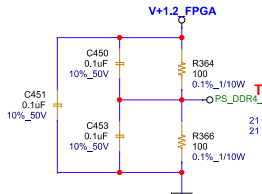


PS SIDE DDR4

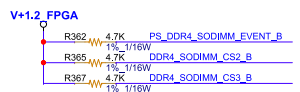
DDR4-SODIMM

[0-63]

Pin	Signal	Pin	Signal	Pin	Signal
137	PS_DDR4_CK0_T	8	DO0	17	PS_DDR4_DO0
138	PS_DDR4_CK0_C	7	DO1	17	PS_DDR4_DO1
139	PS_DDR4_CK1_T	20	DO2	17	PS_DDR4_DO2
140	PS_DDR4_CK1_C	21	DO3	17	PS_DDR4_DO3
109	PS_DDR4_CKE0	4	DO4	17	PS_DDR4_DO4
110	PS_DDR4_CKE1	3	DO5	17	PS_DDR4_DO5
149	DDR4_PS_CS0_B	16	DO6	17	PS_DDR4_DO6
157	PS_DDR4_CS1_B	17	DO7	17	PS_DDR4_DO7
155	PS_DDR4_ODT0	28	DO8	17	PS_DDR4_DO8
161	PS_DDR4_ODT1	29	DO9	17	PS_DDR4_DO9
115	PS_DDR4_BG0	42	DO10	17	PS_DDR4_DO10
113	PS_DDR4_BG1	43	DO11	17	PS_DDR4_DO11
165	PS_DDR4_BA0	24	DO12	17	PS_DDR4_DO12
145	PS_DDR4_BA1	25	DO13	17	PS_DDR4_DO13
144	PS_DDR4_A0	38	DO14	17	PS_DDR4_DO14
133	PS_DDR4_A1	37	DO15	17	PS_DDR4_DO15
132	PS_DDR4_A2	50	DO16	17	PS_DDR4_DO16
131	PS_DDR4_A3	49	DO17	17	PS_DDR4_DO17
128	PS_DDR4_A4	63	DO18	17	PS_DDR4_DO18
126	PS_DDR4_A5	62	DO19	17	PS_DDR4_DO19
122	PS_DDR4_A6	46	DO20	17	PS_DDR4_DO20
121	PS_DDR4_A7	45	DO21	17	PS_DDR4_DO21
125	PS_DDR4_A8	58	DO22	17	PS_DDR4_DO22
121	PS_DDR4_A9	59	DO23	17	PS_DDR4_DO23
146	PS_DDR4_A10	70	DO24	17	PS_DDR4_DO24
119	PS_DDR4_A11	71	DO25	17	PS_DDR4_DO25
118	PS_DDR4_A12	83	DO26	17	PS_DDR4_DO26
156	PS_DDR4_A14_WE_B	84	DO27	17	PS_DDR4_DO27
152	PS_DDR4_A16_RAS_B	87	DO28	17	PS_DDR4_DO28
152	PS_DDR4_A16_RAS_B	67	DO29	17	PS_DDR4_DO29
114	PS_DDR4_ACT_B	79	DO30	17	PS_DDR4_DO30
143	PS_DDR4_PAR	80	DO31	17	PS_DDR4_DO31
116	PS_DDR4_ALERT_B	174	DO32	17	PS_DDR4_DO32
134	PS_DDR4_RESET_B	173	DO33	17	PS_DDR4_DO33
108	PS_DDR4_VREF	167	DO34	17	PS_DDR4_DO34
164	PS_DDR4_VREF	168	DO35	17	PS_DDR4_DO35
254	PS_DDR4_SODIMM_SDA	169	DO36	17	PS_DDR4_DO36
253	PS_DDR4_SODIMM_SCL	183	DO37	17	PS_DDR4_DO37
166	DDR4_SODIMM_SA2	184	DO38	17	PS_DDR4_DO38
260	DDR4_SODIMM_SA1	195	DO39	17	PS_DDR4_DO39
256	DDR4_SODIMM_SA0	194	DO40	17	PS_DDR4_DO40
92	PS_DDR4_SODIMM_CB0	208	DO41	17	PS_DDR4_DO41
81	PS_DDR4_SODIMM_CB1	207	DO42	17	PS_DDR4_DO42
101	PS_DDR4_SODIMM_CB2	208	DO43	17	PS_DDR4_DO43
105	PS_DDR4_SODIMM_CB3	191	DO44	17	PS_DDR4_DO44
88	PS_DDR4_SODIMM_CB4	203	DO45	17	PS_DDR4_DO45
87	PS_DDR4_SODIMM_CB5	204	DO46	17	PS_DDR4_DO46
100	PS_DDR4_SODIMM_CB6	216	DO47	17	PS_DDR4_DO47
104	PS_DDR4_SODIMM_CB7	215	DO48	17	PS_DDR4_DO48
12	PS_DDR4_DM0	228	DO49	17	PS_DDR4_DO49
35	PS_DDR4_DM1	229	DO50	17	PS_DDR4_DO50
54	PS_DDR4_DM2	211	DO51	17	PS_DDR4_DO51
75	PS_DDR4_DM3	212	DO52	17	PS_DDR4_DO52
178	PS_DDR4_DM4	212	DO53	17	PS_DDR4_DO53
199	PS_DDR4_DM5	224	DO54	17	PS_DDR4_DO54
223	PS_DDR4_DM6	225	DO55	17	PS_DDR4_DO55
241	PS_DDR4_DM7	237	DO56	17	PS_DDR4_DO56
96	PS_DDR4_DM8	236	DO57	17	PS_DDR4_DO57
182	DDR4_SODIMM_CS2_B	249	DO58	17	PS_DDR4_DO58
165	DDR4_SODIMM_CS3_B	250	DO59	17	PS_DDR4_DO59
165	DDR4_SODIMM_CS3_B	232	DO60	17	PS_DDR4_DO60
165	DDR4_SODIMM_CS3_B	233	DO61	17	PS_DDR4_DO61
165	DDR4_SODIMM_CS3_B	245	DO62	17	PS_DDR4_DO62
165	DDR4_SODIMM_CS3_B	246	DO63	17	PS_DDR4_DO63
165	DDR4_SODIMM_CS3_B	13	DQS0_T	17	PS_DDR4_DQS0_T
165	DDR4_SODIMM_CS3_B	34	DQS1_T	17	PS_DDR4_DQS1_T
165	DDR4_SODIMM_CS3_B	55	DQS2_T	17	PS_DDR4_DQS2_T
165	DDR4_SODIMM_CS3_B	76	DQS3_T	17	PS_DDR4_DQS3_T
165	DDR4_SODIMM_CS3_B	179	DQS4_T	17	PS_DDR4_DQS4_T
165	DDR4_SODIMM_CS3_B	200	DQS5_T	17	PS_DDR4_DQS5_T
165	DDR4_SODIMM_CS3_B	221	DQS6_T	17	PS_DDR4_DQS6_T
165	DDR4_SODIMM_CS3_B	242	DQS7_T	17	PS_DDR4_DQS7_T
165	DDR4_SODIMM_CS3_B	242	DQS8_T	17	PS_DDR4_DQS8_T
165	DDR4_SODIMM_CS3_B	11	DQS0_C	17	PS_DDR4_DQS0_C
165	DDR4_SODIMM_CS3_B	32	DQS1_C	17	PS_DDR4_DQS1_C
165	DDR4_SODIMM_CS3_B	74	DQS2_C	17	PS_DDR4_DQS2_C
165	DDR4_SODIMM_CS3_B	177	DQS3_C	17	PS_DDR4_DQS3_C
165	DDR4_SODIMM_CS3_B	198	DQS4_C	17	PS_DDR4_DQS4_C
165	DDR4_SODIMM_CS3_B	219	DQS5_C	17	PS_DDR4_DQS5_C
165	DDR4_SODIMM_CS3_B	240	DQS6_C	17	PS_DDR4_DQS6_C
165	DDR4_SODIMM_CS3_B	240	DQS7_C	17	PS_DDR4_DQS7_C
165	DDR4_SODIMM_CS3_B	240	DQS8_C	17	PS_DDR4_DQS8_C



SODIMM SA[2:0]=001



Trace need 20 mil.

IIC Address = 0b1010001 (0x51)

