

# TXS02612 SD Issue

Date



# Analysis

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## 1, Is SEL not set ?

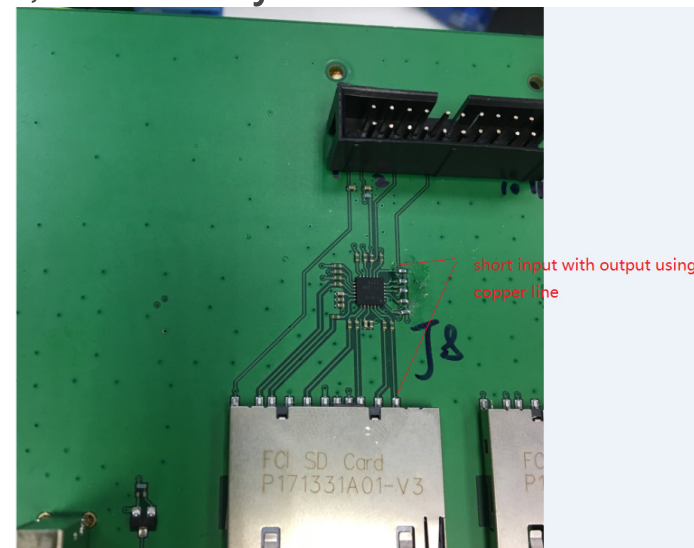
- No ! , Port B0 is functional , signal output from B0

## 2, Is SCH not right ?

- No ! , Port B0 is functional , signal output from B0

## 3, Is layout not Good?

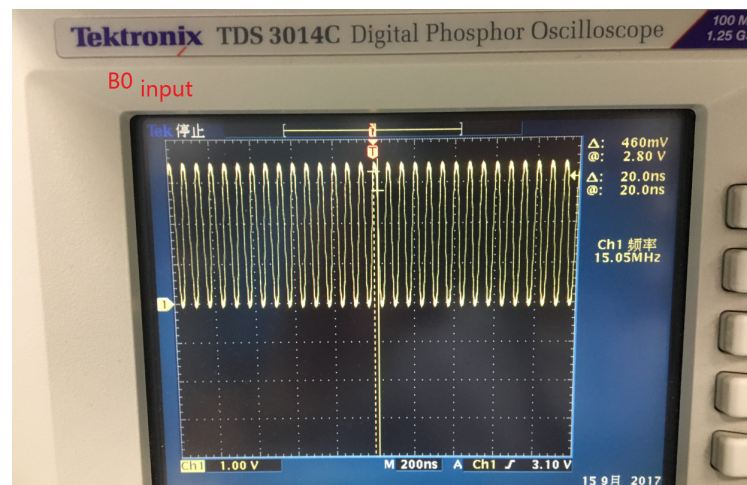
- No ! , Irrespective with layout , Because , if we only short TXS02612 input A to output B  
The signal is good , no distortion



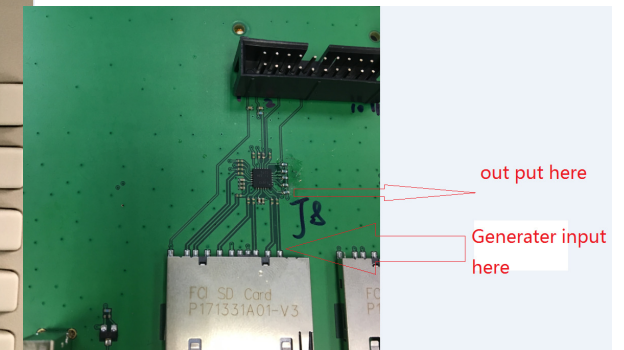
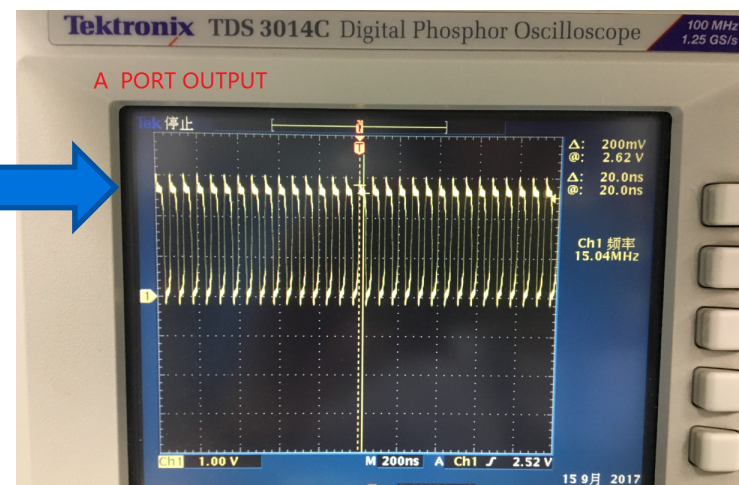
# Analysis

## 4, Is it TXS02612 not good ?

- Action1 : **Yes** , if we using Signal generator to generate a 15MHZ signal from Port B0 to Port A in our PCBA , the signal have a 0.8V decreased . But there is no decreased when Port A to Port B0 . Here is Port B0 to PortA signal Picture



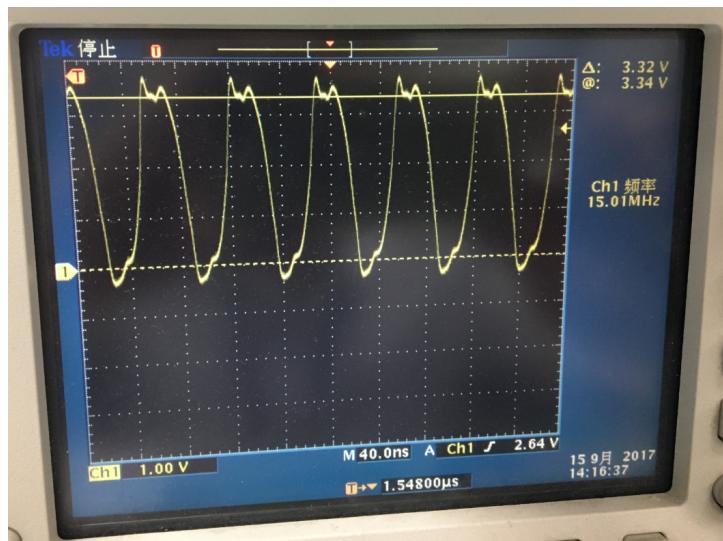
B0 to A



# Analysis

## 4, Is it TXS02612 not good ?

- Action 2: **Yes** , if we only measure TXS02612 like this  
Signal from Port B0 to A is also 0.7V different .



B to A

