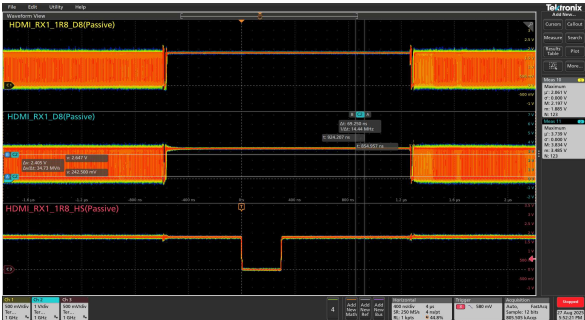
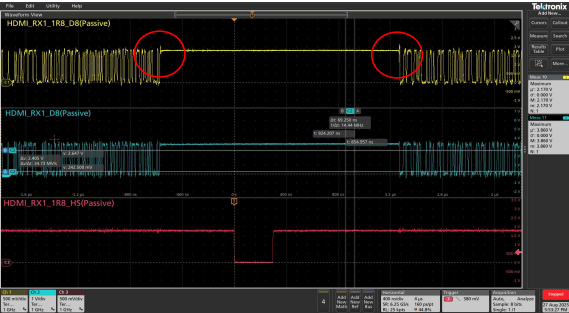


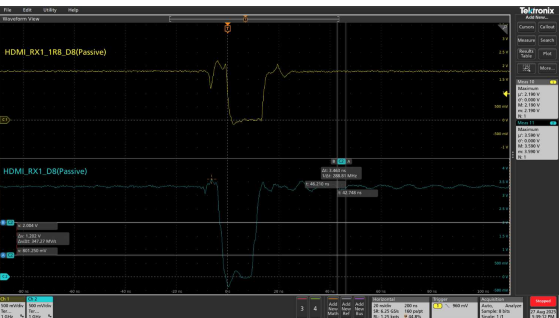
1ch: SN74AVC8T245RHL output (RGB), 2ch: SN74AVC8T245RHL input (RGB), 3ch: SN74AVC8T245RHL output (Hsync)

When layering, overshoot is visible at the beginning and end of drawing.



1ch: SN74AVC8T245RHL output (RGB), 2ch: SN74AVC8T245RHL input (RGB)

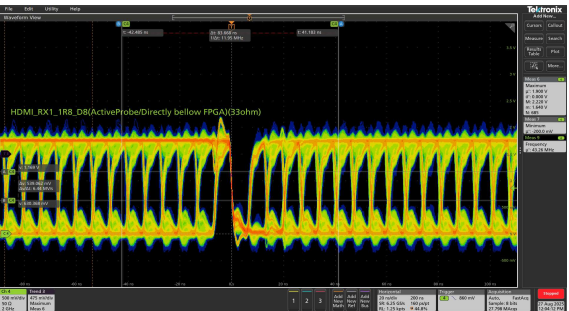
Although there were fluctuations that appeared to be related, they did not cross the threshold of the level conversion IC, so we determined there was no correlation.



5.3 Recommended Operating Conditions					
See (1) (2) (3)		V <sub>CC</sub>	V <sub>CC</sub>	MIN	MAX UNIT
V <sub>CC</sub>	Supply voltage			1.2	3.6 V
V <sub>CC</sub>	Supply voltage			1.2	3.6 V
				V <sub>CC</sub> × 0.85	
	High-level input voltage	Dynamic Input	1.2 V to 1.56 V	2	V
		Static Input	2.7 V to 3.6 V	2	V
	Low-level input voltage	Dynamic Input	1.2 V to 1.56 V	V <sub>CC</sub> × 0.35	V
		Static Input	1.56 V to 3.6 V	0.3	V
			2.7 V to 3.6 V	0.8	V

Repeated overshoots reveal a maximum amplitude of approximately 2.2V.

Various countermeasures were attempted, but none affected this peak.



SN74AVC8T245RHLR Peripheral Circuit

