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uint8_t      Initialise_RF(void)                                //Use CC1125 default values for registers that are not listed below.
{
    RF_SPI_Write(IOCFG3_REG, 0x1A, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Indicates transmitting or not transmitting.
    RF_SPI_Write(IOCFG2_REG, 0x1A, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Indicates transmitting or not transmitting.
    RF_SPI_Write(IOCFG1_REG, 0x30, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(IOCFG0_REG, 0x06, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Asserted when SYNC word has been sent.
    RF_SPI_Write(SYNC_CFG1_REG, 0x0B, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(DEVIATION_M_REG, 0x9D, 0x00, SPI_TRANSACTION_TYPE_WRITE);       //Freq deviation 750Hz
    RF_SPI_Write(MODCFG_DEV_E_REG, 0x08, 0x00, SPI_TRANSACTION_TYPE_WRITE);     //2GFSK. Modalità normale. DEV E = 0.
    RF_SPI_Write(DCFILT_CFG_REG, 0x1C, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(PREAMBLE_CFG1_REG, 0x18, 0x00, SPI_TRANSACTION_TYPE_WRITE);    //Preamble 4 bytes. Modello 0xAA.
    RF_SPI_Write(IQIC_REG, 0xC6, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(CHAN_BW_REG, 0x08, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(MDMCFG0_REG, 0x05, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(SYMBOL_RATE2_REG, 0x4A, 0x00, SPI_TRANSACTION_TYPE_WRITE);     2kbps
    RF_SPI_Write(SYMBOL_RATE1_REG, 0x36, 0x00, SPI_TRANSACTION_TYPE_WRITE);     2kbps
    RF_SPI_Write(SYMBOL_RATE0_REG, 0xE2, 0x00, SPI_TRANSACTION_TYPE_WRITE);     2kbps
    RF_SPI_Write(AGC_REF_REG, 0x20, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(AGC_CS_THR_REG, 0x19, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(AGC_CFG1_REG, 0xA9, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(AGC_CFG0_REG, 0xCF, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(FIFO_CFG_REG, 0x00, 0x00, SPI_TRANSACTION_TYPE_WRITE);
    RF_SPI_Write(FS_CFG_REG, 0x18, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //205MHz to 240MHz. LO divisatore = 16.
}

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RF_SPI_Write(PKT_CFG0_REG, 0x20, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Lunghezza pacchetto variabile. Massimo 255 bytes.
RF_SPI_Write(PA_CFG2_REG, 0x08, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //PA potenza.
RF_SPI_Write(PA_CFG0_REG, 0x7D, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Tx upsampler factor = 32.
RF_SPI_Write(PKT_LEN_REG, 0xFF, 0x00, SPI_TRANSACTION_TYPE_WRITE);           //Max number of bytes that can be transmitted in one frame.
RF_SPI_Write(EXTENDED_REG, IF_MIX_CFG_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FREQOFF_CFG_REG, 0x22, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, RCCAL_FINE_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, RCCAL_COURSE_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, RCCAL_OFFSET_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FREQ2_REG, 0x5B, SPI_TRANSACTION_TYPE_WRITE);       //229MHz
RF_SPI_Write(EXTENDED_REG, FREQ1_REG, 0x99, SPI_TRANSACTION_TYPE_WRITE);       //229MHz
RF_SPI_Write(EXTENDED_REG, FREQ0_REG, 0x99, SPI_TRANSACTION_TYPE_WRITE);       //229MHz
RF_SPI_Write(EXTENDED_REG, FS_DIG1_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_DIG0_REG, 0x5F, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_CAL1_REG, 0x40, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_CAL0_REG, 0x0E, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_CHP_REG, 0x28, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_DIVTWO_REG, 0x03, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_DSM1_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_DSM0_REG, 0x33, SPI_TRANSACTION_TYPE_WRITE);
RF_SPI_Write(EXTENDED_REG, FS_DVCO_REG, 0x17, SPI_TRANSACTION_TYPE_WRITE);     //High performance mode.
RF_SPI_Write(EXTENDED_REG, FS_PFD_REG, 0x50, SPI_TRANSACTION_TYPE_WRITE);

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RF_SPI_Write(EXTENDED_REG, FS_PRE_REG, 0x6E, SPI_TRANSACTION_TYPE_WRITE);

RF_SPI_Write(EXTENDED_REG, FS_REG_DIV_CML_REG, 0x14, SPI_TRANSACTION_TYPE_WRITE);

RF_SPI_Write(EXTENDED_REG, FS_SPARE_REG, 0xAC, SPI_TRANSACTION_TYPE_WRITE);

RF_SPI_Write(EXTENDED_REG, FS_VCO0_REG, 0x00, SPI_TRANSACTION_TYPE_WRITE);

RF_SPI_Write(EXTENDED_REG, XOSC5_REG, 0x0E, SPI_TRANSACTION_TYPE_WRITE);

RF_SPI_Write(EXTENDED_REG, XOSC1_REG, 0x03, SPI_TRANSACTION_TYPE_WRITE);           //High performance mode.

RF_SPI_Read(EXTENDED_REG, 0x90, SPI_TRANSACTION_TYPE_READ);

sprintf(Debug_message_array, "\n\rVersione sintetizzatore RF 0x%x\n\r", SPI_RX_data[2]);
Debug_UartPutString(Debug_message_array);

RF_SPI_Read(EXTENDED_REG, 0x90, SPI_TRANSACTION_TYPE_READ);           //CC1125_PARTNUMBER

if(SPI_RX_data[2] == 0x58)                                           //CC1125_PARTNUMBER
{
    return TRUE;
}

return FALSE;
}

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