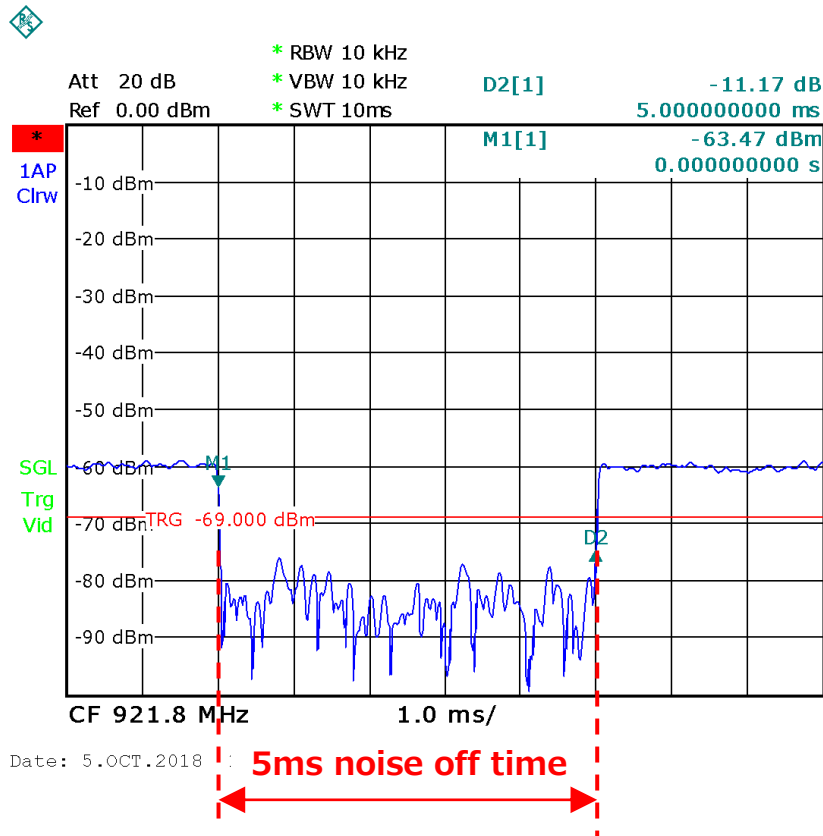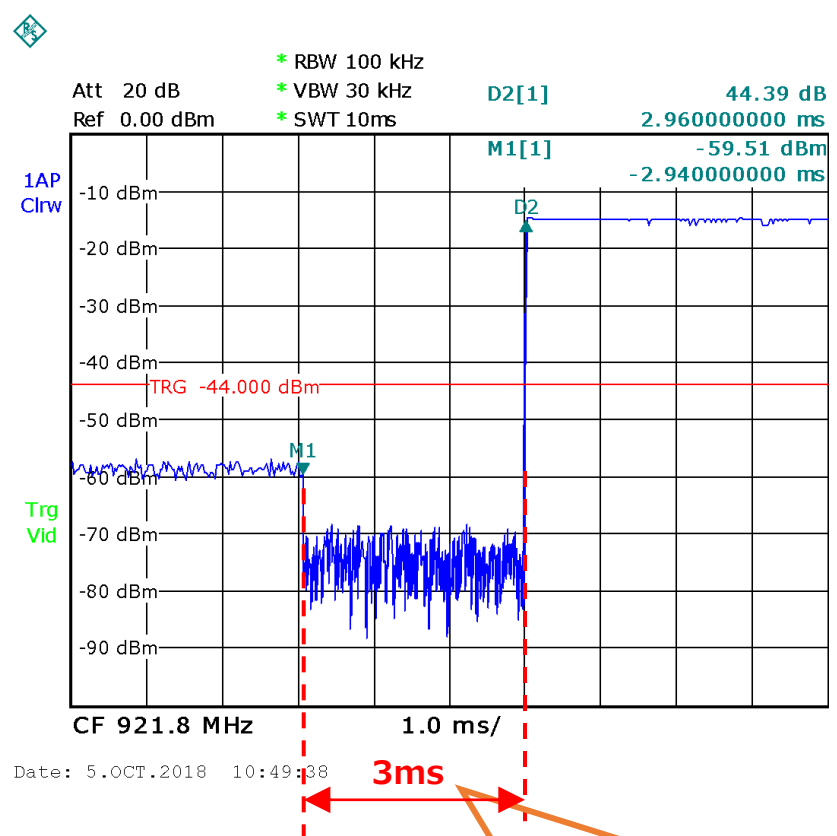Interfering noises applied with 5ms off-time
Noise level: -60dBm

As soon as interfering noises are off, CC1312R output starts.
In below case, only 3ms CS time is met.

Att 20 dB	* RBW 10 kHz
Ref 0.00 dBm	* VBW 10 kHz
	* SWT 10ms

D2[1]　　　　　-11.17 dB
5.000000000 ms
M1[1]　　　　　-63.47 dBm
0.000000000 s

1AP Clrw
SGL
Trg
Vid

TRG -69.000 dBm
M1
D2

CF 921.8 MHz　　　　1.0 ms/
Date: 5.OCT.2018

5ms noise off time

Att 20 dB	* RBW 100 kHz
Ref 0.00 dBm	* VBW 30 kHz
	* SWT 10ms

D2[1]　　　　　44.39 dB
2.960000000 ms
M1[1]　　　　　-59.51 dBm
-2.940000000 ms

1AP Clrw
Trg
Vid

TRG -44.000 dBm
M1
D2

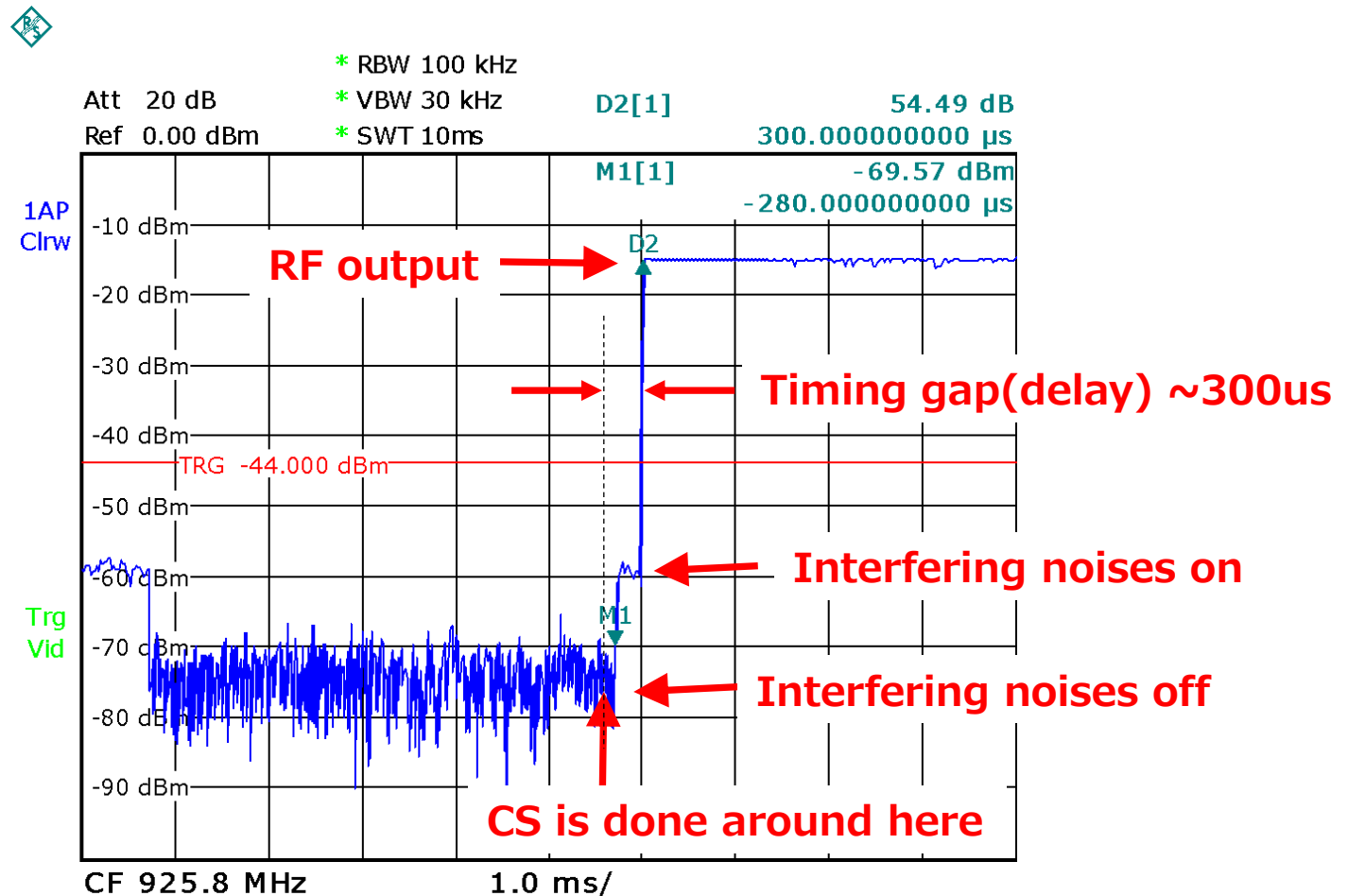CF 921.8 MHz　　　　1.0 ms/
Date: 5.OCT.2018　10:49:38

3ms

To meet Japan regulation, below conditions need to be met.
Carrier sense time: 5ms, 24ch:920.6MHz to 31ch:922.0MHz
Carrier sense time: 128us, 33ch:922.4MHz to 61ch:928.0MHz

To meet Japan regulation, 5ms CS time is required.

Is it possible to implement 5ms (or 128us) CS time?

In below case, even interfering noises exist, CC1312R output RF

It seems there is timing gap between CS and RF output.
~300us time difference exist based on customer's evaluation.