

SmartRF™ Flash Programmer 2 ver. 1.8.2

Connected devices:

- [-] Silicon Labs CP210x USB t...
 - CC2652P
- [-] F5521gw Mobile Broadba...
 - Unknown
- [-] F5521gw Mobile Broadba...
 - Unknown
- [-] F5521gw Mobile Broadba...
 - Unknown
- [-] F5521gw Mobile Broadba...
 - Unknown

Refresh

 Auto-detect

Selected target(1):

CC2652P1Fx
Wireless MCU
2.4 GHz Radio +
20 dBm PA



CC2652P

State: Connected

Flash size: 352 KB

Ram size: 80 KB

Chip revision: unknown

Package: unknown

Main

Edit

Info Page

MAC Address

Read

Write

Copy to Clipboard

Save to File...

16 columns

The highlighted sections mark the FCFG1 registers.
Hover over one of the highlighted sections to see the register name.

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0x270	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
0x280	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
0x290	FE	FF	FF	FF	00	F0	02	32	00	C5	C5	FF	00	00	00	FF
0x2a0	00	C5	F2	FF	FF	FF	FF	FF	03	E7	03	00	F3	2F	2F	2F
0x2b0	03	2C	11	00	7F	78	59	CB	FB	E0	F8	FA	FC	F0	F1	E2
0x2c0	F9	F8	FF	E8	DF	A2	AA	94	4F	FF	FF	FF	FF	FF	FF	FF
0x2d0	FF	FF	FF	FF	FF	FF	FF	FF	FD	FF	FF	FF	FF	FF	FF	FF
0x2e0	FF	FF	FF	FF	FF	FF	FF	FF	E6	05	0A	85	D2	74	FF	FF
0x2f0	E3	B4	C0	24	00	4B	12	00	BC	17	00	00	FF	17	00	00
0x300	FF	FF	FF	FF	07	FE	27	FF	9F	9F	98	98	3B	4A	FF	FF
0x310	03	00	00	00	01	00	01	00	2F	10	B4	3B	28	00	00	00

PA Bit = 0

Status

- >Failed to erase info page
- >Reading Info page finished.
- >Reading Info page finished.

Success!

11.4.1.38 USER_ID Register (Offset = 294h) [Reset = 3000000h]

USER_ID is shown in Figure 11-60 and described in Table 11-64.

Return to the Table 11-25.

User Identification.

Reading this register and the FCFG1:ICEPICK_DEVICE_ID register is the only supported way of identifying a device.

The value of this register will be written to AON_PMCTL:JTAGUSERCODE by boot FW while in safezone.

Figure 11-60. USER_ID Register

31	30	29	28	27	26	25	24
PG_REV				VER		PA	RESERVED
R-3h				R-X		R-X	R-0h
23	22	21	20	19	18	17	16
CC13	SEQUENCE				PKG		
R-X	R-X				R-X		
15	14	13	12	11	10	9	8
PROTOCOL				RESERVED			
R-X				R-0h			
7	6	5	4	3	2	1	0
RESERVED							
R-0h							

Table 11-64. USER_ID Register Field Descriptions

Bit	Field	Type	Reset	Description
31-28	PG_REV	R	3h	Field used to distinguish revisions of the device
27-26	VER	R	X	Version number. 0x0: Bits [25:12] of this register has the stated meaning. Any other setting indicate a different encoding of these bits. Default value differs depending on partnumber.
25	PA	R	X	0: Does not support 20dBm PA 1: Supports 20dBm PA Default value differs depending on partnumber.
24	RESERVED	R	0h	Reserved
23	CC13	R	X	0: CC26xx device type 1: CC13xx device type Default value differs depending on partnumber.
22-19	SEQUENCE	R	X	Sequence. Used to differentiate between marketing/orderable product where other fields of this register are the same (temp range, flash size, voltage range etc) Default value differs depending on partnumber.
18-16	PKG	R	X	Package type. 0x0: 4x4mm QFN (RHB) package 0x1: 5x5mm QFN (RSM) package 0x2: 7x7mm QFN (RGZ) package 0x3: Wafer sale package (naked die) 0x4: WCSP (YFV) 0x5: 7x7mm QFN package with Wettable Flanks Other values are reserved for future use. Packages available for a specific device are shown in the device datasheet. Default value differs depending on partnumber.

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0x290	FE	FF	FF	FF	02	F0	02	32	00	C5	C5	FF	00	00	00	FF
0x2a0	00	C5	F2	FF	FF	FF	FF	FF	03	E7	03	00	F3	2F	2F	2F
0x2b0	03	2C	11	00	7F	78	59	CB	FB	E0	F8	FA	FC	F0	F1	F2
0x2c0	F9	F8	FF	E8	DF	A2	AA	94	4F	FF	FF	FF	FF	FF	FF	FF
0x2d0	FF	FF	FF	FF	FF	FF	FF	FF	FD	FF	FF	FF	FF	FF	FF	FF
0x2e0	FF	FF	FF	FF	FF	FF	FF	FF	E6	05	0A	85	D2	74	FF	FF
0x2f0	E3	B4	C0	24	00	4B	12	00	BC	17	00	00	FF	17	00	00
0x300	FF	FF	FF	FF	07	FE	27	FF	9F	9F	98	98	3B	4A	FF	FF
0x310	03	00	00	00	01	00	01	00	2F	10	B4	3B	28	00	00	00

set PA Bit = 1

▲ Status

- > CC26xx: Check on info page lock state is not implemented.
- > Erasing info page not supported via serial bootloader.
- > Failed to erase info page

Failure !!!

Failed!