

Rev.	Rev. notes	Designer	Approver	Date
A0	First release	Sethuram S		18-Sep-2025

Cover Page

VVDN_IGW1

WirelessHART_Module

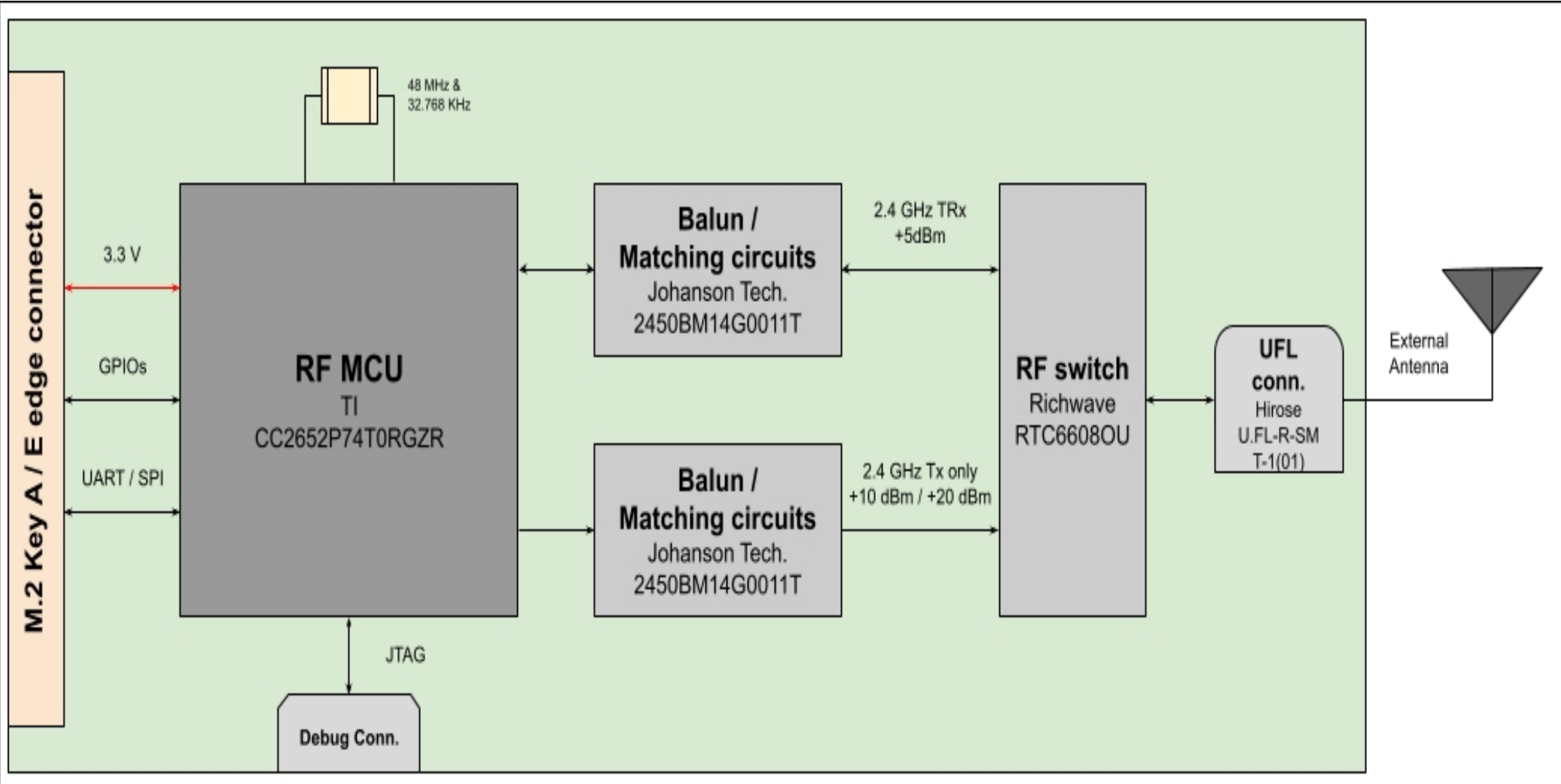
	Content
Page no.	Sheet Name
01	Cover_Page
02	Block_Diagram
03	RF_MCU
04	RF_Section
05	Connectors_M-2_Debug_UFL
06	Miscellaneous

VVDN_IGW1

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14/10/2025:10:52

	A3	Title :	COVER_PAGE
	Fab No :	501-1-04706	Rev: A0
	Asy No :	701-3-00204	Sheet 1 of 6

Block Diagram

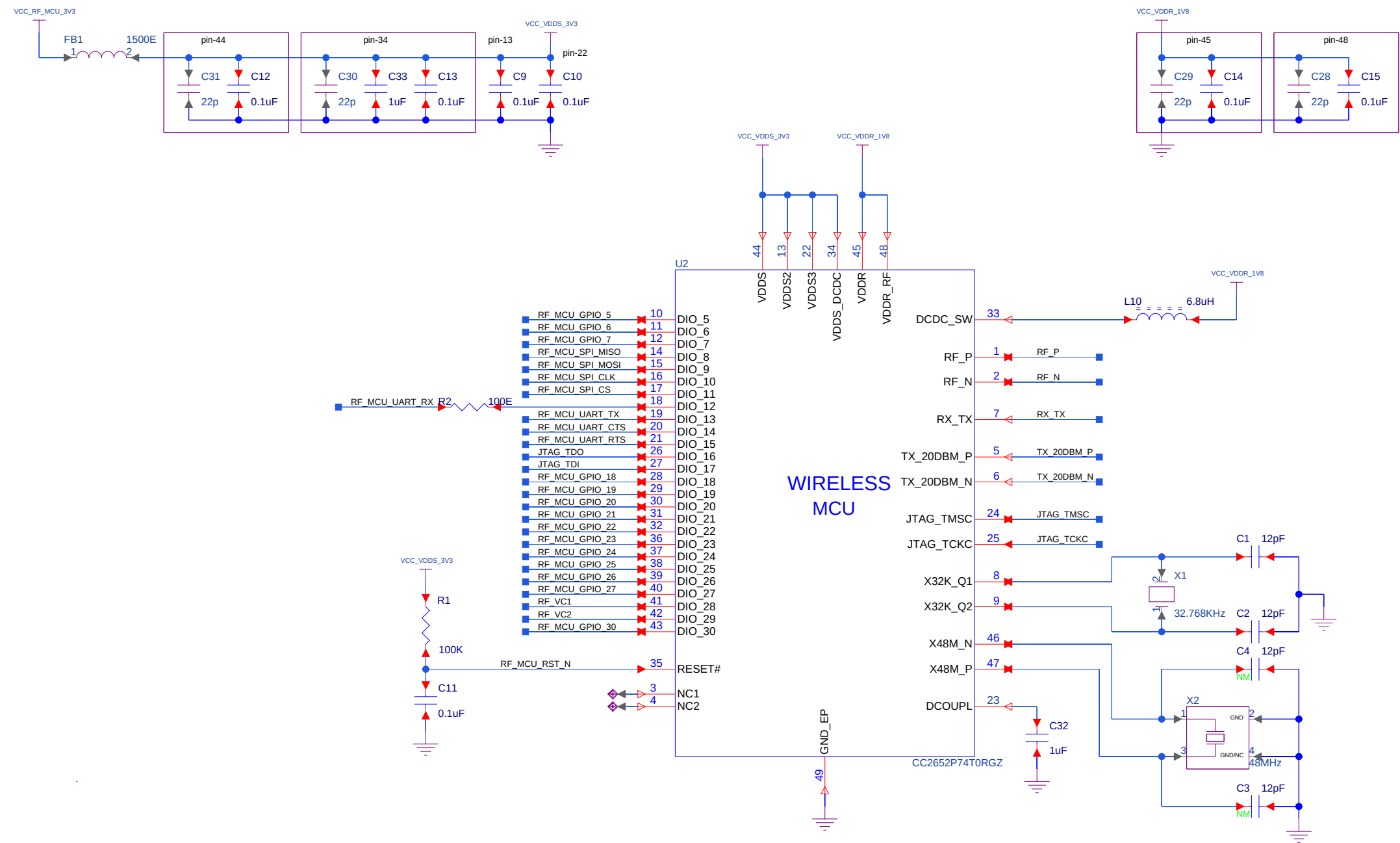


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	A3	Title : BLOCK_DIAGRAM	
	Fab No :	501-1-04706	Rev: A0
	Asy No :	701-3-00204	Sheet 2 of 6

RF_MCU_Sections



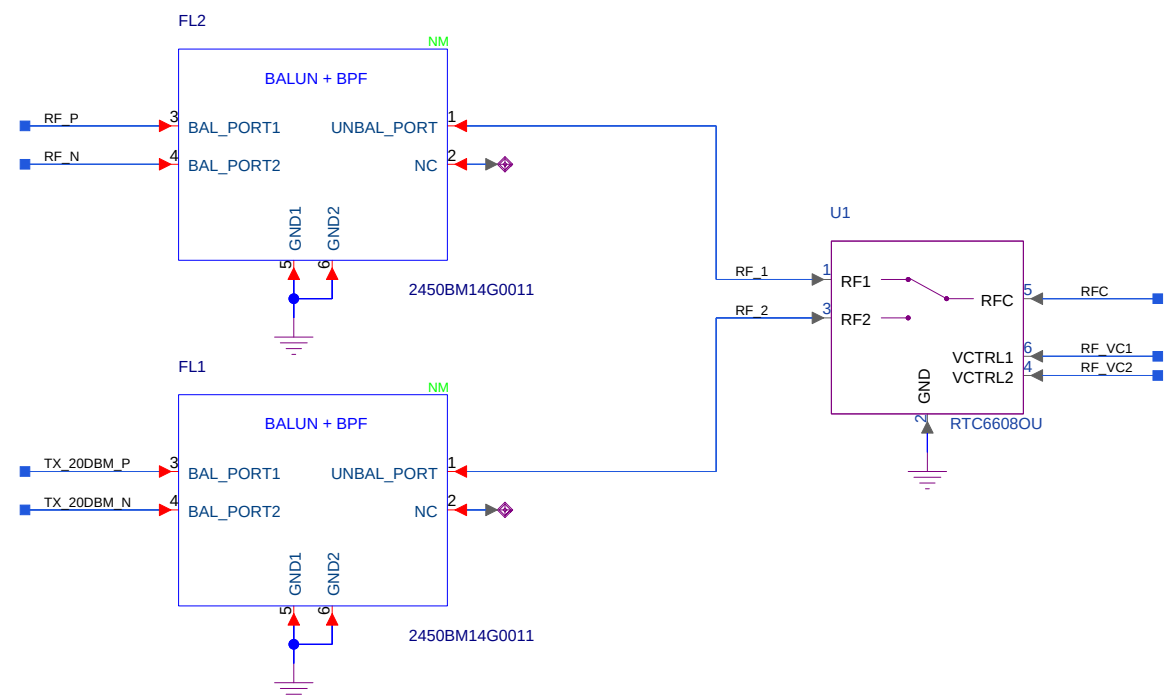
Author: Sethuram S
15/10/2025:13:11

VVDN_IGW1

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	Fab No :	501-1-04706	Rev: A0
	Asy No :	701-3-00204	Sheet 3 of 6

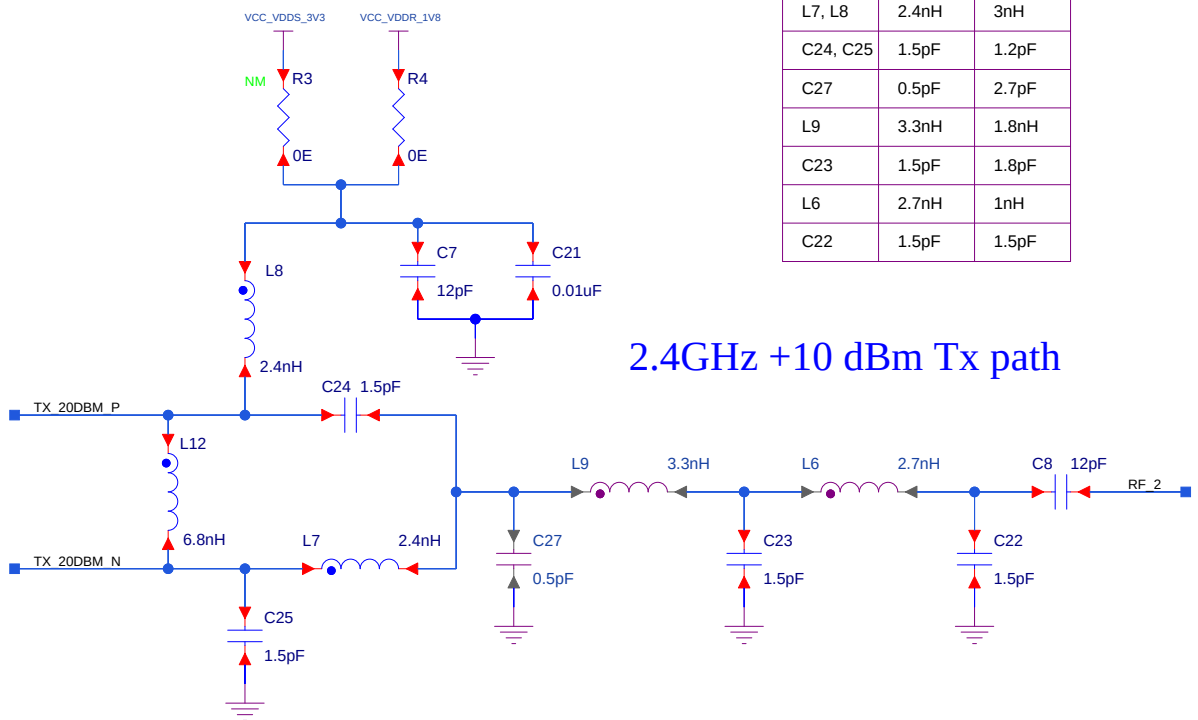
RF_Sections

IPC_Sections

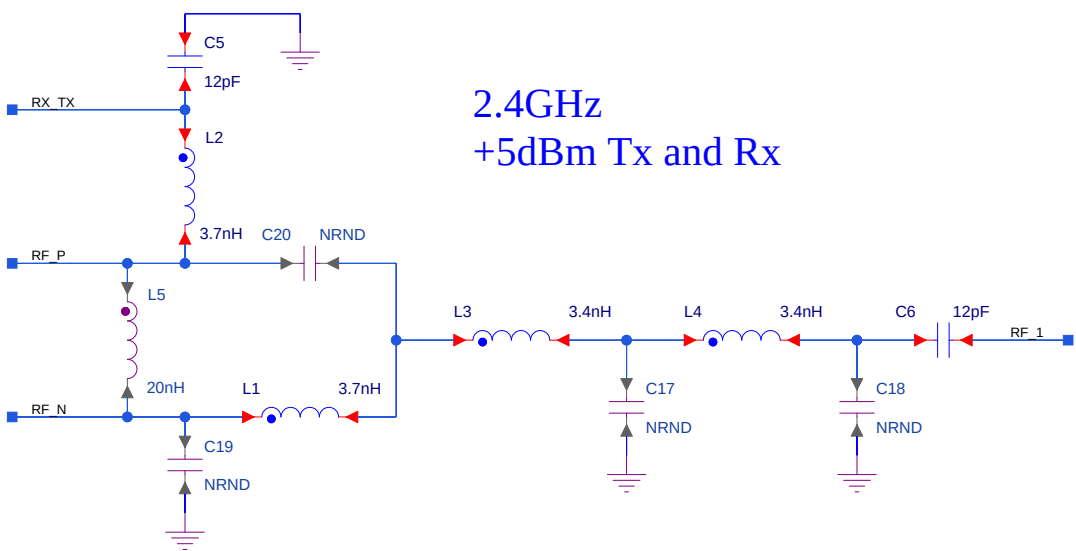


2.4GHz
+10 or +20 dBm Tx path

PA Path Optimizations		
Refdes	+10dBm	+20dBm
R3	NM	0E
R4	0E	NM
C21	10nF	10nF
L12	6.8nH	15nH
L7, L8	2.4nH	3nH
C24, C25	1.5pF	1.2pF
C27	0.5pF	2.7pF
L9	3.3nH	1.8nH
C23	1.5pF	1.8pF
L6	2.7nH	1nH
C22	1.5pF	1.5pF



2.4GHz +10 dBm Tx path



2.4GHz
+5dBm Tx and Rx

VVDN_IGW1

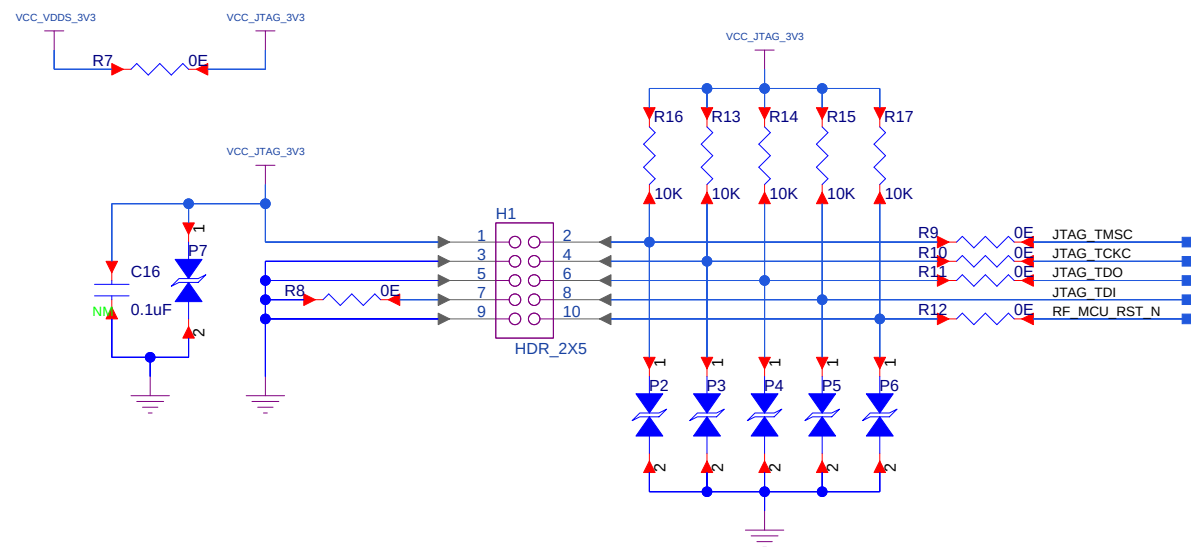
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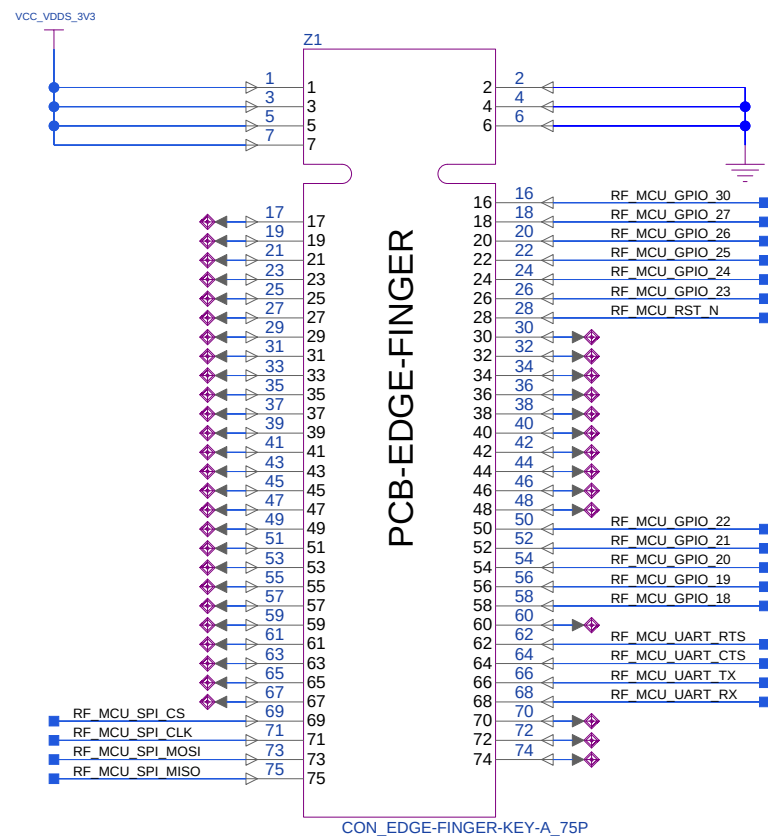
A3	Title :	RF_SECTIONS
Fab No :	501-1-04706	Rev: A0
Asy No :	701-3-00204	Sheet 4 of 6

Connectors_M-2_DEBUG_UFL

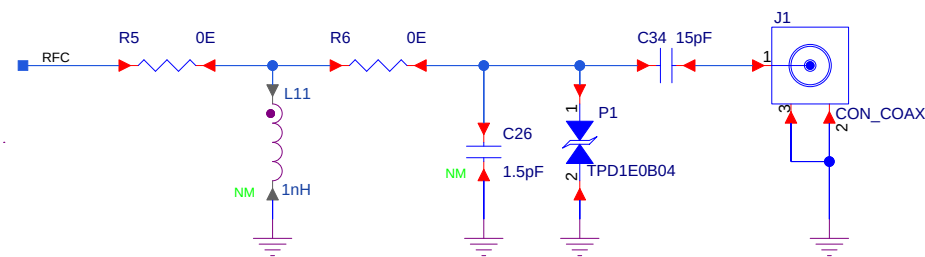
JTAG_Debug_Connector



M.2_Key-A_Edge_Finger



U.FL_Connector



VVDN_IGW1

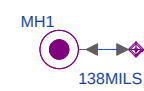
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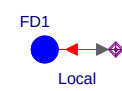
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Fab No :	501-1-04706	Rev: A0
Asy No :	701-3-00204	Sheet 5 of 6

Miscellaneous

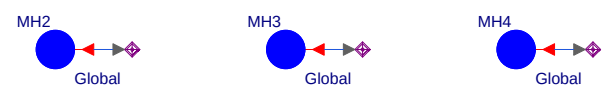
Mounting_Hole



Local_fiducial



Global_fiducial



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	A3	Title : MISCELLANEOUS
	Fab No : 501-1-04706	Rev: A0
	Asy No : 701-3-00204	Sheet 6 of 6