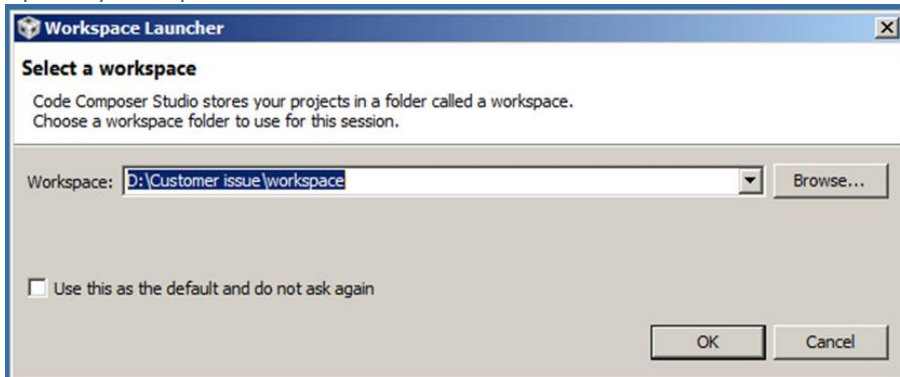
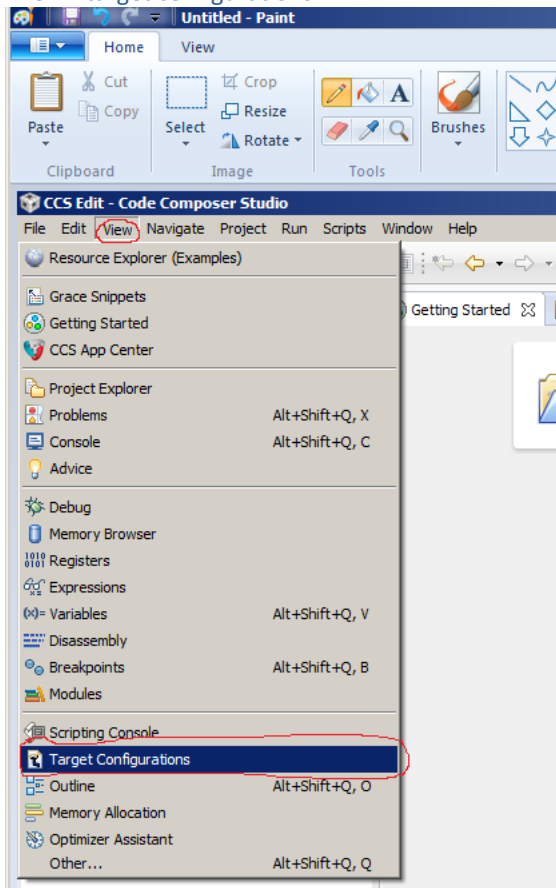


Read MSP430 PC Counter and Register

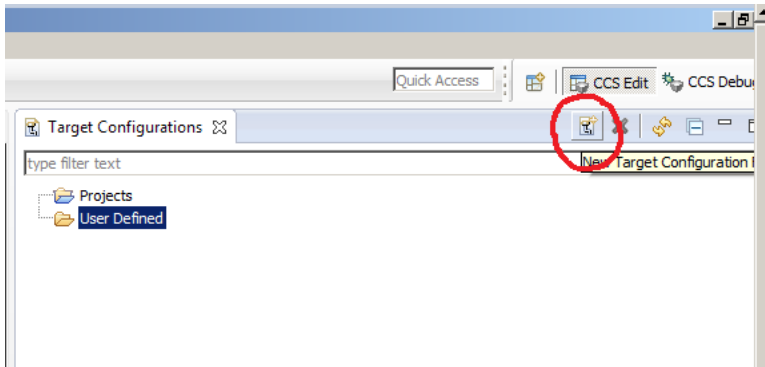
1. Open any workspace in CCS



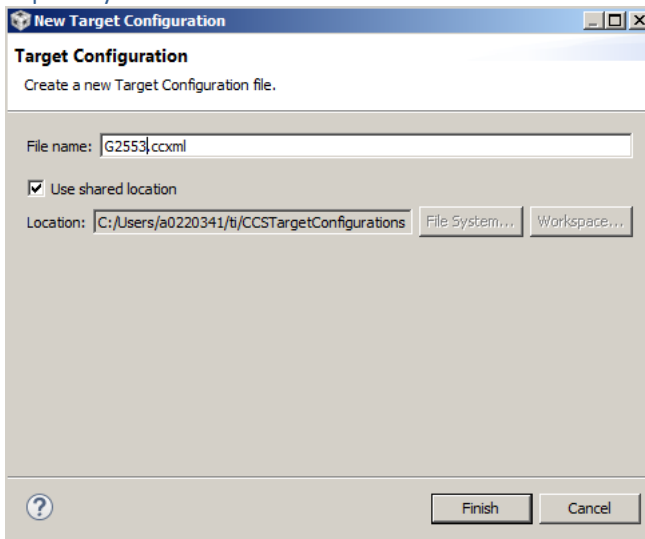
2. View->target configurations.



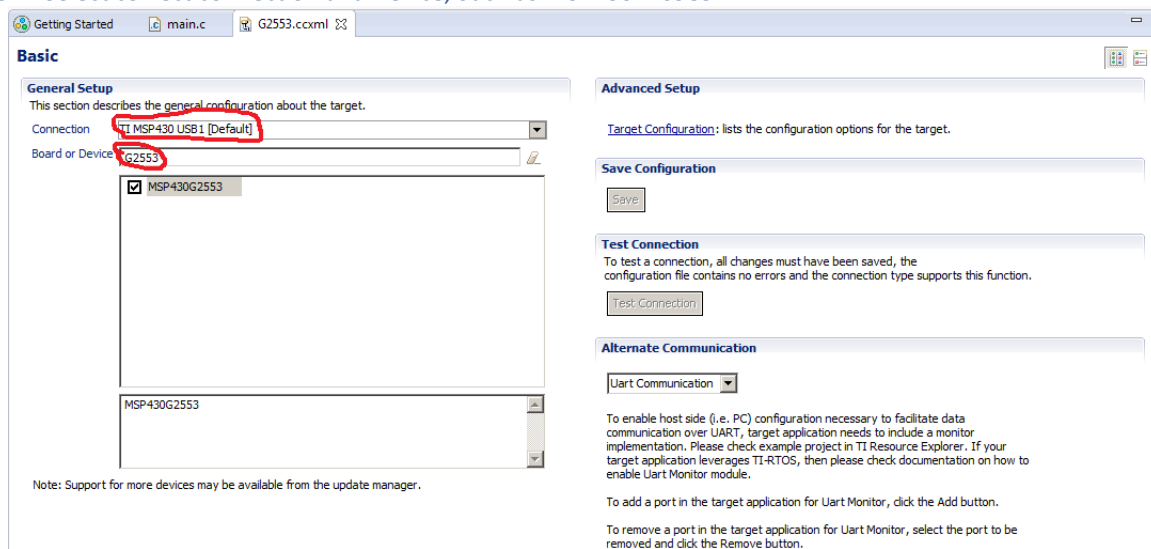
3. New target configuration file



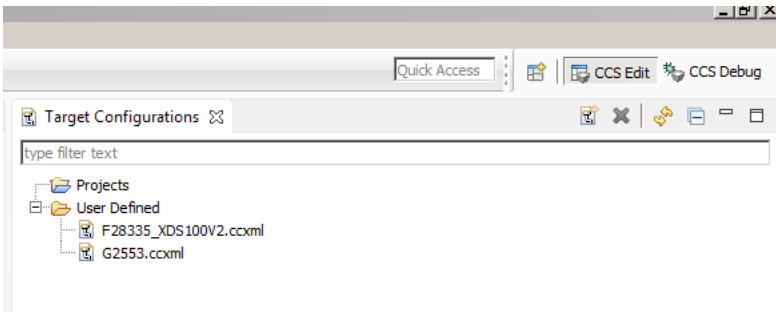
4. Input any name for File name



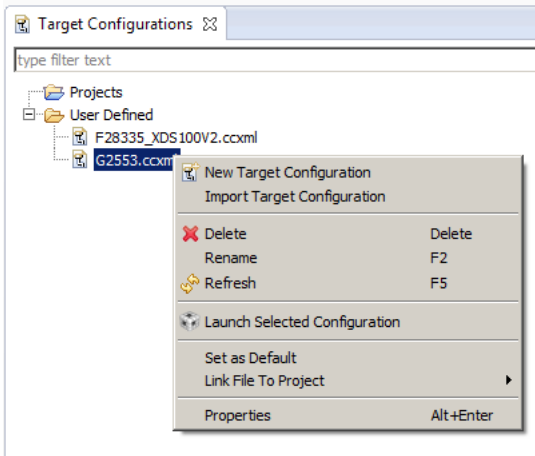
5. Select correct connection and Device, Such as MSP430FR6989



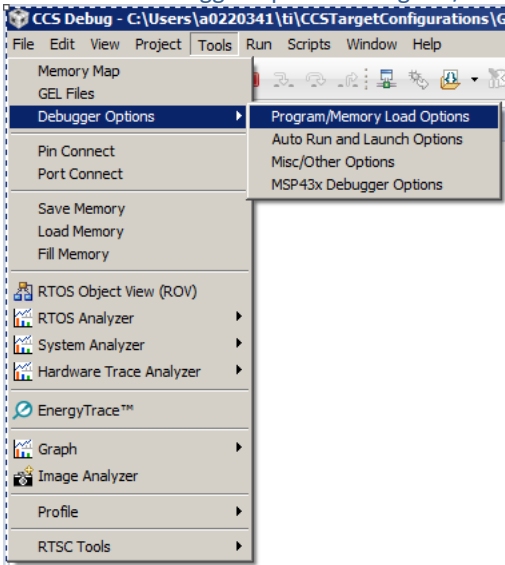
6. After "Save", there is a new ccxml file under User Defined, then connect the MSP-FET with computer, important!!! Connect FET only! not target board.



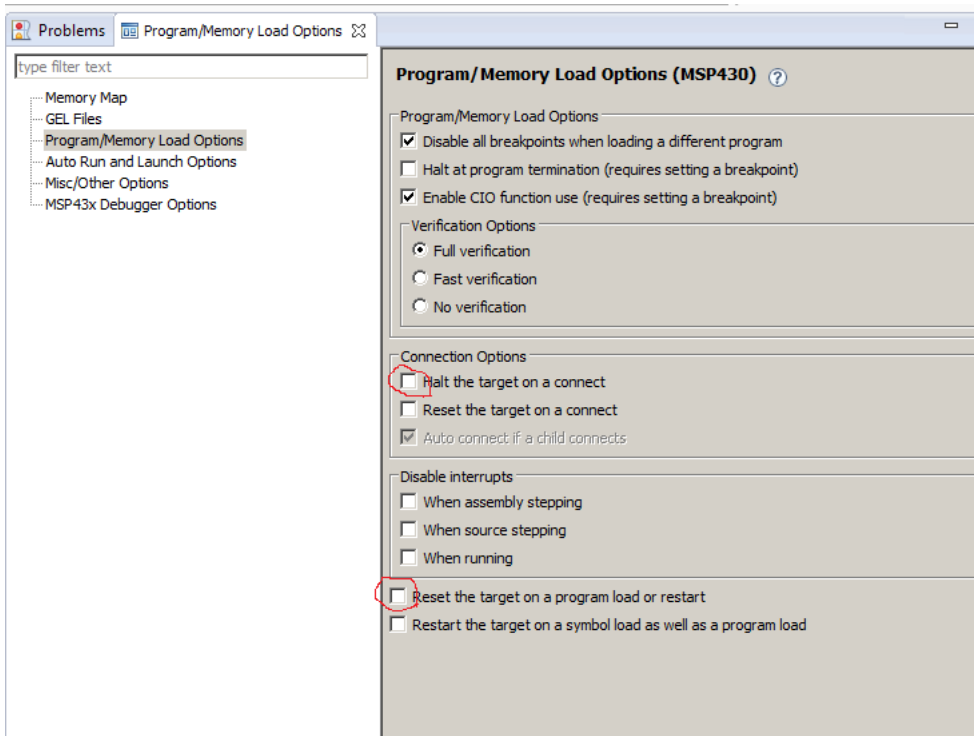
7. Right click and Launch Selected Configuration .



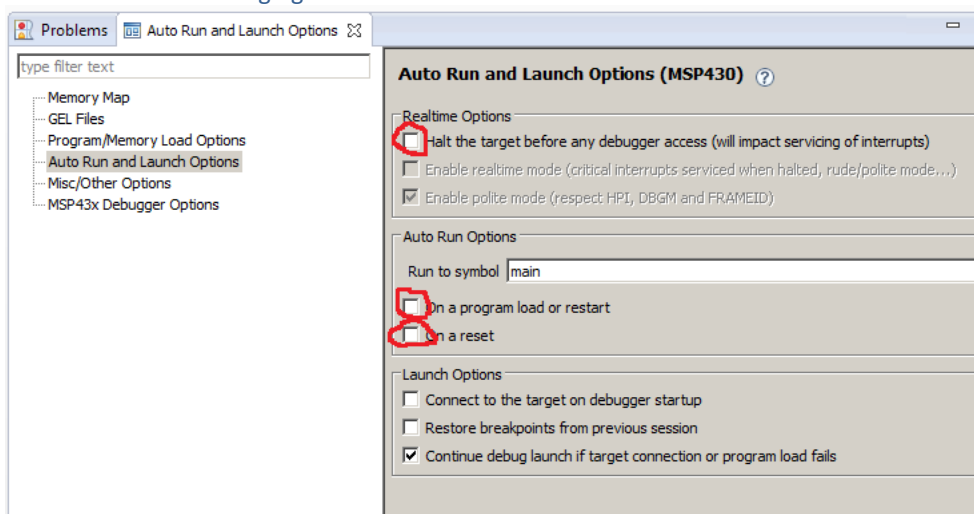
8. Then Tools->Debugger Options->Program/Memory Load Options



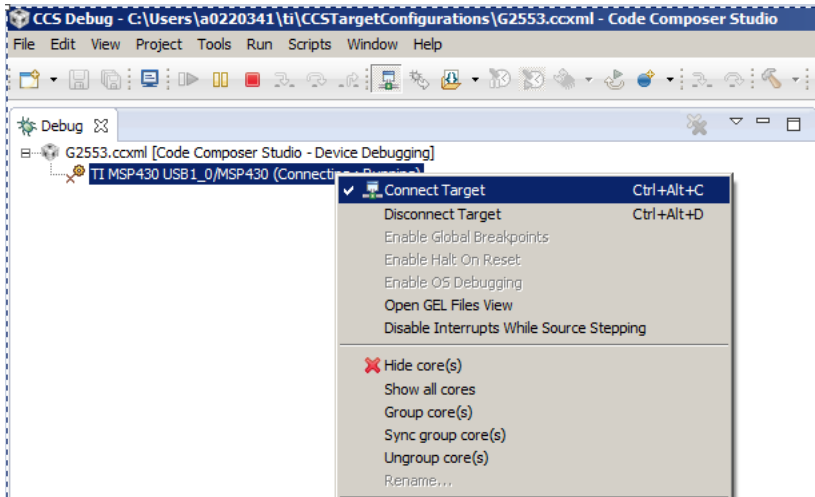
9. Unselect the check box as below red circles, click “Remember My Settings” then select “Auto Run and Launch Options” in left window.



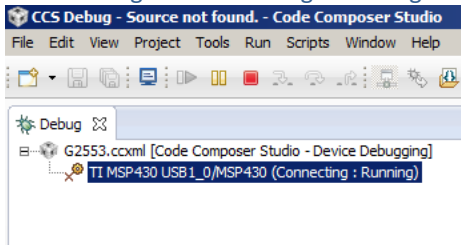
- Unselect the check box as below red circles then “Remember My Settings”, Now connect the FET with target board, be careful that we possible reset the target at this step if the connection is not stable during operation. You can try with normal target board to make sure your operation will not reset the chip before connect the hanging board.



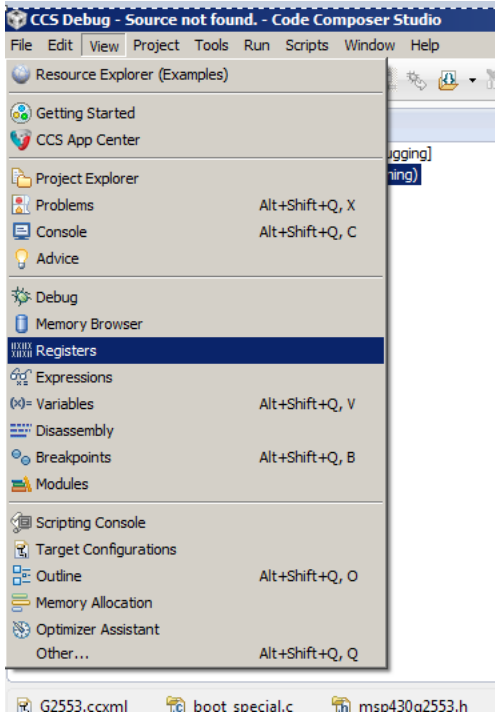
- Back to Debug window, right click on TI MSP430 USB1_0/MSP430 and Connect Target



12. Status change to Connecting : Running



13. View-> Registers.



14. Register window

Variables Expressions Registers		
Name	Value	Description
Core Registers		Core Registers
Special_Function		
ADC10		
ADC10DTC0	Error: unable to read	ADC10 Data Transfer Control 0 [...]
ADC10DTC1	Error: unable to read	ADC10 Data Transfer Control 1 [...]
ADC10AE0	Error: unable to read	ADC10 Analog Enable 0 [Memory ...]
ADC10CTL0	Error: unable to read	ADC10 Control 0 [Memory Mapped]
ADC10CTL1	Error: unable to read	ADC10 Control 1 [Memory Mapped]
ADC10MEM	Error: unable to read	ADC10 Memory [Memory Mapped]
ADC10SA	Error: unable to read	ADC10 Data Transfer Start Addre...
System_Clock		
Comparator_A		
Flash		
Port_1_2		
Port_3_4		
Timer0_A3		

15. Suspend before read registers.

