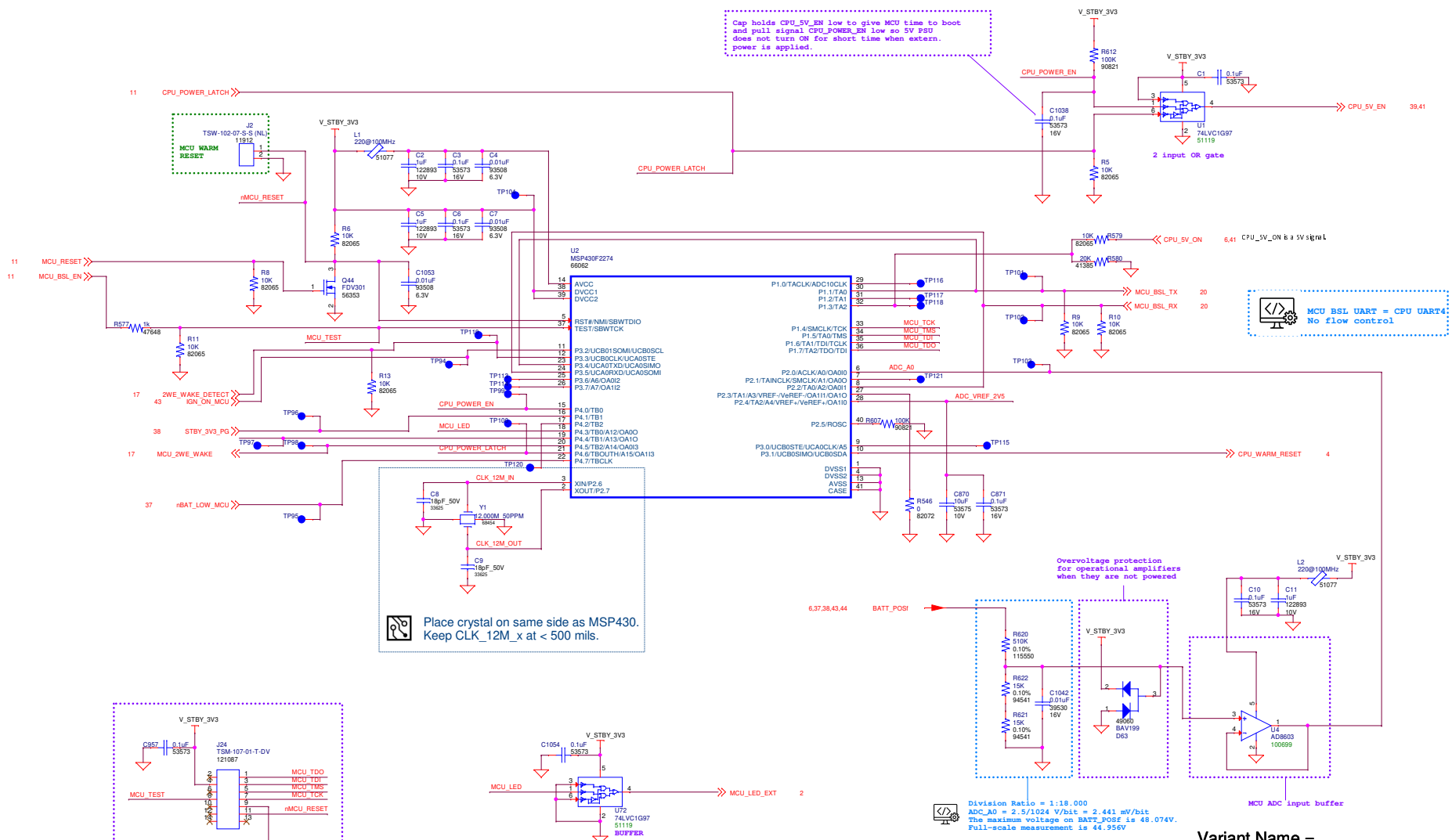


POWER CONTROL MICROCONTROLLER

Cap holds CPU_5V_EN low to give MCU time to boot and pull signal CPU_POWER_EN low so 5V PSU does not turn ON for short time when extern. power is applied.

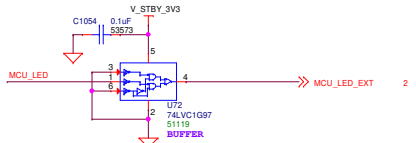
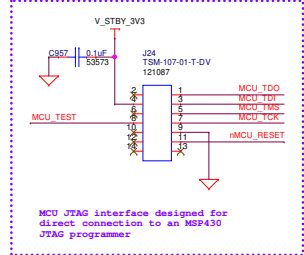


MCU BSL UART = CPU UART
No flow control

Place crystal on same side as MSP430.
Keep CLK_12M_x at < 500 mils.

Oversoltage protection for operational amplifiers when they are not powered

Division Ratio = 1:18.000
ADC_AD = 2.5/1024 V/bit = 2.441 mV/bit
The maximum voltage on BATT_POS is 48.074V.
Full-scale measurement is 44.956V



Variant Name =

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	956 Main Board Project Name	
Designed by Trevor Michel-Smith	Drawing purpose <input type="checkbox"/> Do not use for fabrication <input checked="" type="checkbox"/> Final Plan	
Drawn by Trevor Michel-Smith	Size C	Document Number 135901-XX-SD-A
Checked by "Checked By"	Date Monday, September 07, 2020	Sheet 3 of 44