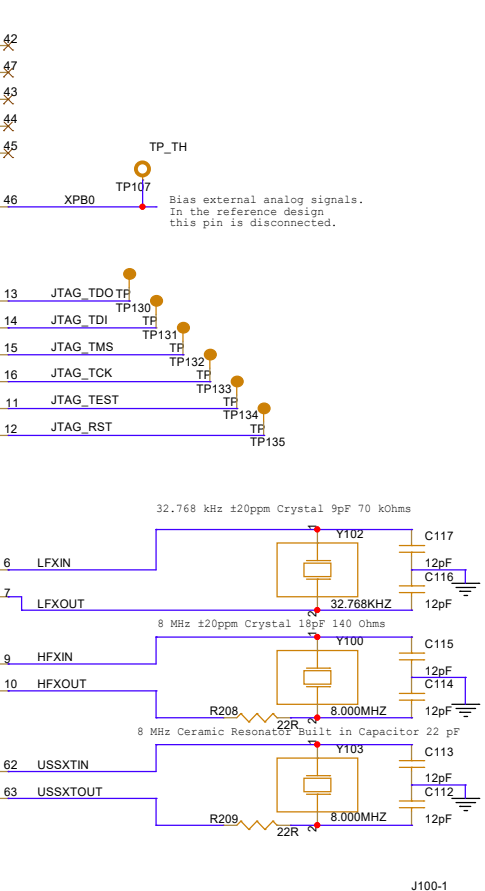
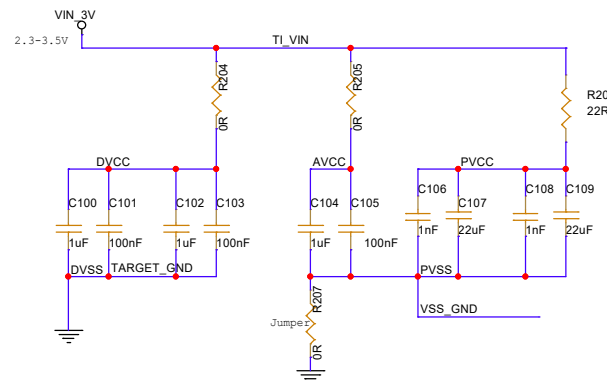


Pin	Signal	Pin	Signal
3	TxPwr	42	P6.0/TA0CLK/COUT/S0
4	Tx5vEn	47	P6.2/TB0CLK/R13/LCDREF/S32
19	MSP430_TXD	43	P6.4/MCLK/COM0
20	MSP430_RXD	44	P6.5/SMCLK/COM1/S34
25	MSP430_IRQ	45	P6.6/ACLK/COM2/S31
26	LED2	46	P7.0/TA1.0/TA1.2/XPB0/S30
23	MSP430_SDA	13	JTAG_TDO
24	MSP430_SCL	14	JTAG_TDI
21		15	JTAG_TMS
22		16	JTAG_TCK
27	MTIF_OUT_IN	11	JTAG_TEST
50		12	JTAG_RST
51		6	LFxin
52		7	LFxout
28	MTIF_PIN_EN	9	HFXin
29		10	HFXout
30		62	USSXTIN
31		63	USSXTOUT
32		60	CH0_IN
34		59	CH0_OUT
35		53	CH1_IN
36		54	CH1_OUT
37			
38			
39			
40			
41			

MSP430FR50431IPM



TI recommends powering AVCC, DVCC, PVCC pins from the same source



TI recommends separated grounds with a single-point connection for better noise isolation from digital to analog circuits on the board and to achieve high analog accuracy.

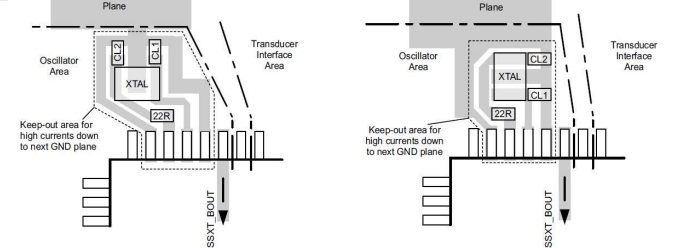
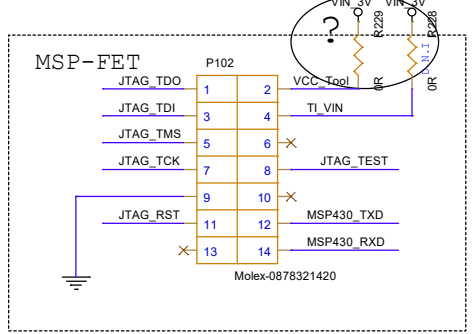
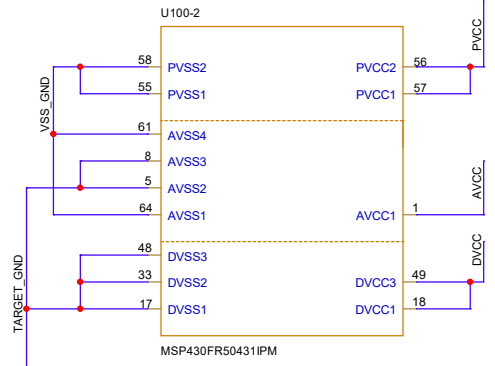


Figure 10-5. USSTX PCB Layout Recommendation



Keep the trace of USSXTIN and USSXTOUT as short as possible. In case one must be longer than the other, keep USSXTIN shorter because USSXTIN Make ground shield open ended without making a loop



Author	NAME	DATE	
<Author>	<Author>	<Date1>	
DRAWN	<DRAWN>	<Date2>	
CHECKED	<CHECKED>	<Date3>	
Variant	<Variant Name>		
Size	<Doc>	Document Number	Rev
A3	<Doc>	Document Number	<RevCode>
Date	Wednesday, January 10, 2024	Sheet 4	of 10

